Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(2.30 GHz, Intel Xeon Platinum 8380)

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Copies

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Specrate®2017_int_base</th>
<th>Specrate®2017_int_peak</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>550</td>
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<tr>
<td>502.gcc_r</td>
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<td>520.omnetpp_r</td>
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<tr>
<td>523.xalancbmk_r</td>
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<tr>
<td>525.x264_r</td>
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<tr>
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<tr>
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<tr>
<td>548.exchange2_r</td>
<td>550</td>
<td>569</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>550</td>
<td>569</td>
</tr>
</tbody>
</table>

**Hardware**

CPU Name: Intel Xeon Platinum 8380
Max MHz: 3400
Nominal: 2300
Enabled: 80 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 60 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 512 GB NVMe SSD
Other: None

**Software**

OS: Red Hat Enterprise Linux release 8.5 (Ootpa)
Kernel 4.18.0-348.el8.x86_64
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
          Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version SE5C620.86B.01.01.0004.2110190142 released Oct-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Integer Rate Result

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SPECrate®2017_int_base = 550
SPECrate®2017_int_peak = 569

Results Table

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<tr>
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<td>576</td>
<td>300</td>
<td>160</td>
<td>572</td>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Enable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on icelakespec Tue Sep 06 04:39:17 2022
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8380 CPU @ 2.30GHz
        2 "physical id"s (chips)
        160 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
        cpu cores : 40
        siblings : 80
        physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
        25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
        physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
        25 26 27 28 29 30 31 32 33 34 35 36 37 38 39
From lscpu from util-linux 2.32.1:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit
    Byte Order: Little Endian
    CPU(s): 160
    On-line CPU(s) list: 0-159
    Thread(s) per core: 2
    Core(s) per socket: 40
    Socket(s): 2
    NUMA node(s): 4
    Vendor ID: GenuineIntel

(Continued on next page)
## Platform Notes (Continued)

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<th>Intel(R) Corporation</th>
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<tr>
<td>Model name:</td>
<td>Intel(R) Xeon(R) Platinum 8380 CPU @ 2.30GHz</td>
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<tr>
<td>BIOS Model name:</td>
<td>Intel(R) Xeon(R) Platinum 8380 CPU @ 2.30GHz</td>
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<td>L3 cache:</td>
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<tr>
<td>NUMA node1 CPU(s):</td>
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<td>NUMA node2 CPU(s):</td>
<td>40-59,120-139</td>
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<tr>
<td>NUMA node3 CPU(s):</td>
<td>60-79,140-159</td>
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</tbody>
</table>

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperffmerf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust sgx bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vld xsaveopt xsavec xsavec vt-x vclflush lvms avx512fms vfnmadd16q vfnmaddq vfnmsub16q vfnmsubd16q vfnmsubq vfnmsubw16q avx512bw vfnmsubq16q vfnmsubw vfnmsubd avx512vabi vfnmsubq16q avx512vabi vfnmsubw vfnmsubd

/proc/cpuinfo cache data
cache size : 61440 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99
node 0 size: 257664 MB
node 0 free: 256109 MB
node 1 cpus: 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119
node 1 size: 258039 MB
node 1 free: 257257 MB
node 2 cpus: 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139
node 2 size: 258001 MB
node 2 free: 257505 MB
node 3 cpus: 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159
node 3 size: 258034 MB
node 3 free: 257577 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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Tested by: Tyrone Systems

SPECrate®2017_int_base = 550
SPECrate®2017_int_peak = 569

Test Date: Sep-2022
Hardware Availability: Jun-2021
Software Availability: May-2022

3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056501796 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.5 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.5"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
uname -a:
Linux icelakespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 6 02:14
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 402G 177G 225G 45% /home

From /sys/devices/virtual/dmi/id
Vendor: Tyrone_Systems
Product: Tyrone_Camarero_IDI100C2R-28
Product Family: Family
Serial: 2X22462203

(Continued on next page)
Platform Notes (Continued)

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:

BIOS Vendor: Intel Corporation
BIOS Version: SESC620.86B.01.01.0004.2110190142
BIOS Date: 10/19/2021

(End of data from sysinfo program)

Compiler Version Notes

```
C       | 502.gcc_r(peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C       | 502.gcc_r(peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base, peak)
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```
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SPEC CPU®2017 Integer Rate Result  

SPECrate®2017_int_base = 550  
SPECrate®2017_int_peak = 569  

CPU2017 License: 006042  
Test Sponsor: Netweb Pte Ltd  
Tested by: Tyrone Systems  

Test Date: Sep-2022  
Hardware Availability: Jun-2021  
Software Availability: May-2022  

Compiler Version Notes (Continued)  

Base Compiler Invocation  

C benchmarks:  
icx  
C++ benchmarks:  
icpx  
Fortran benchmarks:  
ifx  

Base Portability Flags  

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -DSPEC_LP64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64  

Base Optimization Flags  

C benchmarks:  
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -gopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc  

C++ benchmarks:  
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -gopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc  

Fortran benchmarks:  
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  

(Continued on next page)
SPECCPU®2017 Integer Rate Result

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Test Date: Sep-2022
Hardware Availability: Jun-2021
Software Availability: May-2022

Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto
- L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- lqkmalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
- fprofile-generate(pass 1)
- fprofile-use=default.profdata(pass 2) -xCORE-AVX512
- Ofast -ffast-math -fflo -mfpmath=sse -funroll-loops
- qopt-mem-layout-trans=4 -fno-strict-overflow
- L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

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**Tyrone Camarero ID1100C2R-28**  
(2.30 GHz, Intel Xeon Platinum 8380)  

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<th>Test Date: Sep-2022</th>
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<td>Software Availability: May-2022</td>
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</table>

### SPECrate®2017_int_base = 550  
**SPECrate®2017_int_peak = 569**

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**Peak Optimization Flags (Continued)**

500.perlbench_r (continued):
- -lqkmalloc

502.gcc_r: -m32
- -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin
- -std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
- -fprofile-use=default.profdata(pass 2) -xCORE-AVX512
- Ofast -ffast-math -fto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
- -ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=cll -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
- -ffast-math -fto -mfpmath=sse -funroll-loops
- -qopt-mem-layout-trans=4 -fno-alias
- -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- -lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:
520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml
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<thead>
<tr>
<th>SPEC CPU®2017 Integer Rate Result</th>
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</thead>
<tbody>
<tr>
<td><strong>Tyrone Systems</strong></td>
</tr>
<tr>
<td>(Test Sponsor: Netweb Pte Ltd)</td>
</tr>
<tr>
<td><strong>Tyrone Camarero IDI100C2R-28</strong></td>
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<tr>
<td>(2.30 GHz, Intel Xeon Platinum 8380)</td>
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<tr>
<td>SPECrate®2017_int_base = 550</td>
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<td>SPECrate®2017_int_peak = 569</td>
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<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Sep-2022</th>
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<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Jun-2021</td>
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<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: May-2022</td>
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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-09-06 04:39:17-0400.
Report generated on 2024-01-29 17:06:34 by CPU2017 PDF formatter v6716.
Originally published on 2022-09-27.