## Inspur Corporation

**Insput NF5280M6 (Intel Xeon Gold 5318Y)**

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<th>SPECrate®2017_fp_peak</th>
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<tbody>
<tr>
<td>96</td>
<td>405</td>
<td>415</td>
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</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 5318Y
- **Max MHz:** 3400
- **Nominal:** 2100
- **Enabled:** 48 cores, 2 chips, 2 threads/core
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 36 MB I+D on chip per core
- **Other:** None
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R, running at 2933)
- **Storage:** 1 x 2 TB NVME SSD
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler Build 20220316 for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler Build 20220316 for Linux;
- **Parallel:** No
- **Firmware:** Version 04.12.02 released Apr-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
### Results Table

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</tbody>
</table>

**SPECrate®2017_fp_base = 405**

**SPECrate®2017_fp_peak = 415**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Scaling_Governor set to Performance

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Inspur Corporation

Inspur NF5280M6 (Intel Xeon Gold 5318Y)

SPECrate®2017_fp_base = 405
SPECrate®2017_fp_peak = 415

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

General Notes (Continued)

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b5589ef0e16aca64df
running on localhost.localdomain Thu Sep 1 09:12:14 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

From lscpu from util-linux 2.32.1:

(Continued on next page)
**SPEC CPU®2017 Floating Point Rate Result**

**Inspur Corporation**

Inspur NF5280M6 (Intel Xeon Gold 5318Y)

**SPECrate®2017_fp_base = 405**

**SPECrate®2017_fp_peak = 415**

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**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation  
**Test Date:** Sep-2022  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

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**Platform Notes (Continued)**

- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Byte Order:** Little Endian
- **CPU(s):** 96
- **On-line CPU(s) list:** 0-95
- **Thread(s) per core:** 2
- **Core(s) per socket:** 24
- **Socket(s):** 2
- **NUMA node(s):** 4
- **Vendor ID:** GenuineIntel
- **CPU family:** 6
- **Model:** 106
- **Model name:** Intel(R) Xeon(R) Gold 5318Y CPU @ 2.10GHz
- **Stepping:** 6
- **CPU MHz:** 2600.084
- **CPU max MHz:** 3400.0000
- **CPU min MHz:** 800.0000
- **BogoMIPS:** 4200.00
- **Virtualization:** VT-x
- **L1d cache:** 48K
- **L1i cache:** 32K
- **L2 cache:** 1280K
- **L3 cache:** 36864K
- **NUMA node0 CPU(s):** 0-11,48-59
- **NUMA node1 CPU(s):** 12-23,60-71
- **NUMA node2 CPU(s):** 24-35,72-83
- **NUMA node3 CPU(s):** 36-47,84-95
- **Flags:**
  - fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmerf pni pclmulqdq dtes64 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_patin ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaves xgetbv1 xsaves qcm_l1c qcm_occup_llc qcm_mbb total qcm_mbb_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512v bmi umip pku ospke avx512 v bmi2 gfn i vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data  
**cache size:** 36864 KB

From numactl --hardware  
**WARNING:** a numactl 'node' might or might not correspond to a physical chip.  
available: 4 nodes (0-3)

*(Continued on next page)*
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Inspur NF5280M6 (Intel Xeon Gold 5318Y)

SPEC CPU®2017 Floating Point Rate Result

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SPECrate®2017_fp_base = 405
SPECrate®2017_fp_peak = 415

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Test Date: Sep-2022
Tested by: Inspur Corporation
Hardware Availability: Apr-2021
Software Availability: May-2022

Platform Notes (Continued)

node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 251811 MB
node 0 free: 245361 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 1 size: 252388 MB
node 1 free: 248022 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
node 2 size: 252676 MB
node 2 free: 248141 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 252781 MB
node 3 free: 248180 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056485828 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.3 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

(Continued on next page)
Inspur Corporation

Inspur NF5280M6 (Intel Xeon Gold 5318Y)

| SpecRaté2017_fp_base = 405 |
| SpecRaté2017_fp_peak = 415 |

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Platform Notes (Continued)

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Aug 31 10:23

SPEC is set to: /home/CPU2017

From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5280M6
Product Family: Family
Serial: 380251214

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 04.12.02
BIOS Date: 04/02/2021
BIOS Revision: 5.21

(End of data from sysinfo program)
## SPEC CPU®2017 Floating Point Rate Result

**Inspur Corporation**

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**CPU2017 License:** 3358  
**Test Date:** Sep-2022  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

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### Compiler Version Notes

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<td><strong>C</strong></td>
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<td>519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)</td>
</tr>
</tbody>
</table>
|          | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C++**  |             |
|          | 508.namd_r(base, peak) 510.parest_r(base, peak) |
|          | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C++, C** |             |
|           | 511.povray_r(base, peak) 526.blender_r(base, peak) |
|           | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C++, C, Fortran** |             |
|                    | 507.cactuBSSN_r(base, peak) |
|                    | Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **Fortran** |             |
|            | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak) |
|            | Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
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# SPEC CPU®2017 Floating Point Rate Result

**Inspur Corporation**

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## Compiler Version Notes (Continued)

```
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

```
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
```

## Base Compiler Invocation

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

Benchmarks using both Fortran and C:
- ifx icx

Benchmarks using both C and C++:
- icpx icx

Benchmarks using Fortran, C, and C++:
- icpx icx ifx

## Base Portability Flags

- 503.bwaves_r: -DSPEC_LP64
- 507.cactuBSSN_r: -DSPEC_LP64
- 508.namd_r: -DSPEC_LP64
- 510.parest_r: -DSPEC_LP64
- 511.povray_r: -DSPEC_LP64
- 519.lbm_r: -DSPEC_LP64

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Hardware Availability: Apr-2021
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Base Portability Flags (Continued)

521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
- w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
- w -m64 -Wl,-z, muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
- w -m64 -Wl,-z, muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
- w -m64 -std=c11 -Wl,-z, muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
- w -m64 -std=c11 -Wl,-z, muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
- w -m64 -std=c11 -Wl,-z, muldefs -xCORE-AVX512 -Ofast -ffast-math
- flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte -auto -ljemalloc
- L/usr/local/jemalloc64-5.0.1/lib
Inspur Corporation
Inspur NF5280M6 (Intel Xeon Gold 5318Y)

<table>
<thead>
<tr>
<th>SPECrate®2017_fp_base</th>
<th>405</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_fp_peak</td>
<td>415</td>
</tr>
</tbody>
</table>

**Peak Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

Benchmarks using both Fortran and C:
- ifx icx

Benchmarks using both C and C++:
- icpx icx

Benchmarks using Fortran, C, and C++:
- icpx icx ifx

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:
- 519.lbm_r: basepeak = yes
- 538.imagick_r: basepeak = yes
- 544.nab_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -qopt-zmm-usage=high -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
- 508.namd_r: basepeak = yes
- 510.parest_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc

(Continued on next page)
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Inspur NF5280M6 (Intel Xeon Gold 5318Y)

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

SPECrate®2017_fp_base = 405
SPECrate®2017_fp_peak = 415

Peak Optimization Flags (Continued)

510.parest_r (continued):
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes
549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-fito -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes
527.cam4_r: -w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -fito -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512
-Ofast -ffast-math -fito -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-fito -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html
# SPEC CPU®2017 Floating Point Rate Result

**Inspur Corporation**

**Inspur NF5280M6 (Intel Xeon Gold 5318Y)**

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You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml](http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-09-01 09:12:13-0400.  
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