### Spec CPU® 2017 Integer Rate Result

**Insipur Corporation**

**Insipur NF5280M6 (Intel Xeon Gold 6346)**

**SPECrater®2017_int_base = 303**  
**SPECrater®2017_int_peak = 311**

<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>3358</th>
<th>Test Date:</th>
<th>Oct-2022</th>
</tr>
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<tbody>
<tr>
<td>Test Sponsor:</td>
<td>Insipur Corporation</td>
<td>Hardware Availability:</td>
<td>Apr-2021</td>
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<td>Tested by:</td>
<td>Insipur Corporation</td>
<td>Software Availability:</td>
<td>May-2021</td>
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**Copies**

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<th>Name</th>
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<th>SPECrater®2017_int_base (303)</th>
<th>SPECrater®2017_int_peak (311)</th>
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<tr>
<td>500.perlbench_r</td>
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<td>557.xz_r</td>
<td>64</td>
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**Hardware**

- **CPU Name:** Intel Xeon Gold 6346  
- **Max MHz:** 3600  
- **Nominal:** 3100  
- **Enabled:** 32 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 36 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)  
- **Storage:** 1 x 2 TB NVME SSD  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux release 8.3 (Ootpa)  
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++  
  Compiler Build 20220316 for Linux;  
  Fortran: Version 2022.1 of Intel Fortran Compiler  
  Build 20220316 for Linux;  
- **Parallel:** No  
- **Firmware:** Version 04.12.02 released Apr-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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### Inspur Corporation

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- **Inspur NF5280M6 (Intel Xeon Gold 6346)**
- **SPECrate®2017_int_base = 303**
- **SPECrate®2017_int_peak = 311**

### Hardware Availability
- Apr-2021

### Software Availability
- May-2022

### Results Table

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<td></td>
<td></td>
</tr>
</tbody>
</table>

**Results appear in the order in which they were run. Bold underlined text indicates a median measurement.**

### Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Scaling_Governor set to Performance

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
```
SPEC CPU®2017 Integer Rate Result

Inspur Corporation

Inspur NF5280M6 (Intel Xeon Gold 6346)

SPECratenet 2017_int_base = 303
SPECratenet 2017_int_peak = 311

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Oct-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 >/proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5,
and the system compiler gcc 4.8.5;
sources available from jemalloc.net or

Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Sub NUMA Cluster (SNC) set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost.localdomain Thu Oct 13 04:11:04 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
  2 "physical id"s (chips)
  64 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 2
Core(s) per socket: 16
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
CPU family: 6

(Continued on next page)
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Platform Notes (Continued)

Model: 106
Model name: Intel(R) Xeon(R) Gold 6346 CPU @ 3.10GHz
Stepping: 6
CPU MHz: 3600.000
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 6200.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-7,32-39
NUMA node1 CPU(s): 8-15,40-47
NUMA node2 CPU(s): 16-24,48-56
NUMA node3 CPU(s): 25-31,57-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dts efi_compatible mmxext Monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse3_1 sse4_1 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 cdp_l3 invpcid_single intel_pni ssbd mba ibrs ibpb ibrs Enhanced fsgsbase tsc_adjust hle avx2 smep bmi2 ermen invpcid cqm rdt_a avx512f avx512kd rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsavec xsaves cqm_1ll cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect whetstone dtherm ida arat lpm pls avx512vbmi umip pku ospke avx512_vbmi2 fgni vaes vpclmulqdq avx512_vnni avx512_vbitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data
cache size: 36864 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 32 33 34 35 36 37 38 39
node 0 size: 25352 MB
node 0 free: 257312 MB
node 1 cpus: 8 9 10 11 12 13 14 15 40 41 42 43 44 45 46 47
node 1 size: 253449 MB
node 1 free: 257699 MB
node 2 cpus: 16 17 18 19 20 21 22 23 24 48 49 50 51 52 53 54 55 56
node 2 size: 253587 MB
node 2 free: 257774 MB
node 3 cpus: 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40
node 3 size: 254037 MB
node 3 free: 257796 MB
node distances:
node 0 1 2 3
0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo
MemTotal: 1056492600 KB
HugePages_Total: 0
Hugepagesize: 2048 KB

(Continued on next page)
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Platform Notes (Continued)

/sbin/tuned-adm active
Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

os-release:
NAME="Red Hat Enterprise Linux"
ID='rhel'
ID_LIKE="fedora"
VERSION_ID="8.3"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Oct 13 04:10

SPEC is set to: /home/CPU2017

From /sys/devices/virtual/dmi/id
Vendor: Inspur
Product: NF5280M6
Product Family: Family
Serial: 380251214

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200

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### SPECrate®2017_int_peak = 311

### Platform Notes (Continued)

**BIOS:**
- **BIOS Vendor:** American Megatrends Inc.  
- **BIOS Version:** 04.12.02  
- **BIOS Date:** 04/02/2021  
- **BIOS Revision:** 5.21

*(End of data from sysinfo program)*

### Compiler Version Notes

<table>
<thead>
<tr>
<th>Language</th>
<th>Compiler</th>
<th>Notes</th>
</tr>
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<tbody>
<tr>
<td>C</td>
<td>502.gcc_r(peak)</td>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316 Copyright (C) 1985-2022 Intel Corporation. All rights reserved.</td>
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### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifx

### Base Portability Flags

- `500.perlbench_r`: `-DSPEC_LP64 -DSPEC_LINUX_X64`
- `502.gcc_r`: `-DSPEC_LP64`
- `505.mcf_r`: `-DSPEC_LP64`
- `520.omnetpp_r`: `-DSPEC_LP64`
- `523.xalancbmk_r`: `-DSPEC_LP64 -DSPEC_LINUX`
- `525.x264_r`: `-DSPEC_LP64`
- `531.deepsjeng_r`: `-DSPEC_LP64`
- `541.leela_r`: `-DSPEC_LP64`
- `548.exchange2_r`: `-DSPEC_LP64`
- `557.xz_r`: `-DSPEC_LP64`

### Base Optimization Flags

**C benchmarks:**
- `-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math`  
- `-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin`  
- `-lqkmalloc`

**C++ benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin`  
- `-lqkmalloc`

**Fortran benchmarks:**
- `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs -align array32byte -auto`  
- `-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin`  
- `-lqkmalloc`
**SPEC CPU®2017 Integer Rate Result**

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- **Software Availability:** May-2022

---

**Peak Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

---

**Peak Portability Flags**

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mc_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

---

**Peak Optimization Flags**

C benchmarks:
- 500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdatapass2 -xCORE-AVX512
- 502.gcc_r: -m32 -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin -std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1) -fprofile-use=default.profdatapass2 -xCORE-AVX512

*(Continued on next page)*
Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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