SPEC CPU®2017 Integer Rate Result

Inspur Corporation

Inspur NF5180M6 (Intel Xeon Gold 6336Y)

SPECraten®2017_int_base = 382
SPECraten®2017_int_peak = 393

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

Test Date: Sep-2022
Hardware Availability: Apr-2021

Software Availability: May-2022

Copies

<table>
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<tr>
<th>Benchmark</th>
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<td>perlbench_r</td>
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<td>gcc_r</td>
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<td>xalancbmk_r</td>
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<td>x264_r</td>
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<tr>
<td>deepsjeng_r</td>
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<td>leela_r</td>
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<td>exchange2_r</td>
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<tr>
<td>xz_r</td>
<td>96</td>
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</tbody>
</table>

SPECrate®2017_int_base (382) SPECrate®2017_int_peak (393)

Hardware

CPU Name: Intel Xeon Gold 6336Y
Max MHz: 3600
Nominal: 2400
Enabled: 48 cores, 2 chips, 2 threads/core
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1.25 MB I+D on chip per core
L3: 36 MB I+D on chip per chip
Other: None
Memory: 1 TB (32 x 32 GB 2Rx8 PC4-3200AA-R)
Storage: 1 x 2 TB NVME SSD
Other: None

Software

OS: Red Hat Enterprise Linux release 8.3 (Ootpa)
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++
Compiler Build 20220316 for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler
Build 20220316 for Linux;
Parallel: No
Firmware: Version 04.12.02 released Apr-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
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Results Table

<table>
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</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Scaling_Governor set to Performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/lib/ia32:/home/CPU2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
### SPEC CPU®2017 Integer Rate Result

**Inspur Corporation**  
**Inspur NF5180M6 (Intel Xeon Gold 6336Y)**

<table>
<thead>
<tr>
<th>SPECrate®2017_int_peak</th>
<th>SPECrate®2017_int_base</th>
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<th>Software Availability:</th>
</tr>
</thead>
<tbody>
<tr>
<td>Inspur Corporation</td>
<td>May-2022</td>
</tr>
</tbody>
</table>

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### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3>     /proc/sys/vm/drop_caches  

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5,  
and the system compiler gcc 4.8.5;  
sources available from jemalloc.net or  

---

### Platform Notes

- **BIOS configuration:**  
  ENERGY_PERF_BIAS_CFG mode set to Performance  
  Hardware Prefetch set to Disable  
  VT Support set to Disable  
  Sub NUMA Cluster (SNC) set to Enable

- **Sysinfo program** /home/CPU2017/bin/sysinfo  
  Rev: r6622 of 2021-04-07 982a61ec0915b5891ef0e16acaf6d4  
  running on localhost.localdomain Tue Sep 27 00:22:42 2022

- **SUT (System Under Test) info as seen by some common utilities.**  
  For more information on this section, see  
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

- From /proc/cpuinfo  
  model name : Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz  
  96 "processors"  
  cores, sibings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
  cpu cores : 24  
  sibings : 48  
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23  
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

- From lscpu from util-linux 2.32.1:  
  Architecture: x86_64  
  CPU op-mode(s): 32-bit, 64-bit  
  Byte Order: Little Endian  
  CPU(s): 96  
  On-line CPU(s) list: 0-95  
  Thread(s) per core: 2  
  Core(s) per socket: 24  
  Socket(s): 2  
  NUMA node(s): 4  
  Vendor ID: GenuineIntel  
  CPU family: 6

(Continued on next page)
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Test Date: Sep-2022
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Software Availability: May-2022

Platform Notes (Continued)

Model: 106
Model name: Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz
Stepping: 6
CPU MHz: 3000.143
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 da_cpi vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat _13 cd p13 invpcid_single intel_pperl ssbd mba ibrs ibpb ibrs _enhanced fgfsbase tsc_adjust hle avx2 smep bmi2 rome invpcid cmqm rdt_a avx512if avx512dq rdseed adx smap avx512ifmclflushopt clwb intel_pt avx512cd sha _ni avx512bw avx512vl xsaveopt xsaveprec xgetbv1 xsaveeq cmqm _llc cmqm _occup _llc cmqm _mb _total cmqm _mb _local split_lock _detect whoninvd dtherm ida arat pln pts avx512vbmi umip pk uospke avx512_vbmi2 gfni vaes vpcmullqdqavx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rpdpd md_clear pconfig flush_l1d arch_capabilities

/proc/cpuinfo cache data

cache size : 36864 KB

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 48 49 50 51 52 53 54 55 56 57 58 59
node 0 size: 255017 MB
node 0 free: 257333 MB
node 1 cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 1 size: 255338 MB
node 1 free: 257637 MB
node 2 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83
node 2 size: 255460 MB
node 2 free: 257750 MB
node 3 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 255176 MB
node 3 free: 257746 MB
node distances:

0: 10 11 20 20
1: 11 10 20 20
2: 20 20 10 11
3: 20 20 11 10

From /proc/meminfo

MemTotal: 1056485684 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

(Continued on next page)
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Platform Notes (Continued)

/sbin/tuned-adm active
  Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
  performance
From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga
uname -a:
  Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020
  x86_64 x86_64 x86_64 GNU/Linux
Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
  Bypass disabled via prctl and
  seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
  barriers and __user pointer
  sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
  conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 27 00:22

SPEC is set to: /home/CPU2017

Filesystem            Type Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   1.4T  125G  1.3T   9% /home

From /sys/devices/virtual/dmi/id
  Vendor: Inspur
  Product: NF5180M6
  Product Family: Family
  Serial: 380827124

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  32x Micron 18ASF4G72PDZ-3G2E1 32 GB 2 rank 3200

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### Platform Notes (Continued)

**BIOS:**
- **BIOS Vendor:** American Megatrends Inc.  
- **BIOS Version:** 04.12.02  
- **BIOS Date:** 04/02/2021  
- **BIOS Revision:** 5.21

(End of data from sysinfo program)

### Compiler Version Notes

<table>
<thead>
<tr>
<th>Language</th>
<th>Programs</th>
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<tr>
<td><strong>C</strong></td>
<td>502.gcc_r(peak)</td>
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| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316**  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C** | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak) |
| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316**  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C** | 502.gcc_r(peak) |
| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316**  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C** | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak) |
| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316**  
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| **C** | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak) |
| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316**  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **C++** | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak) |
| **Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316**  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved. |

| **Fortran** | 548.exchange2_r(base, peak) |
| **Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316**  
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---

**Base Compiler Invocation**

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

---

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-fflt -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

C++ benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc
Inspec Corporation

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Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.proftdata(pass 2) -xCORE-AVX512
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lgkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.proftdata(pass 2) -xCORE-AVX512
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation

SPECrater®2017_int_base = 382
SPECrater®2017_int_peak = 393

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Inspur Corporation
Inspur NF5180M6 (Intel Xeon Gold 6336Y)

<table>
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<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
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<td>382</td>
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</table>

CPU2017 License: 3358
Test Sponsor: Inspur Corporation
Tested by: Inspur Corporation
Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V2.5.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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