# SPEC CPU®2017 Integer Rate Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero SDI100A3U-212**  
(2.30 GHz, Intel Xeon Silver 4316)

<table>
<thead>
<tr>
<th>CPU2017 License: 006042</th>
<th>Test Date: Sep-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Jun-2021</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: May-2022</td>
</tr>
</tbody>
</table>

## Hardware

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base (253)</th>
<th>SPECrate®2017_int_peak (260)</th>
</tr>
</thead>
<tbody>
<tr>
<td>80</td>
<td>0</td>
<td>260</td>
</tr>
<tr>
<td>165</td>
<td>213</td>
<td>239</td>
</tr>
<tr>
<td>174</td>
<td>431</td>
<td>493</td>
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<tr>
<td>408</td>
<td>498</td>
<td>524</td>
</tr>
<tr>
<td>138</td>
<td>493</td>
<td>524</td>
</tr>
</tbody>
</table>

- **CPU Name:** Intel Xeon Silver 4316
- **Max MHz:** 3400
- **Nominal:** 2300
- **Enabled:** 40 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 30 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)
- **Storage:** 1 x 512 GB NVMe SSD
- **Other:** None

## Software

- **OS:** Red Hat Enterprise Linux release 8.5 (Ootpa)  
  4.18.0-348.el8.x86_64
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
  Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- **Parallel:** No
- **Firmware:** Version 1.2a released May-2022
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 32/64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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SPECrate®2017_int_base = 253
SPECrate®2017_int_peak = 260

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
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<th>Seconds</th>
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<td>505.mcf_r</td>
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<td><strong>627</strong></td>
<td><strong>138</strong></td>
<td>627</td>
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</tr>
</tbody>
</table>

SPECrate®2017_int_base = 253
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "~/home/cpu2017/lib/intel64/:~/home/cpu2017/lib/ia32/:~/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
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Tested by: Tyrone Systems

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the
system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the
system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the
system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
SNC (Sub NUMA)= Enable
KTI Prefetch= Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: v6622 of 2021-04-07 982a61ec915b55891ef0e16acaf64d
running on icelake2 Fri Sep 30 14:22:49 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4316 CPU @ 2.30GHz
  2 "physical id"s (chips)
  80 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 20
siblings : 40
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 80
On-line CPU(s) list: 0-79
Thread(s) per core: 2
Core(s) per socket: 20
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation

(Continued on next page)
## SPEC CPU®2017 Integer Rate Result

**SPECrate®2017_int_base = 253**

**SPECrate®2017_int_peak = 260**

### Platform Notes (Continued)

<table>
<thead>
<tr>
<th>CPU family:</th>
<th>6</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model:</td>
<td>106</td>
</tr>
<tr>
<td>Model name:</td>
<td>Intel(R) Xeon(R) Silver 4316 CPU @ 2.30GHz</td>
</tr>
<tr>
<td>BIOS Model name:</td>
<td>Intel(R) Xeon(R) Silver 4316 CPU @ 2.30GHz</td>
</tr>
<tr>
<td>Stepping:</td>
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<tr>
<td>CPU MHz:</td>
<td>2300.000</td>
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<tr>
<td>BogoMIPS:</td>
<td>4600.00</td>
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<tr>
<td>Virtualization:</td>
<td>VT-x</td>
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<tr>
<td>L1d cache:</td>
<td>48K</td>
</tr>
<tr>
<td>L1i cache:</td>
<td>32K</td>
</tr>
<tr>
<td>L2 cache:</td>
<td>1280K</td>
</tr>
<tr>
<td>L3 cache:</td>
<td>30720K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s):</td>
<td>0-9,40-49</td>
</tr>
<tr>
<td>NUMA node1 CPU(s):</td>
<td>10-19,50-59</td>
</tr>
<tr>
<td>NUMA node2 CPU(s):</td>
<td>20-29,60-69</td>
</tr>
<tr>
<td>NUMA node3 CPU(s):</td>
<td>30-39,70-79</td>
</tr>
<tr>
<td>Flags:</td>
<td>fpu vme de pse tsc msr mcr mxe cmov pat pse36 cli cce abt sm tsc lmt tm vm cmov st smm.InvariantCultures msr pdcm pdi pcid cda sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat _l3 invpcid_single intel_pni ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmx flexpriority ept vpid_ap t_ad fsqbsebase tsc_adjust sgx bmi1 hle avx2 smep bmi2 etsa invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl vxsaveopt xsaves xsavec xsaveopt xsaves cqu _l ccm _occu p1 _lc cqm_mmb_total cqm_mmb_local split_lock_detect wboinvd dtherm ida arat pin pts avx512vmid umip pku ospe avx512_vbmi2 gfi vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid sgx _lc fms _clear pconfig flush_l1d arch_capabilities</td>
</tr>
</tbody>
</table>

From /proc/cpuinfo cache data

cache size : 30720 KB

From numactl --hardware

<table>
<thead>
<tr>
<th>node 0 cpus:</th>
<th>0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49</th>
</tr>
</thead>
<tbody>
<tr>
<td>node 0 size:</td>
<td>257632 MB</td>
</tr>
<tr>
<td>node 0 free:</td>
<td>257327 MB</td>
</tr>
<tr>
<td>node 1 cpus:</td>
<td>10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59</td>
</tr>
<tr>
<td>node 1 size:</td>
<td>258006 MB</td>
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<tr>
<td>node 1 free:</td>
<td>257086 MB</td>
</tr>
<tr>
<td>node 2 cpus:</td>
<td>20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69</td>
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<td>node 2 size:</td>
<td>258043 MB</td>
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<tr>
<td>node 2 free:</td>
<td>257777 MB</td>
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<td>30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79</td>
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<tr>
<td>node 3 size:</td>
<td>258041 MB</td>
</tr>
<tr>
<td>node 3 free:</td>
<td>257775 MB</td>
</tr>
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<td>node distances:</td>
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<tr>
<td>node 0 1 2 3</td>
<td></td>
</tr>
<tr>
<td>node 0 10 11 20 20</td>
<td></td>
</tr>
<tr>
<td>node 1 11 10 20 20</td>
<td></td>
</tr>
<tr>
<td>node 2 20 20 10 11</td>
<td></td>
</tr>
<tr>
<td>node 3 20 20 11 10</td>
<td></td>
</tr>
</tbody>
</table>

From /proc/meminfo

| MemTotal: | 1056484448 kB |
| HugePages_Total: | 0 |
| Hugepagesize: | 2048 kB |

(Continued on next page)
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Platform Notes (Continued)

/sbin/tuned-adm active
   Current active profile: throughput-performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.5 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.5"
      PLATFORM_ID="platform:e18"
      PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
Linux icelake2 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64 x86_64
x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                        Not affected
CVE-2018-3620 (L1 Terminal Fault):                     Not affected
Microarchitectural Data Sampling:                      Not affected
CVE-2017-5754 (Meltdown):                              Not affected
CVE-2018-3639 (Speculative Store Bypass):              Mitigation: Speculative Store
   Bypass disabled via prctl and
   seccomp
CVE-2017-5753 (Spectre variant 1):                     Mitigation: usercopy/swapps
   barriers and __user pointer
   sanitization
CVE-2017-5715 (Spectre variant 2):                     Mitigation: Enhanced IBRS, IBPB:
   conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):                Not affected

run-level 3 Sep 30 13:49

SPEC is set to: /home/cpu2017
Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   402G  154G  248G  39% /home

From /sys/devices/virtual/dmi/id
   Vendor: Tyrone Systems
   Product: Tyrone Camarero SDI100A3U-212
   Product Family: SMC X12

Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
      16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200, configured at 2666
   BIOS:
      BIOS Vendor: American Megatrends International, LLC.
      BIOS Version: 1.2a
      BIOS Date: 05/12/2022

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Platform Notes (Continued)

BIOS Revision: 5.22
(End of data from sysinfo program)

Compiler Version Notes

Base Compiler Invocation

C benchmarks:
icx

(Continued on next page)
## Base Compiler Invocation (Continued)

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

## Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
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<tbody>
<tr>
<td>perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>gcc_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>mcf_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>omnetpp_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>xalancbmk_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>x264_r</td>
<td>-DSPEC_LP64</td>
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<td>deepsjeng_r</td>
<td>-DSPEC_LP64</td>
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<td>leela_r</td>
<td>-DSPEC_LP64</td>
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<td>exchange2_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>xz_r</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

## Base Optimization Flags

### C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

### C++ benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```

### Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc
```
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Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass2) -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass1)
-fprofile-use=default.profdata(pass2) -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

(Continued on next page)
Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
    -ffast-math -flto -mfpmath=sse -funroll-loops
    -qopt-mem-layout-trans=4 -fno-alias
    -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
    -lqkmalloc
557.xz_r: basepeak = yes

C++ benchmarks:
520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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