## SPEC CPU®2017 Integer Speed Result

### CPU2017 License:
006042

### Test Sponsor:
Netweb Pte Ltd

### Tested by:
Tyrone Systems

### Test Date:
Sep-2022

### Hardware Availability:
Apr-2021

### Software Availability:
May-2022

---

### CPU Name:
Intel Xeon Gold 6348

### Max MHz:
3500

### Nominal:
2600

### Enabled:
56 cores, 2 chips, 2 threads/core

### Orderable:
1.2 Chips

### Cache L1:
32 KB I + 48 KB D on chip per core

### Cache L2:
1.25 MB I+D on chip per core

### Cache L3:
42 MB I+D on chip per chip

### Memory:
512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)

### Storage:
1 x 512 GB NVMe SSD

### Other:
None

### OS:
Red Hat Enterprise Linux release 8.5 (Ootpa) 4.18.0-348.el8.x86_64

### Compiler:
C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;

### Parallel:
Yes

### Firmware:
Version PEGC0011 released Aug-2022

### File System:
xfs

### System State:
Run level 3 (multi-user)

### Base Pointers:
64-bit

### Peak Pointers:
64-bit

### Other:
jemalloc memory allocator V5.0.1

### Power Management:
BIOS and OS set to prefer performance at the cost of additional power usage.

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### Threads

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
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</tr>
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</tr>
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</tr>
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</tr>
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<td>112</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>112</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>112</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>112</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>112</td>
</tr>
</tbody>
</table>

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### SPECspeed®2017_int_base = 12.3

### SPECspeed®2017_int_peak = 12.6

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### Software

- OS: Red Hat Enterprise Linux release 8.5 (Ootpa) 4.18.0-348.el8.x86_64
- Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- Parallel: Yes
- Firmware: Version PEGC0011 released Aug-2022
- File System: xfs
- System State: Run level 3 (multi-user)
- Base Pointers: 64-bit
- Peak Pointers: 64-bit
- Other: jemalloc memory allocator V5.0.1
- Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TD100C3R-212
(2.60 GHz, Intel Xeon Gold 6348)

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SPECspeed®2017_int_base = 12.3
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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>112</td>
<td>247</td>
<td>7.18</td>
<td>246</td>
<td>7.22</td>
<td>245</td>
<td>7.25</td>
<td>112</td>
<td>227</td>
<td>7.83</td>
<td>227</td>
<td>7.83</td>
<td>227</td>
<td>7.81</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>112</td>
<td>361</td>
<td>11.0</td>
<td>359</td>
<td>11.1</td>
<td>359</td>
<td>11.1</td>
<td>112</td>
<td>347</td>
<td>11.5</td>
<td>343</td>
<td>11.6</td>
<td>348</td>
<td>11.4</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>112</td>
<td>243</td>
<td>19.4</td>
<td>239</td>
<td>19.7</td>
<td>239</td>
<td>19.7</td>
<td>112</td>
<td>243</td>
<td>19.4</td>
<td>239</td>
<td>19.8</td>
<td>239</td>
<td>19.7</td>
</tr>
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<td>620.omnetpp_s</td>
<td>112</td>
<td>158</td>
<td>10.3</td>
<td>158</td>
<td>10.3</td>
<td>157</td>
<td>10.4</td>
<td>112</td>
<td>158</td>
<td>10.3</td>
<td>158</td>
<td>10.3</td>
<td>157</td>
<td>10.4</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>112</td>
<td>67.2</td>
<td>21.1</td>
<td>67.2</td>
<td>21.1</td>
<td>69.1</td>
<td>20.5</td>
<td>112</td>
<td>67.2</td>
<td>21.1</td>
<td>67.2</td>
<td>21.1</td>
<td>69.1</td>
<td>20.5</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>112</td>
<td>102</td>
<td>17.3</td>
<td>102</td>
<td>17.3</td>
<td>102</td>
<td>17.3</td>
<td>112</td>
<td>97.1</td>
<td>18.2</td>
<td>97.2</td>
<td>18.1</td>
<td>97.3</td>
<td>18.1</td>
</tr>
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<td>112</td>
<td>244</td>
<td>5.87</td>
<td>244</td>
<td>5.87</td>
<td>244</td>
<td>5.87</td>
<td>112</td>
<td>244</td>
<td>5.87</td>
<td>244</td>
<td>5.87</td>
<td>244</td>
<td>5.87</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>112</td>
<td>351</td>
<td>4.87</td>
<td>351</td>
<td>4.86</td>
<td>351</td>
<td>4.87</td>
<td>112</td>
<td>351</td>
<td>4.87</td>
<td>351</td>
<td>4.86</td>
<td>351</td>
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<td>112</td>
<td>147</td>
<td>20.1</td>
<td>150</td>
<td>19.6</td>
<td>148</td>
<td>19.9</td>
<td>112</td>
<td>147</td>
<td>20.1</td>
<td>150</td>
<td>19.6</td>
<td>148</td>
<td>19.9</td>
</tr>
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<td>254</td>
<td>24.3</td>
<td>254</td>
<td>24.3</td>
<td>252</td>
<td>24.5</td>
<td>112</td>
<td>254</td>
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<td>254</td>
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SPECspeed®2017_int_base = 12.3
SPECspeed®2017_int_peak = 12.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalanchmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option ‘submit’ was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TD1100C3R-212
(2.60 GHz, Intel Xeon Gold 6348)

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

SPECspeed®2017_int_base = 12.3
SPECspeed®2017_int_peak = 12.6

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.: numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
SNC (Sub NUMA) = Enable
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: re6622 of 2021-04-07 982a61ec0915b55891ef0e16aca6bc6d
running on Tyrone spec Tue Sep 27 16:44:49 2022
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo
From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6348 CPU @ 2.60GHz
  2 "physical Id"s (chips)
  112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 28
siblings : 56
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
From lsccpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 112
On-line CPU(s) list: 0-111
Thread(s) per core: 2
Core(s) per socket: 28
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
Platform Notes (Continued)

Vendor ID:           GenuineIntel
BIOS Vendor ID:      Intel(R) Corporation
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Gold 6348 CPU @ 2.60GHz
BIOS Model name:     Intel(R) Xeon(R) Gold 6348 CPU @ 2.60GHz
Stepping:            6
CPU MHz:             2600.000
CPU max MHz:         3500.0000
CPU min MHz:         800.0000
BogoMIPS:            5200.00
Virtualization:      VT-x
L1d cache:           48K
L1i cache:           32K
L2 cache:            1280K
L3 cache:            43008K
NUMA node0 CPU(s):   0-27,56-83
NUMA node1 CPU(s):   28-55,84-111
Flags:               fpu vme de pse tsc mar mce pmx msx cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
l1t constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aeprferf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtrp pdcm pccd dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsafe
avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_l3 invvpidd single
intel_pni ssbd mba ibrs ibrb stibp ibrs_enhanced trp_shadow vni flexpriority ept
vpid ept_ad fsgsbase tsc_adjust sgx mni hle avx2 smep bmi2 emms invpcid cqm rdt_a
avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_1lc cqm_occup_1lc cqm_mbb_total
cqm_mmb_local split_lock_detect wbinvd dtherm ida arat pinn pts hwlp wpw_act_window
hwlp epp wpw_pkg_req avx512vbmi umip pku ospke avx512_vbmi gfni vaes vpcmuloqdq
avx512 vnni avx512_bitsalig tme avx512_vpopcntdq ila57 rdrpid sgx_1c firm md_clear
pconfig flush_lld arch_capabilities

From /proc/cpuinfo cache data
  cache size : 43008 KB

WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
  node 0 size: 257590 MB
  node 0 free: 222755 MB
  node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
  node 1 size: 258033 MB
  node 1 free: 224926 MB
  node distances:
    node   0   1
    0:  10  20
    1:  20  10

From /proc/meminfo
  MemTotal:       527998436 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

(Continued on next page)
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(2.60 GHz, Intel Xeon Gold 6348)

SPECspeed®2017_int_base = 12.3
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Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Platform Notes (Continued)

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.5 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.5"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
  Linux Tyronespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass):
    Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
    Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):
    Mitigation: Enhanced IBRS, IBPB: conditional, RBB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 26 18:40

SPEC is set to: /home/cpu2017
  Filesystem Type   Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   402G  166G  236G  42% /home

From /sys/devices/virtual/dmi/id
  Vendor: Tyrone Systems
  Product: Tyrone Camarero TDI100C3R-212
  Product Family: Family
  Serial: 2X22002203

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
  2x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200
  14x Samsung M393A4K40EB3-CWE 32 GB 2 rank 3200

BIOS:

(Continued on next page)
**SPEC CPU®2017 Integer Speed Result**

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</tr>
</tbody>
</table>

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</tr>
</tbody>
</table>

**Platform Notes (Continued)**

- BIOS Vendor: American Megatrends International, LLC.
- BIOS Version: PEGC0011
- BIOS Date: 08/10/2022
- BIOS Revision: 5.22

(End of data from sysinfo program)

**Compiler Version Notes**

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<table>
<thead>
<tr>
<th>Compiler</th>
<th>Benchmark(s)</th>
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</tr>
</thead>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
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<tr>
<th>C++</th>
<th>620.omnetpp_s(base, peak)</th>
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
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<thead>
<tr>
<th>Fortran</th>
<th>648.exchange2_s(base, peak)</th>
</tr>
</thead>
<tbody>
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<td></td>
<td></td>
</tr>
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
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**Base Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

**Base Portability Flags**

- 600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
- 602.gcc_s: -DSPEC_LP64
- 605.mcf_s: -DSPEC_LP64

(Continued on next page)
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## Base Portability Flags (Continued)

- 620.omnetpp_s: -DSPEC_LP64  
- 623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
- 625.x264_s: -DSPEC_LP64  
- 631.deepsjeng_s: -DSPEC_LP64  
- 641.leela_s: -DSPEC_LP64  
- 648.exchange2_s: -DSPEC_LP64  
- 657.xz_s: -DSPEC_LP64

## Base Optimization Flags

### C benchmarks:

- m64 -g -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto  
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
- DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

### C++ benchmarks:

- m64 -g -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto  
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

### Fortran benchmarks:

- m64 -g -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto  
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
- nostandard-realloc-lhs -align array32byte  
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

## Peak Compiler Invocation

### C benchmarks:

- icx

### C++ benchmarks:

- icpx

### Fortran benchmarks:

- ifx
**SPEC CPU®2017 Integer Speed Result**

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TDI100C3R-212
(2.60 GHz, Intel Xeon Gold 6348)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<tr>
<td>12.3</td>
<td>12.6</td>
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**CPU2017 License:** 006042
**Test Sponsor:** Netweb Pte Ltd
**Tested by:** Tyrone Systems
**Test Date:** Sep-2022
**Hardware Availability:** Apr-2021
**Software Availability:** May-2022

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

**C benchmarks:**

600.perlbench_s: m64 -g -std=c11 -Wl,-z,muldefs
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

602.gcc_s: m64 -g -std=c11 -Wl,-z,muldefs
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: m64 -g -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -03
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

**C++ benchmarks:**

620.omnetpp_s: basepeak = yes

623.xalancbnk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

**Fortran benchmarks:**

648.exchange2_s: basepeak = yes
**SPEC CPU®2017 Integer Speed Result**

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The flags files that were used to format this result can be browsed at  

You can also download the XML flags sources by saving the following links:  
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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