Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyroe Camarero TDI100C3R-212
(2.20 GHz, Intel Xeon Gold 5320)

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

SPECrater®2017_int_base = 338
SPECrater®2017_int_peak = 348

Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

500.perlbench_r 104
  246
  261

502.gcc_r 104
  302

505.mcf_r 104

520.omnetpp_r 104
  205

523.xalancbmk_r 104

525.x264_r 104
  504
  681
  718

531.deepsjeng_r 104
  257

541.leela_r 104
  258

548.exchange2_r 104
  748

557.xz_r 104
  178

--- SPECrater®2017_int_base (338) ---
--- SPECrater®2017_int_peak (348) ---

### Hardware

<p>| | |</p>
<table>
<thead>
<tr>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>CPU</td>
<td>Intel Xeon Gold 5320</td>
</tr>
<tr>
<td>Max MHz</td>
<td>3400</td>
</tr>
<tr>
<td>Nominal</td>
<td>2200</td>
</tr>
<tr>
<td>Enabled</td>
<td>52 cores, 2 chips, 2 threads/core</td>
</tr>
<tr>
<td>Orderable</td>
<td>1.2 Chips</td>
</tr>
<tr>
<td>Cache L1</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2</td>
<td>1.25 MB I+D on chip per core</td>
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<tr>
<td>L3</td>
<td>39 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
</tr>
<tr>
<td>Memory</td>
<td>512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R, running at 2933)</td>
</tr>
<tr>
<td>Storage</td>
<td>1 x 512 GB NVMe SSD</td>
</tr>
<tr>
<td>Other</td>
<td>None</td>
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</table>

### Software

<p>| | |</p>
<table>
<thead>
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<tbody>
<tr>
<td>OS</td>
<td>Red Hat Enterprise Linux release 8.5 (Ootpa) 4.18.0-348.el8.x86_64</td>
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<tr>
<td>Compiler</td>
<td>C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;</td>
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<tr>
<td>Parallel</td>
<td>No</td>
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<tr>
<td>Firmware</td>
<td>Version PEGC0011 released Aug-2022</td>
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<tr>
<td>File System</td>
<td>xfs</td>
</tr>
<tr>
<td>System State</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers</td>
<td>32/64-bit</td>
</tr>
<tr>
<td>Other</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
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</tbody>
</table>
SPEC CPU®2017 Integer Rate Result

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Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)

Tyroone Camarero TDI100C3R-212
(2.20 GHz, Intel Xeon Gold 5320)

SPECrate®2017_int_base = 338
SPECrate®2017_int_peak = 348

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>104</td>
<td>734</td>
<td>226</td>
<td>734</td>
<td>225</td>
<td>734</td>
<td>226</td>
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<tr>
<td>502.gcc_r</td>
<td>104</td>
<td>565</td>
<td>261</td>
<td>564</td>
<td>261</td>
<td>565</td>
<td>260</td>
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<tr>
<td>505.mcf_r</td>
<td>104</td>
<td>316</td>
<td>531</td>
<td>318</td>
<td>529</td>
<td>318</td>
<td>529</td>
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<tr>
<td>520.omnetpp_r</td>
<td>104</td>
<td>661</td>
<td>206</td>
<td>666</td>
<td>205</td>
<td>670</td>
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<td>523.xalancbmk_r</td>
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<td>218</td>
<td>505</td>
<td>219</td>
<td>502</td>
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<td>525.x264_r</td>
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<td>681</td>
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<td>681</td>
<td>268</td>
<td>680</td>
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<tr>
<td>531.deepsjeng_r</td>
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<td>462</td>
<td>258</td>
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<td>257</td>
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<tr>
<td>541.leela_r</td>
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<td>258</td>
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<td>258</td>
<td>651</td>
<td>265</td>
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<tr>
<td>548.exchange2_r</td>
<td>104</td>
<td>364</td>
<td>748</td>
<td>364</td>
<td>749</td>
<td>365</td>
<td>747</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>104</td>
<td>624</td>
<td>180</td>
<td>630</td>
<td>178</td>
<td>630</td>
<td>178</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"
SPEC CPU®2017 Integer Rate Result

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Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TD11C3R-212
(2.20 GHz, Intel Xeon Gold 5320)

SPECrate®2017_int_base = 338
SPECrate®2017_int_peak = 348

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 >/proc/sys/vm/drop_caches
runcpu command invoked through numaclt i.e.:
umaclt --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
SNC (Sub NUMA) = Enable
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on Tyronespec Fri Sep 30 18:29:38 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
  2 "physical Id"s (chips)
  104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 26
siblings : 52
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
2h 25
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 104
On-line CPU(s) list: 0-103
Thread(s) per core: 2
Core(s) per socket: 26
Socket(s): 2
NUMA node(s): 2

(Continued on next page)
**SPEC CPU®2017 Integer Rate Result**

**Tyrone Systems**
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**Hardware Availability:** Apr-2021
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**SPECrate®2017_int_base = 338**
**SPECrate®2017_int_peak = 348**

---

**Platform Notes (Continued)**

Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
BIOS Model name: Intel(R) Xeon(R) Gold 5320 CPU @ 2.20GHz
Stepping: 6
CPU MHz: 2200.000
CPU max MHz: 3400.0000
CPU min MHz: 800.0000
BogoMIPS: 4400.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 39936K
NUMA node0 CPU(s): 0-25,52-77
NUMA node1 CPU(s): 26-51,78-103

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pclid dca sse4_1 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lmlahf_lm invpcid_single intel_pni ssbd mba ibrs ibpb ibrs_enhanced tpr_shadow vmmflexpriority ept vpid fsgsbase tsc_adjust ksm bpl cp tents esttl acpi_pstate vsxvmx pdcm pkplicmpx cpuid_fault

```
/platform/cpuinfo cache data
```

cache size : 39936 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
MemTotal:       528000168 kB
HugePages_Total:   0
Hugepagesize:       2048 kB
```

From /sbin/tuned-adm active
Current active profile: throughput-performance

(Continued on next page)
### Platform Notes (Continued)

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

os-release:
- NAME="Red Hat Enterprise Linux"
- VERSION="8.5 (Ootpa)"
- ID="rhel"
- ID_LIKE="fedora"
- VERSION_ID="8.5"
- PLATFORM_ID="platform:el8"
- PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
- ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
```
Linux Tyronespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64
x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 30 17:06

SPEC is set to: /home/cpu2017

Filesystem | Type  | Size  | Used Avail Used% Mounted on |
--- | --- | --- | --- | --- |
/devmapper/rhel-home | xfs | 402G | 167G | 235G | 42% /home |

From /sys/devices/virtual/dmi/id
Vendor: Tyrone Systems
Product: Tyrone Camarero TDI100C3R-212
Product Family: Family
Serial: 2X22002203

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 2x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200, configured at 2933
- 14x Samsung M393A4K40EB3-CWE 32 GB 2 rank 2933, configured at 2933

BIOS:
- BIOS Vendor: American Megatrends International, LLC.
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TDI100C3R-212
(2.20 GHz, Intel Xeon Gold 5320)

SPECrate®2017_int_base = 338
SPECrate®2017_int_peak = 348

<table>
<thead>
<tr>
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<th>Test Date: Sep-2022</th>
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</thead>
<tbody>
<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Apr-2021</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: May-2022</td>
</tr>
</tbody>
</table>

Platform Notes (Continued)

BIOS Version: PEGC0011
BIOS Date: 08/10/2022
BIOS Revision: 5.22

(End of data from sysinfo program)

Compiler Version Notes

----------------------------------------------------------
C   | 502.gcc_r(peak)
----------------------------------------------------------
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------
C   | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
C   | 557.xz_r(base, peak)
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------
C++ | 502.gcc_r(peak)
----------------------------------------------------------
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------
C   | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
C   | 557.xz_r(base, peak)
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)
| Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
----------------------------------------------------------
Fortran | 548.exchange2_r(base, peak)
| Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
| Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
SPEC CPU®2017 Integer Rate Result
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</tr>
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<td>Software Availability: May-2022</td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
  icx

C++ benchmarks:
  icpx

Fortran benchmarks:
  ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
  -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
  -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
  -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
  -lqkmalloc

C++ benchmarks:
  -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
  -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
  -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
  -lqkmalloc

Fortran benchmarks:
  -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
  -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
  -nostandard-realloc-lhs -align array32byte -auto
  -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
  -lqkmalloc

Page 7 Standard Performance Evaluation Corporation (info@spec.org) https://www.spec.org/
# SPEC CPU®2017 Integer Rate Result

## Test Report

### Test Details

<table>
<thead>
<tr>
<th>Test Sponsor</th>
<th>Netweb Pte Ltd</th>
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<tbody>
<tr>
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</tr>
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### System Details

- **CPU2017 License:** 006042
- **Tested by:** Tyrone Systems
- **Test Sponsor:** Netweb Pte Ltd
- **Test Date:** Sep-2022
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- **Test Sponsor:** Netweb Pte Ltd
- **Tested by:** Tyrone Systems
- **Test Date:** Sep-2022

### SPECrate Results

- **SPECrate®2017_int_base = 338**
- **SPECrate®2017_int_peak = 348**

### Peak Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifx

### Peak Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -D_FILE_OFFSET_BITS=64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Peak Optimization Flags

**C benchmarks:**

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs -fprofile-generate(pass 1)
- fprofile-use=default.profdatalpass 2) -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
- qopt-mem-layout-trans=4 -fno-strict-overflow
- L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- lqkmalloc

502.gcc_r: -m32
- L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin
- std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
- fprofile-use=default.profdatalpass 2) -xCORE-AVX2 -Ofast
- fffast-math -flto -mfpmath=sse -funroll-loops
- qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
- ljemalloc

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TDI100C3R-212
(2.20 GHz, Intel Xeon Gold 5320)

SPECrate®2017_int_base = 338
SPECrate®2017_int_peak = 348

Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
                      -ffast-math -flto -mfpmath=sse -funroll-loops
                      -qopt-mem-layout-trans=4 -fno-alias
                      -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
                      -lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU*2017 v1.1.8 on 2022-09-30 08:59:37-0400.
Report generated on 2024-01-29 17:09:51 by CPU2017 PDF formatter v6716.
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