## SPEC CPU®2017 Integer Rate Result

**Tyronne Systems**  
(Test Sponsor: Netweb Pte Ltd)  
Tyronne Camarero IDI100C2R-28  
(2.40 GHz, Intel Xeon Gold 6336Y)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 350</th>
<th>SPECrate®2017_int_peak = 360</th>
</tr>
</thead>
</table>

**Copies**  
- **500.perlbench_r** 96  
- **502.gcc_r** 96  
- **505.mcf_r** 96  
- **520.omnetpp_r** 96  
- **523.xalancbmk_r** 96  
- **525.x264_r** 96  
- **531.deepsjeng_r** 96  
- **541.leela_r** 96  
- **548.exchange2_r** 96  
- **557.xz_r** 96  

### Hardware
- **CPU Name:** Intel Xeon Gold 6336Y  
- **Max MHz:** 3600  
- **Nominal:** 2400  
- **Enabled:** 48 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 Chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 1.25 MB I+D on chip per core  
- **L3:** 36 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 1 x 512 GB NVMe SSD  
- **Other:** None

### Software
- **OS:** Red Hat Enterprise Linux release 8.5 (Ootpa)  
- **Kernel:** 4.18.0-348.el8.x86_64  
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;  
- **Parallel:** No  
- **Firmware:** Version SE5C620.86B.01.01.0004.2110190142 released Oct-2021  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 32/64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
### SPEC CPU® 2017 Integer Rate Result

**Tyrone Systems**
(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero ID1100C2R-28**
(2.40 GHz, Intel Xeon Gold 6336Y)

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

**Test Date:** Sep-2022

**Hardware Availability:** Apr-2021

**Software Availability:** May-2022

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**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
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<th>Seconds</th>
<th>Ratio</th>
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<tr>
<td>500.perlbench_r</td>
<td>96</td>
<td>656</td>
<td>233</td>
<td>654</td>
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<td>505.mcf_r</td>
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<td>276</td>
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<td>555</td>
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<td>520.omnetpp_r</td>
<td>96</td>
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<td>217</td>
<td>576</td>
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<td>579</td>
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<td>523.xalancbmk_r</td>
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<td>538</td>
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<td>695</td>
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<td>541.leela_r</td>
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<td>633</td>
<td>251</td>
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<td>258</td>
<td>616</td>
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<tr>
<td>548.exchange2_r</td>
<td>96</td>
<td>332</td>
<td>757</td>
<td>332</td>
<td>758</td>
<td>333</td>
<td>756</td>
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<tr>
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<td>96</td>
<td>552</td>
<td>188</td>
<td>550</td>
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**SPECrate®2017_int_base = 350**

**SPECrate®2017_int_peak = 360**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/jde5.0.1-32"
MALLOC_CONF = "retain:true"
```
General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Filesystem page cache synced and cleared with:
 sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the
system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the
system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the
system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on icelakespec Thu Sep 22 05:18:50 2022
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz
  2 "physical id"s (chips)
  96 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 24
siblings : 48
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23

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<td>(2.40 GHz,Intel Xeon Gold 6336Y)</td>
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</tr>
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<td>CPU2017 License: 006042</td>
<td>Test Date: Sep-2022</td>
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<tr>
<td>Test Sponsor: Netweb Pte Ltd</td>
<td>Hardware Availability: Apr-2021</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: May-2022</td>
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</tbody>
</table>

Platform Notes (Continued)

```
Model: 106
Model name: Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6336Y CPU @ 2.40GHz
Stepping: 6
CPU MHz: 2401.000
CPU max MHz: 2401.0000
CPU min MHz: 800.0000
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 36864K
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95
Flags: fpu vme de pse tsc msr pae mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperffm perf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mqa ibpb ibrs_ip.ibrs enhanced fsgsbase tsc_adjust sgx bmi1 hle avx2 smep bmi2
erms invpcid cqm rdt_a_a.xavx12f_a.xavx12f_dq rdseed adx smap avx512f fma clflushopt clwb
intel_pt avx512cd sha_ni xavx12f_a.xavx12f_dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni xavx12f_a.xavx12f_dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni xavx12f_a.xavx12f_dq rdseed adx smap avx512ifma clflushopt clwb
intel_pt avx512cd sha_ni xavx12f_a.xavx12f_dq rdseed adx smap avx512ifma clflushopt clwb
/proc/cpuinfo cache data
  cache size: 36864 KB
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
nodeno cpus: 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 0 size: 257630 MB
node 0 free: 257105 MB
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 0 size: 257630 MB
node 0 free: 257105 MB
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 0 size: 257630 MB
node 0 free: 257105 MB
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 60 61 62 63 64 65 66 67 68 69 70 71
node 0 size: 257630 MB
node 0 free: 257105 MB
From /proc/meminfo
MemTotal: 1056517660 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

(Continued on next page)
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Platform Notes (Continued)

/sbin/tuned-adm active
  Current active profile: throughput-performance
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*
s-os-release:
  NAME="Red Hat Enterprise Linux"
  VERSION="8.5 (Ootpa)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="8.5"
  PLATFORM_ID="platform:el8"
  PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
  ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
uname -a:
  Linux icelakespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 22 05:17

SPEC is set to: /home/cpu2017
Filesystem          Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   402G  212G  190G  53% /home

From /sys/devices/virtual/dmi/id
  Vendor: Tyrone_Systems
  Product: Tyrone_Camarero_IDI100C2R-28
  Product Family: Family
  Serial: 2X22462203

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

(Continued on next page)
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Test Date: Sep-2022
Hardware Availability: Apr-2021
Software Availability: May-2022

Platform Notes (Continued)

BIOS:
  BIOS Vendor:       Intel Corporation
  BIOS Version:      SE5C620.86B.01.01.0004.2110190142
  BIOS Date:         10/19/2021

(End of data from sysinfo program)

Compiler Version Notes

============================================================================================================
C       | 502.gcc_r(peak)
------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------------------------------------
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

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C       | 502.gcc_r(peak)
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C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
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------------------------------------------------------------------------------------------------------------
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
  541.leela_r(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
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------------------------------------------------------------------------------------------------------------
Fortran | 548.exchange2_r(base, peak)
------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
**Base Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

**Base Optimization Flags**

C benchmarks:
- -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- -lqkmalloc

C++ benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- -lqkmalloc

Fortran benchmarks:
- -w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- -nostandard-realloc-lhs -align array32byte -auto
- -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
- -lqkmalloc
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Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -Ofast
-ffast-math -fhto -mpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin
-standard=c89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -Ofast
-ffast-math -fhto -mpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

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Hardware Availability: Apr-2021
Software Availability: May-2022

Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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