SPEC CPU®2017 Integer Speed Result

Dell Inc. PowerEdge MX760c (Intel Xeon Platinum 8458P)

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Dec-2022
Hardware Availability: Feb-2023
Software Availability: Nov-2022

 SPECspeed®2017_int_base = 14.3
 SPECspeed®2017_int_peak = 14.5

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
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<td>gcc</td>
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<td>mcf</td>
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<td>omnetpp</td>
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<tr>
<td>xalancbmk</td>
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<td>x264</td>
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<td>exchange2</td>
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</tr>
<tr>
<td>xz</td>
<td>88</td>
<td>25.6</td>
<td></td>
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</tbody>
</table>

Hardware

CPU Name: Intel Xeon Platinum 8458P
Max MHz: 3800
Nominal: 2700
Enabled: 88 cores, 2 chips
Orderable: 1.2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 82.5 MB I+D on chip per chip
Other: None
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 125 GB on tmpfs
Other: None

Software

OS: Red Hat Enterprise Linux 8.7 (Ootpa)
Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Version 0.3.2 released Nov-2022
File System: tmpfs
System State: Run level 5 (graphical multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
# SPEC CPU®2017 Integer Speed Result

## Dell Inc.

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**SPECspeed®2017_int_base = 14.3**

**SPECspeed®2017_int_peak = 14.5**

### Results Table

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<tr>
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<th>Seconds</th>
<th>Ratio</th>
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<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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SPECSpeed®2017_int_base = 14.3

SPECSpeed®2017_int_peak = 14.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = 
  "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default

(Continued on next page)
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
- ADDDC Setting: Disabled
- DIMM Self Healing on
- Uncorrectable Memory Error: Disabled
- Virtualization Technology: Disabled
- Logical Processor: Disabled
- Sub NUMA Cluster: 2-way Clustering
- DCU Streamer Prefetcher: Disabled
- LLC Prefetch: Disabled
- Dead Line LLC Alloc: Disabled
- Optimizer Mode: Enabled
- System Profile: Custom
- CPU Power Management: Maximum Performance
- C1E: Disabled
- C States: Autonomous
- Memory Patrol Scrub: Disabled
- Energy Efficiency Policy: Performance
- PCI ASPM L1 Link
- Power Management: Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d
running on localhost.localdomain Mon Dec 5 23:14:56 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
- model name: Intel(R) Xeon(R) Platinum 8458P
- 2 "physical id"s (chips)
- 88 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores: 44
siblings: 44
physical 0: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29
30 31 32 33 34 35 36 37 38 39 40 41 42 43
physical 1: cores 0 1 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29
(Continued on next page)
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PowerEdge MX760c (Intel Xeon Platinum 8458P)

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SPECspeed®2017_int_base = 14.3

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Platform Notes (Continued)

30 31 32 33 34 35 36 37 38 39 40 41 42 43

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 88
On-line CPU(s) list: 0-87
Thread(s) per core: 1
Core(s) per socket: 44
Socket(s): 2
NUMA node(s): 4
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
CPU family: 6
Model: 143
Model name: Intel(R) Xeon(R) Platinum 8458P
BIOS Model name: Intel(R) Xeon(R) Platinum 8458P
Stepping: 8
CPU MHz: 2700.000
BogoMIPS: 5400.00
L1d cache: 48K
L1i cache: 32K
L2 cache: 2048K
L3 cache: 84480K
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68,72,76,80,84
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70,74,78,82,86
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69,73,77,81,85

Flags: fpu vme de pse tsc msr pae mce cmov pat pse36 clflush dts acpi smmarton mce cmov pat pse36 clflush dts acpi smmarton mce cmov

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68 72 76 80 84
node 0 size: 257198 MB
node 0 free: 256401 MB
node 1 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86
node 1 size: 258042 MB
node 1 free: 256565 MB
node 2 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69 73 77 81 85
node 2 size: 258042 MB
node 2 free: 250226 MB

(Continued on next page)
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---

**Platform Notes (Continued)**

node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71 75 79 83 87
node 3 size: 258040 MB
node 3 free: 257641 MB
node distances:
node 0 1 2 3
  0: 10 12 21 21
  1: 12 10 21 21
  2: 21 21 10 12
  3: 21 21 12 10

From /proc/meminfo
MemTotal: 1056075948 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
o-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.7 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.7"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.7 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.7 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.7 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
uname -a:
Linux localhost.localdomain 4.18.0-425.3.1.el8.x86_64 #1 SMP Fri Sep 30 11:45:06 EDT 2022 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Not affected
- mmi0_stale_data: Not affected
- retbleed: Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapsgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling, PBRSB—IBRS: SW sequence
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Dec 5 23:03

---

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2022.1
Filesystem Type Size Used Avail Use% Mounted on
tmpfs tmpfs 125G 3.6G 122G 3% /mnt/ramdisk

(Continued on next page)
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---

**Platform Notes (Continued)**

From /sys/devices/virtual/dmi/id

Vendor: Dell Inc.
Product: PowerEdge MX760c
Product Family: PowerEdge
Serial: MWFPG04

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 16x 002C0632002C MTC40F204G611RC48BA1 64 GB 2 rank 4800

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 0.3.2
- BIOS Date: 11/30/2022
- BIOS Revision: 0.3

(End of data from sysinfo program)

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**Compiler Version Notes**

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<th>C</th>
<th>600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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<tr>
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**Base Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

(Continued on next page)
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Base Compiler Invocation (Continued)

Fortran benchmarks:  
ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64  
602.gcc_s: -DSPEC_LP64  
605.mcf_s: -DSPEC_LP64  
620.omnetpp_s: -DSPEC_LP64  
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX  
625.x264_s: -DSPEC_LP64  
631.deepsjeng_s: -DSPEC_LP64  
641.leela_s: -DSPEC_LP64  
648.exchange2_s: -DSPEC_LP64  
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:  
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=ssse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:  
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=ssse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:  
-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=ssse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:  
icx
Dell Inc. PowerEdge MX760c (Intel Xeon Platinum 8458P)  

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Peak Compiler Invocation (Continued)

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

Peak Portability Flags
Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes

(Continued on next page)
Dell Inc.
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<td>Nov-2022</td>
</tr>
</tbody>
</table>

Peak Optimization Flags (Continued)

631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:
648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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