Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8452Y)

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Test Date:** Dec-2022

**Tested by:** Dell Inc.

**Hardware Availability:** Feb-2023

**Software Availability:** Jun-2022

---

### SPEC CPU®2017 Integer Speed Result

**SPECspeed®2017_int_base = 11.0**

**SPECspeed®2017_int_peak = 11.2**

---

**Threads**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>72</td>
<td>7.61</td>
<td>6.78</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>72</td>
<td>9.38</td>
<td>9.87</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>72</td>
<td>9.05</td>
<td>17.2</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>72</td>
<td>20.5</td>
<td>16.4</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>72</td>
<td>15.9</td>
<td>16.4</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>72</td>
<td>5.36</td>
<td>16.4</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>72</td>
<td>4.16</td>
<td>21.2</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>72</td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>72</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

### Hardware

- **CPU Name:** Intel Xeon Platinum 8452Y
- **Max MHz:** 3200
- **Nominal:** 2000
- **Enabled:** 72 cores, 2 chips
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 2 MB I+D on chip per core
- **L3:** 67.5 MB I+D on chip per chip
- **Other:** None
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
- **Storage:** 125 GB on tmpfs
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
- **Compiler:** C/C++; Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
  Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- **Parallel:** Yes
- **Firmware:** Version 0.3.1 released Nov-2022
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8452Y)

**SPEC CPU®2017 Integer Speed Result**

Copyright 2017-2024 Standard Performance Evaluation Corporation

---

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Dec-2022  
**Hardware Availability:** Feb-2023  
**Software Availability:** Jun-2022

---

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Base</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Peak</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>72</td>
<td>261</td>
<td>6.79</td>
<td>262</td>
<td>72</td>
<td>233</td>
<td>7.62</td>
<td>233</td>
<td>7.61</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>72</td>
<td>425</td>
<td>9.38</td>
<td>424</td>
<td>72</td>
<td>403</td>
<td>9.87</td>
<td>404</td>
<td>9.87</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>72</td>
<td>274</td>
<td>17.2</td>
<td>274</td>
<td>72</td>
<td>274</td>
<td>17.2</td>
<td>274</td>
<td>17.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>72</td>
<td>180</td>
<td>9.05</td>
<td>180</td>
<td>72</td>
<td>180</td>
<td>9.05</td>
<td>180</td>
<td>9.08</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>72</td>
<td>69.2</td>
<td>20.5</td>
<td>68.7</td>
<td>72</td>
<td>69.2</td>
<td>20.5</td>
<td>68.7</td>
<td>20.6</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>625.x264_s</td>
<td>72</td>
<td>111</td>
<td>15.9</td>
<td>111</td>
<td>72</td>
<td>108</td>
<td>16.4</td>
<td>108</td>
<td>16.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>72</td>
<td>267</td>
<td>5.36</td>
<td>267</td>
<td>72</td>
<td>267</td>
<td>5.36</td>
<td>267</td>
<td>5.36</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>641.leela_s</td>
<td>72</td>
<td>410</td>
<td>4.16</td>
<td>410</td>
<td>72</td>
<td>410</td>
<td>4.16</td>
<td>410</td>
<td>4.17</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>72</td>
<td>179</td>
<td>16.4</td>
<td>179</td>
<td>72</td>
<td>179</td>
<td>16.4</td>
<td>179</td>
<td>16.4</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>72</td>
<td>291</td>
<td>21.2</td>
<td>292</td>
<td>72</td>
<td>291</td>
<td>21.2</td>
<td>292</td>
<td>21.2</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**SPECspeed®2017_int_base = 11.0**  
**SPECspeed®2017_int_peak = 11.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

---

### Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

---

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

---

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

- **KMP_AFFINITY** = "granularity=fine,scatter"
- **LD_LIBRARY_PATH** = "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/je5.0.1-64"
- **MALLOC_CONF** = "retain:true"
- **OMP_STACKSIZE** = "192M"

---

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default

(Continued on next page)
Dell Inc.
PowerEdge R760 (Intel Xeon Platinum 8452Y)

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

General Notes (Continued)

Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3 > /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
  ADDDC Setting : Disabled
  DIMM Self Healing on
  Uncorrectable Memory Error : Disabled
  Virtualization Technology : Disabled
  Logical Processor : Disabled
  Sub NUMA Cluster : 2-way Clustering
  DCU Streamer Prefetcher : Disabled
  LLC Prefetch : Disabled
  Dead Line LLC Alloc : Disabled
  Optimizer Mode : Enabled
  System Profile : Custom
  CPU Power Management : Maximum Performance
  CIE : Disabled
  C States : Autonomous
  Memory Patrol Scrub : Disabled
  Energy Efficiency Policy : Performance
  PCI ASPM L1 Link
  Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d
running on localhost Thu Dec  8 10:10:57 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
  https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Platinum 8452Y
  2 "physical id"s (chips)
  72 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
  excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 36
  siblings : 36
  physical 0: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
   26 27 28 29 30 31 32 33 34 35
  physical 1: cores 0 1 2 3 4 5 6 7 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
(Continued on next page)
SPEC CPU®2017 Integer Speed Result

Dell Inc.
PowerEdge R760 (Intel Xeon Platinum 8452Y)

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

SPECspeed®2017_int_base = 11.0
SPECspeed®2017_int_peak = 11.2

Platform Notes (Continued)

26 27 28 29 30 31 32 33 34 35

From lscpu from util-linux 2.37.2:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 72
On-line CPU(s) list: 0-71
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Platinum 8452Y
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 36
Socket(s): 2
Stepping: 8
BogoMIPS: 4000.00

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr mshr pmxs ami stpm mtrunc ss movbe popcnt p_LVL1

L1d cache: 3.4 MiB (72 instances)
L1i cache: 2.3 MiB (72 instances)
L2 cache: 134 MiB (72 instances)
L3 cache: 135 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71

Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Mceliece: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srdb: Not affected
Vulnerability Txs async abort: Not affected

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 48K 3.4M 12 Data 1 64 1 64
L1i 32K 2.3M 8 Instruction 1 64 1 64
L2 2M 144M 16 Unified 2 2048 1 64

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.
PowerEdge R760 (Intel Xeon Platinum 8452Y)  

| SPECspeed®2017_int_base = 11.0 | SPECspeed®2017_int_peak = 11.2 |

CPU2017 License: 6573 Test Date: Dec-2022
Test Sponsor: Dell Inc. Hardware Availability: Feb-2023
Tested by: Dell Inc. Software Availability: Jun-2022

Platform Notes (Continued)

L3  67.5M  135M  15 Unified  3 73728  1  64

/proc/cpuinfo cache data
cache size : 69120 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 4 8 12 16 20 24 28 32 36 40 44 48 52 56 60 64 68
node 0 size: 257495 MB
node 0 free: 249240 MB
node 1 cpus: 2 6 10 14 18 22 26 30 34 38 42 46 50 54 58 62 66 70
node 1 size: 258008 MB
node 1 free: 257710 MB
node 2 cpus: 1 5 9 13 17 21 25 29 33 37 41 45 49 53 57 61 65 69
node 2 size: 258043 MB
node 2 free: 257671 MB
node 3 cpus: 3 7 11 15 19 23 27 31 35 39 43 47 51 55 59 63 67 71
node 3 size: 258014 MB
node 3 free: 257694 MB
node distances:
node   0   1   2   3
0:  10  12  21  21
1:  12  10  21  21
2:  21  21  10  12
3:  21  21  12  10

From /proc/meminfo
MemTotal: 1056319776 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*
os-release:
NAME="SLES"
VERSION="15-SP4"
VERSION_ID="15.4"
PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
ID="sles"
ID_LIKE="suse"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:15:sp4"

uname -a:
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
CVE-2017-5754 (Meltdown): Mitigation: Enhanced IBRS, IBPB:
CVE-2017-5715 (Spectre variant 2):

(Continued on next page)
**Dell Inc.**

PowerEdge R760 (Intel Xeon Platinum 8452Y)

---

**Platform Notes (Continued)**

<table>
<thead>
<tr>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>conditional, RSB filling</td>
</tr>
<tr>
<td>CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected</td>
</tr>
<tr>
<td>CVE-2019-11135 (TSX Asynchronous Abort): Not affected</td>
</tr>
<tr>
<td>run-level 3 Dec 8 10:08</td>
</tr>
<tr>
<td>SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-lc2022.1</td>
</tr>
<tr>
<td>Filesystem</td>
</tr>
<tr>
<td>tmppfs</td>
</tr>
<tr>
<td>/mnt/ramdisk</td>
</tr>
<tr>
<td>From /sys/devices/virtual/dmi/id</td>
</tr>
<tr>
<td>Vendor: Dell Inc.</td>
</tr>
<tr>
<td>Product: PowerEdge R760</td>
</tr>
<tr>
<td>Product Family: PowerEdge</td>
</tr>
</tbody>
</table>

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x 00AD00B300AD HMCG94MEBRA123N 64 GB 2 rank 4800

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 0.3.1
- BIOS Date: 11/24/2022
- BIOS Revision: 0.3

(End of data from sysinfo program)

---

**Compiler Version Notes**

C
- 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
- 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++
- 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
- 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran
- 648.exchange2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
## SPEC CPU®2017 Integer Speed Result

### Dell Inc.
**PowerEdge R760 (Intel Xeon Platinum 8452Y)**

<table>
<thead>
<tr>
<th>CPU2017 License: 6573</th>
<th>Test Date: Dec-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Dell Inc.</td>
<td>Hardware Availability: Feb-2023</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Jun-2022</td>
</tr>
</tbody>
</table>

### SPECspeed®2017 Int Base Result
- `SPECspeed®2017_int_base = 11.0`

### SPECspeed®2017 Int Peak Result
- `SPECspeed®2017_int_peak = 11.2`

## Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifx

## Base Portability Flags

- `600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64`
- `602.gcc_s: -DSPEC_LP64`
- `605.mcf_s: -DSPEC_LP64`
- `620.omnetpp_s: -DSPEC_LP64`
- `623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX`
- `625.x264_s: -DSPEC_LP64`
- `631.deepsjeng_s: -DSPEC_LP64`
- `641.leela_s: -DSPEC_LP64`
- `648.exchange2_s: -DSPEC_LP64`
- `657.xz_s: -DSPEC_LP64`

## Base Optimization Flags

**C benchmarks:**
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`
- `-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**C++ benchmarks:**
- `-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**Fortran benchmarks:**
- `-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`
- `-nostandard-realloc-lhs -align array32byte`
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`
**SPEC CPU®2017 Integer Speed Result**

Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8452Y)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base = 11.0</th>
<th>SPECspeed®2017_int_peak = 11.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Date: Dec-2022</td>
<td>Hardware Availability: Feb-2023</td>
</tr>
<tr>
<td>Tested by: Dell Inc.</td>
<td>Software Availability: Jun-2022</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

---

**Peak Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

---

**Peak Portability Flags**

Same as Base Portability Flags

---

**Peak Optimization Flags**

C benchmarks:
- 600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile=generate(pass 1)
- fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

- 602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile=generate(pass 1)
- fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 605.mcf_s: basepeak = yes

- 625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

- 657.xz_s: basepeak = yes

**C++ benchmarks:**

(Continued on next page)
Dell Inc.
PowerEdge R760 (Intel Xeon Platinum 8452Y)

SPECspeed\textsuperscript{2017} \textit{int\_peak} = 11.2
SPECspeed\textsuperscript{2017} \textit{int\_base} = 11.0

Peak Optimization Flags (Continued)

620.omnetpp\_s: basepeak = yes
623.xalancbmk\_s: basepeak = yes
631.deepsjeng\_s: basepeak = yes
641.leela\_s: basepeak = yes
Fortran benchmarks:
648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU\textsuperscript{2017} v1.1.8 on 2022-12-07 21:10:56-0500.
Originally published on 2023-01-17.