### Dell Inc.

PowerEdge R760 (Intel Xeon Platinum 8471N)  

<table>
<thead>
<tr>
<th>SPEC CPU®2017 Floating Point Speed Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_fp_base = 225</td>
</tr>
<tr>
<td>SPECspeed®2017_fp_peak = 225</td>
</tr>
</tbody>
</table>

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#### SPECspeed®2017_fp_base (225)  

<table>
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<th>SPECspeed®2017_fp_peak (225)</th>
</tr>
</thead>
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<tr>
<td>Threads</td>
</tr>
<tr>
<td>---------</td>
</tr>
<tr>
<td>603.bwaves_s</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
</tr>
<tr>
<td>619.lbm_s</td>
</tr>
<tr>
<td>621.wrf_s</td>
</tr>
<tr>
<td>627.cam4_s</td>
</tr>
<tr>
<td>628.pop2_s</td>
</tr>
<tr>
<td>638.imagick_s</td>
</tr>
<tr>
<td>644.nab_s</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
</tr>
<tr>
<td>654.roms_s</td>
</tr>
</tbody>
</table>

#### Hardware

- **CPU Name:** Intel Xeon Platinum 8471N  
- **Max MHz:** 3600  
- **Nominal:** 1800  
- **Enabled:** 52 cores, 1 chip  
- **Orderable:** 1 chip  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **L2:** 2 MB I+D on chip per core  
- **L3:** 97.5 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 512 GB (8 x 64 GB 2Rx4 PC5-4800B-R)  
- **Storage:** 125 GB on tmpfs  
- **Other:** None

#### Software

- **OS:** SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
- **Compiler:** C/C++; Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- **Parallel:** Yes  
- **Firmware:** Version 0.3.2 released Nov-2022  
- **File System:** tmpfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
SPEC CPU®2017 Floating Point Speed Result

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Results Table

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<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
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<tr>
<td>603.bwaves_s</td>
<td>52</td>
<td>101</td>
<td>582</td>
<td>102</td>
<td>579</td>
<td>52</td>
<td>102</td>
<td>579</td>
<td>52</td>
<td>580</td>
<td>52</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>52</td>
<td>56.1</td>
<td>297</td>
<td>55.8</td>
<td>299</td>
<td>52</td>
<td>56.1</td>
<td>297</td>
<td>55.8</td>
<td>299</td>
<td>52</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>52</td>
<td>32.6</td>
<td>161</td>
<td>32.7</td>
<td>160</td>
<td>52</td>
<td>32.6</td>
<td>161</td>
<td>32.7</td>
<td>160</td>
<td>52</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>52</td>
<td>74.4</td>
<td>178</td>
<td>74.4</td>
<td>178</td>
<td>52</td>
<td>74.4</td>
<td>178</td>
<td>74.4</td>
<td>178</td>
<td>52</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>52</td>
<td>59.3</td>
<td>149</td>
<td>59.3</td>
<td>149</td>
<td>52</td>
<td>59.3</td>
<td>149</td>
<td>59.3</td>
<td>149</td>
<td>52</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>52</td>
<td>132</td>
<td>89.8</td>
<td>134</td>
<td>88.7</td>
<td>52</td>
<td>132</td>
<td>89.8</td>
<td>134</td>
<td>88.7</td>
<td>52</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>52</td>
<td>29.4</td>
<td>491</td>
<td>29.6</td>
<td>487</td>
<td>52</td>
<td>29.4</td>
<td>491</td>
<td>29.6</td>
<td>487</td>
<td>52</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>52</td>
<td>39.6</td>
<td>442</td>
<td>39.6</td>
<td>442</td>
<td>52</td>
<td>39.6</td>
<td>442</td>
<td>39.6</td>
<td>442</td>
<td>52</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>52</td>
<td>92.4</td>
<td>98.6</td>
<td>93.1</td>
<td>97.9</td>
<td>52</td>
<td>92.4</td>
<td>98.6</td>
<td>93.1</td>
<td>97.9</td>
<td>52</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>52</td>
<td>64.1</td>
<td>246</td>
<td>64.2</td>
<td>245</td>
<td>52</td>
<td>64.1</td>
<td>246</td>
<td>64.2</td>
<td>245</td>
<td>52</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 225  
SPECspeed®2017_fp_peak = 225

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
- ADDDC Setting : Disabled
- DIMM Self Healing on
- Uncorrectable Memory Error : Disabled
- Virtualization Technology : Disabled
- Logical Processor : Disabled
- Sub NUMA Cluster : 2-way Clustering
- DCU Streamer Prefetcher : Disabled
- LLC Prefetch : Disabled
- Dead Line LLC Alloc : Disabled
- Optimizer Mode : Enabled

- System Profile : Custom
- CPU Power Management : Maximum Performance
  - C1E : Disabled
  - C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- PCI ASPM L1 Link
- Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
running on localhost Tue Dec 13 11:34:51 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) Platinum 8471N
- 1 "physical id"s (chips)
- 52 "processors" cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
- cpu cores : 52

(Continued on next page)
Dell Inc. PowerEdge R760 (Intel Xeon Platinum 8471N)

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SPECspeed®2017_fp_peak = 225

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Test Date: Dec-2022
Hardware Availability: Feb-2023
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Software Availability: Jun-2022

Platform Notes (Continued)

siblings : 52
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51

From lscpu from util-linux 2.37.2:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 52
On-line CPU(s) list: 0-51
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Platinum 8471N
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 52
Socket(s): 1
Stepping: 8
BogoMIPS: 3600.00
Flags: fpv mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 kpm cpixtf est tm2
sse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt

(Continued on next page)
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Platform Notes (Continued)

prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbd: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

<table>
<thead>
<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1d</td>
<td>48K</td>
<td>2.4M</td>
<td>12</td>
<td>Data</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L1i</td>
<td>32K</td>
<td>1.6M</td>
<td>8</td>
<td>Instruction</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L2</td>
<td>2M</td>
<td>104M</td>
<td>16</td>
<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>97.5M</td>
<td>97.5M</td>
<td>15</td>
<td>Unified</td>
<td>3</td>
<td>106496</td>
<td>1</td>
<td>64</td>
</tr>
</tbody>
</table>

/proc/cpuinfo cache data
  cache size : 99840 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
  node 0 size: 257493 MB
  node 0 free: 243188 MB
  node 1 cpus: 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51
  node 1 size: 257987 MB
  node 1 free: 256827 MB
  node distances:
  node 0:  10  12
  node 1:  12  10

From /proc/meminfo
  MemTotal:       527852404 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15-SP4"
    VERSION_ID="15.4"
    PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"

(Continued on next page)
Dell Inc.

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### Platform Notes (Continued)

CPE_NAME="cpe:/o:suse:sles:15:sp4"

```bash
uname -a:
    Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- Microarchitectural Data Sampling: Not affected
- CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
- CVE-2017-5753 (Spectre variant 1):
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 13 08:20

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-ic2022.1

```
From /sys/devices/virtual/dmi/id
Vendor: Dell Inc.
Product: PowerEdge R760
Product Family: PowerEdge
Serial: SLR7602
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
8x 00CE00B300CE M321R8GA0BB0–CQKEG 64 GB 2 rank 4800

BIOS:
- BIOS Vendor: Dell Inc.
- BIOS Version: 0.3.2
- BIOS Date: 11/30/2022
- BIOS Revision: 0.3

(Continued on next page)
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Platform Notes (Continued)

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
-----------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
==============================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)
| 654.roms_s(base, peak)
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------
==============================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
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Compiler Version Notes (Continued)
____________________________________________

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
   -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

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### Base Optimization Flags (Continued)

Fortran benchmarks (continued):
- `nostandard-realloc-lhs` -align array32byte -auto
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
- `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math` `flto`
- `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-fiopenmp`
- `-DSPEC_OPENMP` `nostandard-realloc-lhs` `-align array32byte` `-auto`
  `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Benchmarks using Fortran, C, and C++:
- `-m64` `-std=c11` `-Wl,-z,muldefs` `-xCORE-AVX512` `-Ofast` `-ffast-math` `flto`
- `-mfpmath=sse` `-funroll-loops` `-qopt-mem-layout-trans=4` `-fiopenmp`
- `-DSPEC_OPENMP` `nostandard-realloc-lhs` `-align array32byte` `-auto`
  `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

### Peak Compiler Invocation

**C benchmarks:**
- `icx`

**Fortran benchmarks:**
- `ifx`

Benchmarks using both Fortran and C:
- `ifx` `icx`

Benchmarks using Fortran, C, and C++:
- `icpx` `icx` `ifx`

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

**C benchmarks:**

- `619.lbm_s: basepeak = yes`

(Continued on next page)
### Peak Optimization Flags (Continued)

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

**Fortran benchmarks:**


649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

**Benchmarks using both Fortran and C:**

621.wrf_s: basepeak = yes

627.cam4_s: basepeak = yes

628.pop2_s: basepeak = yes

**Benchmarks using Fortran, C, and C++:**

607.cactuBSSN_s: basepeak = yes

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The flags files that were used to format this result can be browsed at:


http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html

You can also download the XML flags sources by saving the following links:


http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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