# SPEC CPU®2017 Floating Point Speed Result

## Dell Inc.

**PowerEdge R760 (Intel Xeon Gold 6454S)**

**SPECspeed®2017_fp_base = 271**

**SPECspeed®2017_fp_peak = 272**

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_fp_base (271)</th>
<th>SPECspeed®2017_fp_peak (272)</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s 64</td>
<td>315</td>
<td></td>
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<tr>
<td>607.cactuBSSN_s 64</td>
<td></td>
<td></td>
</tr>
<tr>
<td>619.lbm_s 64</td>
<td>221</td>
<td></td>
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<tr>
<td>621.wrf_s 64</td>
<td>189</td>
<td></td>
</tr>
<tr>
<td>627.cam4_s 64</td>
<td>157</td>
<td>160</td>
</tr>
<tr>
<td>628.pop2_s 64</td>
<td>67.9</td>
<td></td>
</tr>
<tr>
<td>638.imagick_s 64</td>
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<td>540</td>
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<tr>
<td>644.nab_s 64</td>
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<td>514</td>
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<tr>
<td>649.fotonik3d_s 64</td>
<td>139</td>
<td></td>
</tr>
<tr>
<td>654.roms_s 64</td>
<td></td>
<td>372</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6454S
- **Max MHz:** 3400
- **Nominal:** 2200
- **Enabled:** 64 cores, 2 chips
- **Orderable:** 1,2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 2 MB I+D on chip per core
- **L3:** 60 MB I+D on chip per chip
- **Other:** None
- **Memory:** 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)
- **Storage:** 125 GB on tmpfs
- **Other:** None

### Software

- **OS:** SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- **Parallel:** Yes
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
**Results Table**

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
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<tr>
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<td>1080</td>
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<tr>
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<tr>
<td>619.lbm_s</td>
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<td>627.cam4_s</td>
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<td>638.imagick_s</td>
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<tr>
<td>644.nab_s</td>
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<td>391</td>
<td>42.3</td>
<td>372</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.8-ic2022.1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3 > /proc/sys/vm/drop_caches
```

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5


NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)
Dell Inc.  
PowerEdge R760 (Intel Xeon Gold 6454S)  

SPECspeed®2017_fp_base = 271  
SPECspeed®2017_fp_peak = 272

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:

- ADDDC Setting : Disabled
- DIMM Self Healing on
- Uncorrectable Memory Error : Disabled
- Virtualization Technology : Disabled
- Logical Processor : Disabled
- Sub NUMA Cluster : 2-way Clustering
- DCU Streamer Prefetcher : Disabled
- LLC Prefetch : Disabled
- Dead Line LLC Alloc : Disabled
- Optimizer Mode : Enabled
- System Profile : Custom
- CPU Power Management : Maximum Performance
  - C1E : Disabled
  - C States : Autonomous
- Memory Patrol Scrub : Disabled
- Energy Efficiency Policy : Performance
- PCI ASPM L1 Link
- Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 9b82a61ec0915b55891ef0e16aca9c64d
running on localhost Wed Dec 14 09:56:29 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

- model name : Intel(R) Xeon(R) Gold 6454S
- 2 "physical id"s (chips)
- 64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

- cpu cores : 32

(Continued on next page)
Dell Inc.
PowerEdge R760 (Intel Xeon Gold 6454S)

**SPEC CPU®2017 Floating Point Speed Result**

**SPECspeed®2017_fp_base = 271**
**SPECspeed®2017_fp_peak = 272**

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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<tbody>
<tr>
<td>CPU2017 License</td>
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<tr>
<td>Test Sponsor</td>
<td>Dell Inc.</td>
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<td>Tested by</td>
<td>Dell Inc.</td>
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<td>Test Date</td>
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<td>Hardware Availability</td>
<td>Feb-2023</td>
</tr>
<tr>
<td>Software Availability</td>
<td>Jun-2022</td>
</tr>
</tbody>
</table>

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**Platform Notes (Continued)**

siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

From lscpu from util-linux 2.37.2:

Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Gold 6454S
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
Stepping: 8
BogoMIPS: 4400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault ebpxcat_l13 csvcat_l12 csvcpid_single csvcpd_l12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 .crmv interrupts rdenvclt crm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprec xgetbv1 xsavec xsaveopt xsavea xsaveopt xsaveprechrace

L1d cache: 3 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 128 MiB (64 instances)
L3 cache: 120 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-15
NUMA node1 CPU(s): 16-31
NUMA node2 CPU(s): 32-47
NUMA node3 CPU(s): 48-63
Vulnerability Itlb multihit: Not affected

(Continued on next page)
Dell Inc.

PowerEdge R760 (Intel Xeon Gold 6454S)

SPECspeed®2017 fp_base = 271
SPECspeed®2017 fp_peak = 272

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Platform Notes (Continued)

Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d 48K 3M 12 Data 1 64 1 64
L1i 32K 2M 8 Instruction 1 64 1 64
L2 2M 128M 16 Unified 2 2048 1 64
L3 60M 120M 15 Unified 3 65536 1 64

/proc/cpuinfo cache data
cache size: 61440 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 128472 MB
node 0 free: 120182 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 size: 128985 MB
node 1 free: 128811 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 2 size: 129019 MB
node 2 free: 128837 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 3 size: 128991 MB
node 3 free: 122291 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10

From /proc/meminfo
MemTotal: 527839816 KB
 HugePages_Total: 0
SPEC CPU®2017 Floating Point Speed Result

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PowerEdge R760 (Intel Xeon Gold 6454S)  

**SPECspeed®2017_fp_base** = 271  
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- **CPU2017 License:** 6573  
- **Test Sponsor:** Dell Inc.  
- **Test Date:** Dec-2022  
- **Tested by:** Dell Inc.  
- **Hardware Availability:** Feb-2023  
- **Software Availability:** Jun-2022

---

### Platform Notes (Continued)

- **Hugepagesize:** 2048 kB

From /etc/*release* /etc/*version*  

```plaintext
os-release:
  NAME="SLES"
  VERSION="15-SP4"
  VERSION_ID="15.4"
  PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
  ID="sles"
  ID_LIKE="suse"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:15:sp4"
```

```plaintext
uname -a:
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux
```

**Kernel self-reported vulnerability status:**

- **CVE-2018-12207 (iTLB Multihit):** Not affected
- **CVE-2018-3620 (L1 Terminal Fault):** Not affected
- **Microarchitectural Data Sampling:** Not affected
- **CVE-2017-5754 (Meltdown):** Not affected
- **CVE-2018-3639 (Speculative Store Bypass):** Mitigation: Speculative Store Bypass disabled via prctl and seccomp

- **CVE-2017-5753 (Spectre variant 1):** Mitigation: usercopy/swapsps barriers and __user pointer sanitation
- **CVE-2017-5715 (Spectre variant 2):** Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- **CVE-2020-0543 (Special Register Buffer Data Sampling):** Not affected
- **CVE-2019-11135 (TSX Asynchronous Abort):** Not affected

**run-level 3 Dec 14 06:54**

**SPEC is set to:** /mnt/ramdisk/cpu2017-1.1.8-ic2022.1

<table>
<thead>
<tr>
<th>Filesystem</th>
<th>Type</th>
<th>Size</th>
<th>Used</th>
<th>Avail</th>
<th>Use%</th>
<th>Mounted on</th>
</tr>
</thead>
<tbody>
<tr>
<td>tmpfs</td>
<td>tmpfs</td>
<td>125G</td>
<td>9.6G</td>
<td>116G</td>
<td>8%</td>
<td>/mnt/ramdisk</td>
</tr>
</tbody>
</table>

From /sys/devices/virtual/dmi/id

- **Vendor:** Dell Inc.
- **Product:** PowerEdge R760
- **Product Family:** PowerEdge
- **Serial:** SLR7601

**Additional information from dmidecode 3.2 follows.** WARNING: Use caution when you

(Continued on next page)
Dell Inc. PowerEdge R760 (Intel Xeon Gold 6454S)

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CPU2017 License: 6573
Test Sponsor: Dell Inc.
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Test Date: Dec-2022
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Platform Notes (Continued)

interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
16x 00AD063200AD HMCG88MEBRA107N 32 GB 2 rank 4800

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 0.3.2
BIOS Date: 11/30/2022
BIOS Revision: 0.3

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
| C                      |
| 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak) |
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

==============================================================================
| C++, C, Fortran         |
| 607.cactuBSSN_s(base, peak) |
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

==============================================================================
| Fortran                 |
| 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak) |
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
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CPU2017 License: 6573
Test Sponsor: Dell Inc.
Test Date: Dec-2022
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Software Availability: Jun-2022

Compiler Version Notes (Continued)

==============================================================================
Fortran, C  | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
           | 628.pop2_s(base, peak)
==============================================================================
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
==============================================================================

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.

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Base Optimization Flags

C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags
SPEC CPU®2017 Floating Point Speed Result

Dell Inc.

PowerEdge R760 (Intel Xeon Gold 6454S)

SPECspeed®2017_fp_base = 271
SPECspeed®2017_fp_peak = 272

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Test Date: Dec-2022
Tested by: Dell Inc.
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

649.fotonik3d_s: basepeak = yes
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.2.xml
### SPEC CPU®2017 Floating Point Speed Result

**Dell Inc.**

**PowerEdge R760 (Intel Xeon Gold 6454S)**

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<td>272</td>
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</table>

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