## SPEC CPU®2017 Floating Point Speed Result

### ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
<tbody>
<tr>
<td>OS: Red Hat Enterprise Linux release 8.4 (Ootpa) 4.18.0-305.25.1.el8_4.x86_64</td>
<td>CPU Name: Intel Xeon Platinum 8362</td>
</tr>
<tr>
<td>Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;</td>
<td>Max MHz: 3600</td>
</tr>
<tr>
<td>Parallel: Yes</td>
<td>Nominal: 2800</td>
</tr>
<tr>
<td>Firmware: Version 0802 released Apr-2022</td>
<td>Enabled: 64 cores, 2 chips</td>
</tr>
<tr>
<td>File System: xfs</td>
<td>Orderable: 1, 2 chip(s)</td>
</tr>
<tr>
<td>System State: Run level 3 (multi-user)</td>
<td>Cache L1: 32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>Base Pointers: 64-bit</td>
<td>L2: 1.25 MB I+D on chip per core</td>
</tr>
<tr>
<td>Peak Pointers: 64-bit</td>
<td>L3: 48 MB I+D on chip per core</td>
</tr>
<tr>
<td>Other: None</td>
<td>Other: None</td>
</tr>
<tr>
<td>Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.</td>
<td>Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)</td>
</tr>
<tr>
<td>Storage: 1 x 1 TB SATA SSD</td>
<td>Storage: None</td>
</tr>
</tbody>
</table>

**SPECspeed®2017_fp_base = 270**

**SPECspeed®2017_fp_peak = 270**

---

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<table>
<thead>
<tr>
<th>Test Date: Dec-2022</th>
<th>Hardware Availability: Apr-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tested by: ASUSTeK Computer Inc.</td>
<td>Software Availability: May-2022</td>
</tr>
<tr>
<td>Test Sponsor: ASUSTeK Computer Inc.</td>
<td>CPU2017 License: 9016</td>
</tr>
</tbody>
</table>

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### Threads

<table>
<thead>
<tr>
<th>Specmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
</tr>
<tr>
<td>619.ibm_s</td>
<td>64</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>64</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>64</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>64</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>64</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>64</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>64</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>64</td>
</tr>
</tbody>
</table>

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### SPECspeed®2017_fp_base

<table>
<thead>
<tr>
<th>SPECmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>270</td>
<td></td>
</tr>
</tbody>
</table>

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### SPECspeed®2017_fp_peak

<table>
<thead>
<tr>
<th>SPECmark</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>270</td>
<td></td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Tests</th>
<th>Value</th>
</tr>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
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### Hardware

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<tbody>
<tr>
<td>CPU Name:</td>
<td>Intel Xeon Platinum 8362</td>
</tr>
<tr>
<td>Max MHz:</td>
<td>3600</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2800</td>
</tr>
<tr>
<td>Enabled:</td>
<td>64 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable:</td>
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<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
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<tr>
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</tr>
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<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
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ASUSTeK Computer Inc.

ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

CPU2017 License: 9016
Test Date: Dec-2022

Test Sponsor: ASUSTeK Computer Inc.
Hardware Availability: Apr-2022

Tested by: ASUSTeK Computer Inc.
Software Availability: May-2022

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>64</td>
<td>76</td>
<td>773</td>
<td>75.9</td>
<td>777</td>
<td>76.2</td>
<td>774</td>
<td>76.5</td>
<td>771</td>
<td>76.1</td>
<td>775</td>
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<tr>
<td>607.cactuBSSN_s</td>
<td>64</td>
<td>50</td>
<td>334</td>
<td>50.1</td>
<td>333</td>
<td>49.6</td>
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<td>333</td>
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<td>336</td>
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<tr>
<td>619.lbm_s</td>
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<td>185</td>
<td>25.3</td>
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<td>25.8</td>
<td>203</td>
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<tr>
<td>621.wrf_s</td>
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<td>627.cam4_s</td>
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<td>51.5</td>
<td>172</td>
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<td>169</td>
<td>51.5</td>
<td>172</td>
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<tr>
<td>628.pop2_s</td>
<td>64</td>
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<td>92.7</td>
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<td>93.1</td>
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<td>129</td>
<td>92.4</td>
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<tr>
<td>638.imagick_s</td>
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<td>644.nab_s</td>
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<td>32.4</td>
<td>539</td>
<td>32.3</td>
<td>541</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
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<td>121</td>
<td>75.3</td>
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<td>74.9</td>
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<td>121</td>
<td>74.9</td>
<td>122</td>
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<td>654.roms_s</td>
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<td>54.7</td>
<td>288</td>
<td>54.9</td>
<td>287</td>
<td>54.7</td>
<td>288</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 270
SPECspeed®2017_fp_peak = 270

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

- Stack size set to unlimited using "ulimit -s unlimited"
- OS set to performance mode via cpupower frequency-set -g performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,compact"
- LD_LIBRARY_PATH = "/home/ic22u1/lib/intel64:/home/ic22u1/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

General Notes

- Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
- Transparent Huge Pages enabled by default
- Prior to runcpu invocation
- Filesystem page cache synced and cleared with:
  - sync; echo 3 > /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation

(Continued on next page)
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ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
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**SPECspeed®2017_fp_base = 270**
**SPECspeed®2017_fp_peak = 270**

<table>
<thead>
<tr>
<th>CPU2017 License: 9016</th>
<th>Test Date: Dec-2022</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: ASUSTeK Computer Inc.</td>
<td>Hardware Availability: Apr-2022</td>
</tr>
<tr>
<td>Tested by: ASUSTeK Computer Inc.</td>
<td>Software Availability: May-2022</td>
</tr>
</tbody>
</table>

**General Notes (Continued)**

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

**Platform Notes**

BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
Hyper-Threading = Disable
Engine Boost = Aggressive
SR-IOV Support = Disabled
BMC Configuration:
Fan mode = Full speed mode

Sysinfo program /home/ic22u1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aca64d4
running on localhost.localdomain Fri Dec 16 13:30:01 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
2 "physical id"s (chips)
64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel

(Continued on next page)
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Test Date: Dec-2022
Hardware Availability: Apr-2022
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Platform Notes (Continued)

BIOS Vendor ID: Intel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
BIOS Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
Stepping: 6
CPU MHz: 3500.000
CPU max MHz: 3600.0000
CPU min MHz: 800.0000
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-31
NUMA noden CPU(s): 32-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invdpcid_single intel_ppln ssbd mba ibrs ibpb stibp ibrsenhanced tpr_shadow vmmi flexpriority ept vpid ept_ad fsbsbase tsc_adjust bmi1 hle avx2 smep bmi2 erts invpcid cmqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha ni avx512bw avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp epp hwp_pkg_req avx512vmbi umip pku ospke avx512 vbmi2 gfn vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld arch_capabilities

/cache data

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 0 size: 515627 MB
node 0 free: 509767 MB
node 1 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 1 size: 516086 MB
node 1 free: 512072 MB
node distances:

(Continued on next page)
Platform Notes (Continued)

node 0 1
   0: 10 20
   1: 20 10

From /proc/meminfo
   MemTotal: 1056475108 kB
   HugePages_Total: 0
   Hugepagesize: 2048 kB

/sbin/tuned-adm active
   Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.4 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.4"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
      ANSI_COLOR="0;31"
   redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
   system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga

uname -a:
   Linux localhost.localdomain 4.18.0-305.25.1.el8_4.x86_64 #1 SMP Mon Oct 18 14:34:11 EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

(Continued on next page)
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Copyright 2017-2023 Standard Performance Evaluation Corporation

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Hardware Availability: Apr-2022
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Software Availability: May-2022

Platform Notes (Continued)
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 16 05:10

SPEC is set to: /home/ic22u1

Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs 878G 125G 753G 15% /home

From /sys/devices/virtual/dmi/id
Vendor: ASUSTeK COMPUTER INC.
Product: RS720-E10-RS12
Product Family: Server
Serial: 012345678901

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 0802
BIOS Date: 04/29/2022
BIOS Revision: 8.2

(End of data from sysinfo program)

Compiler Version Notes
================================================================================
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
   644.nab_s(base, peak)
================================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
================================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
================================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316

(Continued on next page)
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Compiler Version Notes (Continued)

Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel (R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316

Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel (R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316

Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

------------------------------------------------------------------------------
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak)  
| 654.roms_s(base, peak)
------------------------------------------------------------------------------

------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316

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------------------------------------------------------------------------------
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)  
| 628.pop2_s(base, peak)
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316

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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316

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Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx
ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>9016</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>ASUSTeK Computer Inc.</td>
</tr>
<tr>
<td>Tested by</td>
<td>ASUSTeK Computer Inc.</td>
</tr>
</tbody>
</table>

**Base Portability Flags**

```
603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
   -assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64
```

**Base Optimization Flags**

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
 -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
 -nostandard-realloc-lhs -align array32byte -auto
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
 -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
 -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

**Peak Compiler Invocation**

C benchmarks:

```
icx
```

(Continued on next page)
ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_fp_base = 270
SPECspeed®2017_fp_peak = 270

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Test Date: Dec-2022
Tested by: ASUSTeK Computer Inc.
Hardware Availability: Apr-2022
Software Availability: May-2022

Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: basepeak = yes

Fortran benchmarks:
603.bwaves_s: -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc
649.fotonik3d_s: basepeak = yes
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:
621.wrf_s: basepeak = yes
627.cam4_s: basepeak = yes

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SPEC CPU®2017 Floating Point Speed Result

ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

SPECspeed®2017_fp_base = 270
SPECspeed®2017_fp_peak = 270

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.
Test Date: Dec-2022
Hardware Availability: Apr-2022
Software Availability: May-2022

Peak Optimization Flags (Continued)

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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