## SPEC CPU®2017 Integer Speed Result

**ASUSTeK Computer Inc.**

ASUS RS720-E10(Z12PP-D32) Server System (2.80 GHz, Intel Xeon Platinum 8362)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>13.0</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>13.2</td>
</tr>
</tbody>
</table>

**Threads**

<table>
<thead>
<tr>
<th>Test</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>perlbench_s</td>
<td>64</td>
</tr>
<tr>
<td>gcc_s</td>
<td>64</td>
</tr>
<tr>
<td>mcf_s</td>
<td>64</td>
</tr>
<tr>
<td>omnetpp_s</td>
<td>64</td>
</tr>
<tr>
<td>xalancbmk_s</td>
<td>64</td>
</tr>
<tr>
<td>x264_s</td>
<td>64</td>
</tr>
<tr>
<td>deepsjeng_s</td>
<td>64</td>
</tr>
<tr>
<td>leela_s</td>
<td>64</td>
</tr>
<tr>
<td>exchange2_s</td>
<td>64</td>
</tr>
<tr>
<td>xz_s</td>
<td>64</td>
</tr>
</tbody>
</table>

**Hardware**

- **CPU Name:** Intel Xeon Platinum 8362  
  - Max MHz: 3600  
  - Nominal: 2800  
  - Enabled: 64 cores, 2 chips  
  - Orderable: 1, 2 chip(s)  
  - Cache L1: 32 KB I + 48 KB D on chip per core  
  - L2: 1.25 MB I+D on chip per core  
  - L3: 48 MB I+D on chip per chip  
  - Other: None  
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 1 x 1 TB SATA SSD  
- **Other:** None

**Software**

- **OS:** Red Hat Enterprise Linux release 8.4 (Ootpa)  
  - 4.18.0-305.25.1.el8_4.x86_64  
- **Compiler:**  
  - C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
  - Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;  
- **Parallel:** Yes  
- **Firmware:** Version 0802 released Apr-2022  
- **File System:** xfs  
- **System State:** Run level 3 (multi-user)  
- **Base Pointers:** 64-bit  
- **Peak Pointers:** 64-bit  
- **Other:** jemalloc memory allocator V5.0.1  
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.
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<tr>
<th>Benchmark</th>
<th>Test Sponsor: ASUSTeK Computer Inc.</th>
<th>Tested by: ASUSTeK Computer Inc.</th>
</tr>
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</table>

<table>
<thead>
<tr>
<th>CPU2017 License: 9016</th>
<th>Test Date: Dec-2022</th>
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<tbody>
<tr>
<td>Test Sponsor: ASUSTeK Computer Inc.</td>
<td>Hardware Availability: Apr-2022</td>
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<tr>
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<td>Software Availability: May-2022</td>
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#### Results Table

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<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
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<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>64</td>
<td>236</td>
<td>7.53</td>
<td>235</td>
<td>7.54</td>
<td>236</td>
<td>7.51</td>
<td>64</td>
<td>217</td>
<td>8.20</td>
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<td>8.22</td>
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<td>602.gcc_s</td>
<td>64</td>
<td>344</td>
<td>11.6</td>
<td>344</td>
<td>11.6</td>
<td>340</td>
<td>11.7</td>
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<td>325</td>
<td>12.3</td>
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<tr>
<td>605.mcf_s</td>
<td>64</td>
<td>225</td>
<td>21.0</td>
<td>226</td>
<td>20.8</td>
<td><strong>226</strong></td>
<td><strong>20.9</strong></td>
<td>64</td>
<td>225</td>
<td>21.0</td>
<td>226</td>
<td>20.8</td>
<td><strong>226</strong></td>
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<tr>
<td>620.omnetpp_s</td>
<td>64</td>
<td>153</td>
<td>10.7</td>
<td><strong>152</strong></td>
<td><strong>10.7</strong></td>
<td>150</td>
<td>10.9</td>
<td>64</td>
<td>153</td>
<td>10.7</td>
<td><strong>152</strong></td>
<td><strong>10.7</strong></td>
<td>150</td>
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<td>623.xalancbmk_s</td>
<td>64</td>
<td><strong>65.3</strong></td>
<td><strong>21.7</strong></td>
<td>66.0</td>
<td>21.5</td>
<td>65.1</td>
<td>21.8</td>
<td>64</td>
<td><strong>65.3</strong></td>
<td><strong>21.7</strong></td>
<td>66.0</td>
<td>21.5</td>
<td>65.1</td>
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<tr>
<td>625.x264_s</td>
<td>64</td>
<td>98.9</td>
<td>17.8</td>
<td><strong>98.7</strong></td>
<td><strong>17.9</strong></td>
<td>98.6</td>
<td>17.9</td>
<td>64</td>
<td>95.5</td>
<td>18.5</td>
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<td>18.5</td>
<td><strong>95.5</strong></td>
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<td>631.deepsjeng_s</td>
<td>64</td>
<td><strong>229</strong></td>
<td><strong>6.25</strong></td>
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<td>6.25</td>
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<td><strong>6.25</strong></td>
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<td>230</td>
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<tr>
<td>641.leela_s</td>
<td>64</td>
<td>335</td>
<td>5.09</td>
<td><strong>336</strong></td>
<td><strong>5.09</strong></td>
<td>336</td>
<td>5.08</td>
<td>64</td>
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<td>5.09</td>
<td><strong>336</strong></td>
<td><strong>5.08</strong></td>
<td>336</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>64</td>
<td><strong>141</strong></td>
<td><strong>20.9</strong></td>
<td>143</td>
<td>20.6</td>
<td>141</td>
<td>20.9</td>
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<td><strong>20.9</strong></td>
<td>143</td>
<td>20.6</td>
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<tr>
<td>657.xz_s</td>
<td>64</td>
<td>234</td>
<td>26.4</td>
<td>233</td>
<td>26.6</td>
<td><strong>233</strong></td>
<td><strong>26.6</strong></td>
<td>64</td>
<td>234</td>
<td>26.4</td>
<td>233</td>
<td>26.6</td>
<td><strong>233</strong></td>
</tr>
</tbody>
</table>

**SPECspeed®2017_int_base = 13.0**  
**SPECspeed®2017_int_peak = 13.2**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

#### Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

#### Operating System Notes

Stack size set to unlimited using "ulimit –s unlimited"  
OS set to performance mode via cpupower frequency-set -g performance

#### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP_AFFINITY = "granularity=fine,scatter"  
LD_LIBRARY_PATH = "/home/ic22u1/lib/intel64:/home/ic22u1/je5.0.1-64"  
MALLOC_CONF = "retain:true"  
OMP_STACKSIZE = "192M"

#### General Notes

Binsaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
Copyright 2017-2024 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.
ASUS RS720-E10(Z12PP-D32) Server System
(2.80 GHz, Intel Xeon Platinum 8362)

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

SPECspeed®2017_int_base = 13.0
SPECspeed®2017_int_peak = 13.2

General Notes (Continued)
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3 > /proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes
BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
Hyper-Threading = Disable
Engine Boost = Aggressive
SR-IOV Support = Disabled
BMC Configuration:
Fan mode = Full speed mode
Sysinfo program /home/ic22u1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16ac6f64d
running on localhost.localdomain Fri Dec 16 09:51:44 2022
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
  2 "physical id"s (chips)
  64 "processors"
core, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 32
siblings : 32
physical 0: cores 0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24
  25,26,27,28,29,30,31
physical 1: cores 0,1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24
  25,26,27,28,29,30,31
From ls fanc from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 64
On-line CPU(s) list: 0-63
Thread(s) per core: 1
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel
CPU family: 6
Model: 106

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Test Date: Dec-2022
Hardware Availability: Apr-2022
Software Availability: May-2022

Platform Notes (Continued)

Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
BIOS Model name: Intel(R) Xeon(R) Platinum 8362 CPU @ 2.80GHz
Stepping: 6
CPU MHz: 3500.463
CPU max MHz: 3600.000
CPU min MHz: 800.0000
BogoMIPS: 5600.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-31
NUMA node1 CPU(s): 32-63
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
       lmx constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
       aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
       xtrarms pcm cda esse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer xsave
       avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat l3 invpcid_single
       intel_pnip sbbdb mb aibr aibp aibrs enhanced tpr_shadow vmni fpxpriority ept
       vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invvpid cqm rdt_a
       avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni
       avx512bw avx512vl xsaveopt xsaves xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbb_total
       cqm_mlb_local split_lock_detect wbinvd dtherm ida arat pinn pts hwp_act_window
       hwp_epp hwp_pkg_req avx512v bmi umip pku ospke avx512_vbmi2 fni vaes vptraulq dq
       avx512_vnni avx512_vbdal tme avx512_vpopcntdq la57 rdpid fsrcm_smsl_clear pconfi
       flush_l1d arch_capabilities

/proc/cpuinfo cache data
  cache size: 49152 MB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
  node 0 size: 515627 MB
  node 0 free: 513157 MB
  node distances:
    node   0   1
      0:   10  20
      1:   20  10

From /proc/meminfo
  MemTotal: 1056475108 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
  performance

From /etc/*release* /etc/*version*

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Hardware Availability: Apr-2022
Software Availability: May-2022

Platform Notes (Continued)

os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga
uname -a:
Linux localhost.localdomain 4.18.0-305.25.1.el8_4.x86_64 #1 SMP Mon Oct 18 14:34:11 EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Melttdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RBB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 16 05:10
SPEC is set to: /home/ic22u1

From /sys/devices/virtual/dmi/id
Vendor: ASUSTeK COMPUTER INC.
Product: RS720-E10-RS12
Product Family: Server
Serial: 012345678901

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200

BIOS:
BIOS Vendor: American Megatrends Inc.
BIOS Version: 0802
BIOS Date: 04/29/2022
BIOS Revision: 8.2

(End of data from sysinfo program)
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## Compiler Version Notes

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<th>Benchmark Functions</th>
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<td>C benchmarks: icx</td>
</tr>
<tr>
<td>C++</td>
<td>C++ benchmarks: icpx</td>
</tr>
<tr>
<td>Fortran</td>
<td>Fortran benchmarks: ifx</td>
</tr>
</tbody>
</table>

### Base Compiler Invocation

**C benchmarks:**
- icx

**C++ benchmarks:**
- icpx

**Fortran benchmarks:**
- ifx

### Base Portability Flags

- `600.perlbench_s`: -DSPEC_LP64 -DSPEC_LINUX_X64
- `602.gcc_s`: -DSPEC_LP64
- `605.mcf_s`: -DSPEC_LP64
- `620.omnetpp_s`: -DSPEC_LP64
- `623.xalancbmk_s`: -DSPEC_LP64 -DSPEC_LINUX
- `625.x264_s`: -DSPEC_LP64
- `631.deepsjeng_s`: -DSPEC_LP64
- `641.leela_s`: -DSPEC_LP64
- `648.exchange2_s`: -DSPEC_LP64
- `657.xz_s`: -DSPEC_LP64
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Base Optimization Flags

C benchmarks:
- -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
- DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
- -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:
- -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
- mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
- nostandard-realloc-lhs -align array32byte
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
- fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
- ffast-math -flto -mfpmath=sse -funroll-loops
- qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
- fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib
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Peak Optimization Flags (Continued)

600.perlbench_s (continued):
-lljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-ffast-math -filto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -lljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes

623.xalancbmk_s: basepeak = yes

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z12-V1.2.xml
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ASUSTeK Computer Inc.  
ASUS RS720-E10(Z12PP-D32) Server System  
(2.80 GHz, Intel Xeon Platinum 8362)  

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-12-16 09:51:44-0500.  
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