### Lenovo Global Technology

**ThinkSystem SR650 V3**  
*(2.50 GHz, Intel Xeon Gold 6426Y)*

<table>
<thead>
<tr>
<th>SPECspeed®2017_fp_base</th>
<th>= 241</th>
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<tbody>
<tr>
<td>SPECspeed®2017_fp_peak</td>
<td>= Not Run</td>
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<table>
<thead>
<tr>
<th>CPU2017 License:</th>
<th>9017</th>
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<tr>
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<td>Lenovo Global Technology</td>
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<tr>
<td>Tested by:</td>
<td>Lenovo Global Technology</td>
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#### Threads

<table>
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<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base (241)</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
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<td>274</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>32</td>
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<td>619.lbm_s</td>
<td>32</td>
<td>215</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>32</td>
<td>188</td>
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<tr>
<td>627.cam4_s</td>
<td>32</td>
<td>124</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>32</td>
<td>87.1</td>
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<td>638.imagick_s</td>
<td>32</td>
<td>443</td>
</tr>
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<td>644.nab_s</td>
<td>32</td>
<td>314</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
<td>145</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>32</td>
<td>289</td>
</tr>
</tbody>
</table>

#### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6426Y</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>4100</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2500</td>
</tr>
<tr>
<td>Enabled:</td>
<td>32 cores, 2 chips</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1.2 chips</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>2 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>37.5 MB I+D on chip per chip</td>
</tr>
<tr>
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<tr>
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<td>512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)</td>
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<tr>
<td>Storage:</td>
<td>1 x 960 GB SATA SSD</td>
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<tr>
<td>Other:</td>
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#### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>SUSE Linux Enterprise Server 15 SP4 (x86_64)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Kernel 5.14.21-150400.22-default</td>
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<tr>
<td>Compiler:</td>
<td>C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;</td>
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<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Lenovo BIOS Version ESE109G 0.83 released Dec-2022</td>
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<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
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<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
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<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage</td>
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</table>
SPEC CPU®2017 Floating Point Speed Result

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Lenovo Global Technology
ThinkSystem SR650 V3
(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
<tr>
<td>603.bwaves_s</td>
<td>32</td>
<td>66.3</td>
<td>890</td>
<td>66.6</td>
<td>886</td>
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<td>621.wrf_s</td>
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<td>70.7</td>
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<tr>
<td>627.cam4_s</td>
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<tr>
<td>628.pop2_s</td>
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</tr>
<tr>
<td>638.imagick_s</td>
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<td>445</td>
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<td>441</td>
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<tr>
<td>644.nab_s</td>
<td>32</td>
<td>52.4</td>
<td>334</td>
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<tr>
<td>649.fotonik3d_s</td>
<td>32</td>
<td>62.4</td>
<td>146</td>
<td>63.0</td>
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<td>62.7</td>
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<td>32</td>
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<td>287</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.8-ic2022.1/lib/intel64:/home/cpu2017-1.1.8-ic2022.1/j e5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>>/proc/sys/vm/drop_caches
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650 V3
(2.50 GHz, Intel Xeon Gold 6426Y)

SPECIAL CPU®2017 Floating Point Speed Result
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General Notes (Continued)
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Operating Mode set to Custom Mode
CPU P-State Control set to Legacy
Hyper-Threading set to Disabled
DCU IP Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.8-ic2022.1/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ecb915b5589e016a9acfc64d
running on localhost Thu Dec 29 16:56:19 2022

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6426Y
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 16
siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.37.2:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) Gold 6426Y
CPU family: 6
Model: 143
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s): 2
Stepping: 8

(Continued on next page)
## Lenovo Global Technology

**ThinkSystem SR650 V3**  
(2.50 GHz, Intel Xeon Gold 6426Y)

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
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<tr>
<td><strong>SPECspeed®2017_fp_base</strong></td>
<td>241</td>
</tr>
<tr>
<td><strong>SPECspeed®2017_fp_peak</strong></td>
<td>Not Run</td>
</tr>
</tbody>
</table>

---

### Platform Notes (Continued)

**Frequency boost:** enabled  
**CPU max MHz:** 2501.0000  
**CPU min MHz:** 800.0000  
**BogoMIPS:** 5000.00  
**Flags:** fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdcp cce rdtscp lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmpref tsc_known_freq pni pclmulqdq dtes64 monitor ds cpl vmx smx est tm2 ssse3 sseh smd fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single intel_pni cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vmni flexpriority ept vpid ept_ad fsgsbase tsc_adjust bts hle avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512v1 xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local split_lock Detect avx_vnni avx512_bif16 wbinvd dtherm ida arat pln pts avx512vbmi ump kpu ospke waitpkg avx512_vmbmi gfn vi vpc1mulqdq avx512_vnni avx512_lite alg tme avx512_vpopcnt dq la57 zrpid bus_lock Detect cldemote movdiri movdir64b enqcmd fasm md_clear serialize txsdtrk pconfig arch_lbr avx512_fp16 amx_tile flush_ll1 arch_capabilities  
**Virtualization:** VT-x  
**L1d cache:** 1.5 MiB (32 instances)  
**L1i cache:** 1 MiB (32 instances)  
**L2 cache:** 64 MiB (32 instances)  
**L3 cache:** 75 MiB (2 instances)  
**NUMA node(s):** 2  
**NUMA node0 CPU(s):** 0-15  
**NUMA node1 CPU(s):** 16-31  
**Vulnerability Itlb multihit:** Not affected  
**Vulnerability Lttf:** Not affected  
**Vulnerability Mds:** Not affected  
**Vulnerability Meltdown:** Not affected  
**Vulnerability Spec store bypass:** Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
**Vulnerability Spectre v1:** Mitigation; usercopy/swapgs barriers and __user pointer sanitization  
**Vulnerability Spectre v2:** Mitigation; Enhanced IBRS, IBFB conditional, RSB filling  
**Vulnerability Srbd:** Not affected  
**Vulnerability Tsx async abort:** Not affected

---

From `lscpu --cache`:

<table>
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<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
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<tbody>
<tr>
<td>L1d</td>
<td>48K</td>
<td>1.5M</td>
<td>12</td>
<td>Data</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L1i</td>
<td>32K</td>
<td>1M</td>
<td>8</td>
<td>Instruction</td>
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<td>64</td>
<td>1</td>
<td>64</td>
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<tr>
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<td>2M</td>
<td>64M</td>
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<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
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<tr>
<td>L3</td>
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<td>75M</td>
<td>15</td>
<td>Unified</td>
<td>3</td>
<td>40960</td>
<td>1</td>
<td>64</td>
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(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650 V3
(2.50 GHz, Intel Xeon Gold 6426Y)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = Not Run

Test Date: Dec-2022
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Platform Notes (Continued)

/proc/cpuinfo cache data
  cache size: 38400 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
    node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
    node 0 size: 257706 MB
    node 0 free: 256830 MB
    node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
    node 1 size: 257989 MB
    node 1 free: 257351 MB
    node distances:
      node 0 1
      0: 10 21
      1: 21 10

From /proc/meminfo
  MemTotal: 528073088 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has ondemand

From /etc/*release* /etc/*version*
  os-release:
    NAME="SLES"
    VERSION="15-SP4"
    VERSION_ID="15.4"
    PRETTY_NAME="SUSE Linux Enterprise Server 15 SP4"
    ID="sles"
    ID_LIKE="suse"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:15:sp4"

  uname -a:
    Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18
    UTC 2022 (49db222) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

  CVE-2018-12207 (iTLB Multihit): Not affected
  CVE-2018-3620 (L1 Terminal Fault): Not affected
  Microarchitectural Data Sampling: Not affected
  CVE-2017-5754 (Meltdown): Not affected

(Continued on next page)
Lenovo Global Technology
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SPECSpeed®2017_fp_base = 241
SPECSpeed®2017_fp_peak = Not Run

Platform Notes (Continued)

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
CVE-2017-5715 (Spectre variant 2):
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Dec 29 16:53

SPEC is set to: /home/cpu2017-1.1.8-ic2022.1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 889G 24G 865G 3% /

From /sys/devices/virtual/dmi/id
Vendor: Lenovo
Product: ThinkSystem SR650 V3 MB,EGS,DDR5,SH,2U
Product Family: ThinkSystem
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
  16x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800

BIOS:
  BIOS Vendor: Lenovo
  BIOS Version: ESE109G-0.83
  BIOS Date: 12/20/2022
  BIOS Revision: 0.83
  Firmware Revision: 0.75

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650 V3
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<td>Lenovo Global Technology</td>
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<td>Tested by:</td>
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</tbody>
</table>

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = Not Run

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Dec-2022</th>
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<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Feb-2023</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>Jun-2022</td>
</tr>
</tbody>
</table>

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Base Compiler Invocation

C benchmarks:
- icx

Fortran benchmarks:
- ifx

Benchmarks using both Fortran and C:
- ifx icx

(Continued on next page)
## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
```bash
icpx icx ifx
```

### Base Portability Flags

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Flags</th>
</tr>
</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian</td>
</tr>
<tr>
<td>627.cam4_s</td>
<td>-DSPEC_LP64 -DSPEC_CASE_FLAG</td>
</tr>
<tr>
<td>628.pop2_s</td>
<td>-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl</td>
</tr>
<tr>
<td>638.imagick_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>644.nab_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>649.fotonik3d_s</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>654.roms_s</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

### Base Optimization Flags

#### C benchmarks:
```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Fortran benchmarks:
```bash
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Benchmarks using both Fortran and C:
```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Benchmarks using Fortran, C, and C++:
```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
```

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR650 V3
(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 241
SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Dec-2022
Hardware Availability: Feb-2023
Software Availability: Jun-2022

Base Optimization Flags (Continued)
Benchmarks using Fortran, C, and C++ (continued):
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-12-29 03:56:18-0500.
Report generated on 2023-01-18 14:10:11 by CPU2017 PDF formatter v6442.
Originally published on 2023-01-17.