SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

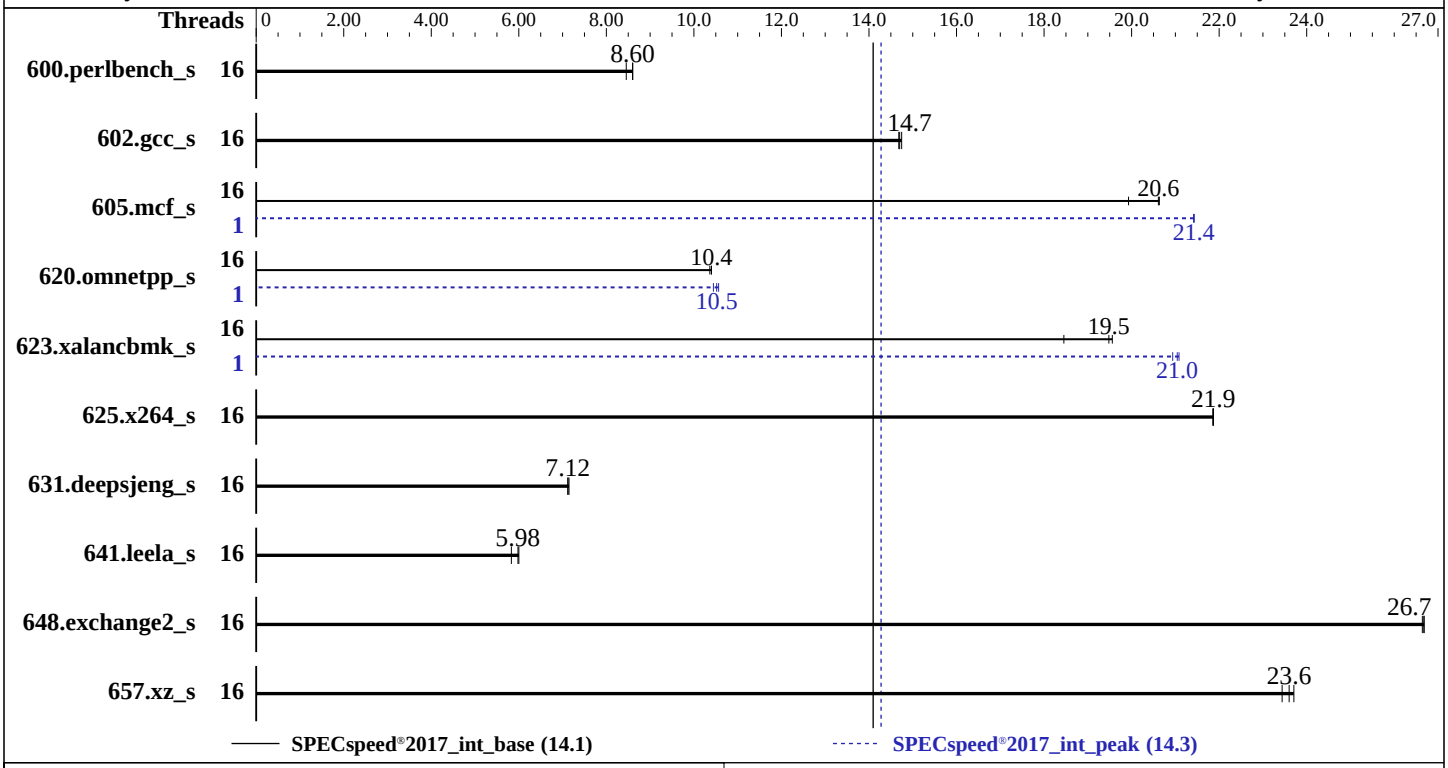
ProLiant DL345 Gen11
(3.25 GHz, AMD EPYC 9354P)

SPECspeed®2017_int_base = 14.1

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2022
Hardware Availability: Dec-2022
Software Availability: Nov-2022



Hardware

CPU Name:	AMD EPYC 9354P
Max MHz:	3800
Nominal:	3250
Enabled:	32 cores, 1 chip
Orderable:	1 chip
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	256 MB I+D on chip per chip, 32 MB shared / 4 cores
Other:	None
Memory:	384 GB (12 x 32 GB 2Rx8 PC5-4800B-R)
Storage:	1 x 1.6 TB NVMe SSD, RAID 0
Other:	None

Software

OS:	Ubuntu 22.04.1 LTS Kernel 5.15.0-53-generic
Compiler:	C/C++/Fortran: Version 4.0.0 of AOCC
Parallel:	Yes
Firmware:	HPE BIOS Version v1.12 11/24/2022 released Nov-2022
File System:	ext4
System State:	Run level 5 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

SPEC CPU®2017 Integer Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise
(Test Sponsor: HPE)
ProLiant DL345 Gen11
(3.25 GHz, AMD EPYC 9354P)

SPECspeed®2017_int_base = 14.1

SPECspeed®2017_int_peak = 14.3

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Dec-2022
Hardware Availability: Dec-2022
Software Availability: Nov-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	16	210	8.45	**206**	**8.60**	206	8.60	16	210	8.45	**206**	**8.60**	206	8.60
602.gcc_s	16	270	14.7	271	14.7	**271**	**14.7**	16	270	14.7	271	14.7	**271**	**14.7**
605.mcf_s	16	237	19.9	229	20.6	**229**	**20.6**	1	220	21.4	**220**	**21.4**	220	21.4
620.omnetpp_s	16	157	10.4	**157**	**10.4**	157	10.4	1	154	10.6	156	10.4	**155**	**10.5**
623.xalancbmk_s	16	76.8	18.5	**72.7**	**19.5**	72.4	19.6	1	67.2	21.1	**67.3**	**21.0**	67.7	20.9
625.x264_s	16	**80.7**	**21.9**	80.7	21.9	80.7	21.9	16	**80.7**	**21.9**	80.7	21.9	80.7	21.9
631.deepsjeng_s	16	201	7.12	201	7.15	**201**	**7.12**	16	201	7.12	201	7.15	**201**	**7.12**
641.leela_s	16	293	5.83	**285**	**5.98**	284	6.00	16	293	5.83	**285**	**5.98**	284	6.00
648.exchange2_s	16	**110**	**26.7**	110	26.6	110	26.7	16	**110**	**26.7**	110	26.6	110	26.7
657.xz_s	16	**262**	**23.6**	261	23.7	264	23.4	16	**262**	**23.6**	261	23.7	264	23.4

SPECspeed®2017_int_base = 14.1

SPECspeed®2017_int_peak = 14.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
```

(Continued on next page)

Operating System Notes (Continued)

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-15"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc400_genoa_B_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "16"

Environment variables set by runcpu during the 605.mcf_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 620.omnetpp_s peak run:
GOMP_CPU_AFFINITY = "15"

Environment variables set by runcpu during the 623.xalancbmk_s peak run:
GOMP_CPU_AFFINITY = "15"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

BIOS Configuration
 Workload Profile set to General Peak Frequency Compute
 Determinism Control set to Manual
   Performance Determinism set to Power Deterministic

(Continued on next page)

Platform Notes (Continued)

```
 AMD SMT Option set to Disabled
 NUMA memory domains per socket set to Four memory domains per socket
 Last-Level Cache (LLC) as NUMA Node set to Enabled
 ACPI CST C2 Latency set to 18 microseconds
 Memory PStates set to Disabled
 Thermal Configuration set to Maximum Cooling

The system ROM used for this result contains microcode version 0x0A10110e for the
AMD EPYC 9nn4X family of processors. The reference code/AGESA version used in this
ROM is version GenoaPI 1.0.0.1-L6

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on admin1 Mon Jun 27 18:44:34 2022

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 9354P 32-Core Processor
       1  "physical id"s (chips)
       32 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
       siblings  : 32
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31

 From lscpu from util-linux 2.37.2:
      Architecture:                    x86_64
      CPU op-mode(s):                  32-bit, 64-bit
      Address sizes:                   52 bits physical, 57 bits virtual
      Byte Order:                      Little Endian
      CPU(s):                          32
      On-line CPU(s) list:             0-31
      Vendor ID:                       AuthenticAMD
      Model name:                      AMD EPYC 9354P 32-Core Processor
      CPU family:                      25
      Model:                           17
      Thread(s) per core:              1
      Core(s) per socket:              32
      Socket(s):                       1
      Stepping:                        1
      Frequency boost:                 enabled
      CPU max MHz:                     3800.0000
```

(Continued on next page)

Platform Notes (Continued)

```
      CPU min MHz:                         400.0000
      BogoMIPS:                            6490.42
      Flags:                               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
      pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
      pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
      aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
      popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
      misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
      bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
      ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
      avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
      avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
      cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat
      npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
      pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku
      ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq
      la57 rdpid overflow_recov succor smca fsrm flush_l1d
      Virtualization:                      AMD-V
      L1d cache:                           1 MiB (32 instances)
      L1i cache:                           1 MiB (32 instances)
      L2 cache:                            32 MiB (32 instances)
      L3 cache:                            256 MiB (8 instances)
      NUMA node(s):                        8
      NUMA node0 CPU(s):                   0-3
      NUMA node1 CPU(s):                   16-19
      NUMA node2 CPU(s):                   8-11
      NUMA node3 CPU(s):                   24-27
      NUMA node4 CPU(s):                   12-15
      NUMA node5 CPU(s):                   28-31
      NUMA node6 CPU(s):                   4-7
      NUMA node7 CPU(s):                   20-23
      Vulnerability Itlb multihit:         Not affected
      Vulnerability L1tf:                  Not affected
      Vulnerability Mds:                   Not affected
      Vulnerability Meltdown:              Not affected
      Vulnerability Mmio stale data:       Not affected
      Vulnerability Retbleed:              Not affected
      Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
      prctl and seccomp
      Vulnerability Spectre v1:            Mitigation; usercopy/swapgs barriers and __user
      pointer sanitization
      Vulnerability Spectre v2:           Mitigation; Retpolines, IBPB conditional, IBRS_FW,
      STIBP disabled, RSB filling, PBRSB-eIBRS Not affected
      Vulnerability Srbds:                 Not affected
      Vulnerability Tsx async abort:       Not affected

 From lscpu --cache:
```

(Continued on next page)

Platform Notes (Continued)

```
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       32K       1M    8 Data            1    64        1             64
      L1i       32K       1M    8 Instruction     1    64        1             64
      L2         1M      32M    8 Unified         2  2048        1             64
      L3        32M     256M   16 Unified         3 32768        1             64

  /proc/cpuinfo cache data
     cache size : 1024 KB

  From numactl --hardware
  WARNING: a numactl 'node' might or might not correspond to a physical chip.
    available: 8 nodes (0-7)
    node 0 cpus: 0 1 2 3
    node 0 size: 48037 MB
    node 0 free: 47897 MB
    node 1 cpus: 16 17 18 19
    node 1 size: 48382 MB
    node 1 free: 48218 MB
    node 2 cpus: 8 9 10 11
    node 2 size: 48382 MB
    node 2 free: 48174 MB
    node 3 cpus: 24 25 26 27
    node 3 size: 48382 MB
    node 3 free: 48254 MB
    node 4 cpus: 12 13 14 15
    node 4 size: 48382 MB
    node 4 free: 48155 MB
    node 5 cpus: 28 29 30 31
    node 5 size: 48382 MB
    node 5 free: 48251 MB
    node 6 cpus: 4 5 6 7
    node 6 size: 48382 MB
    node 6 free: 48189 MB
    node 7 cpus: 20 21 22 23
    node 7 size: 48342 MB
    node 7 free: 48234 MB
    node distances:
    node   0   1   2   3   4   5   6   7
      0:  10  11  12  12  12  12  12  12
      1:  11  10  12  12  12  12  12  12
      2:  12  12  10  11  12  12  12  12
      3:  12  12  11  10  12  12  12  12
      4:  12  12  12  12  10  11  12  12
      5:  12  12  12  12  11  10  12  12
      6:  12  12  12  12  12  12  10  11
      7:  12  12  12  12  12  12  11  10
```

(Continued on next page)

Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       395957328 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/sbin/tuned-adm active
    Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
   performance

/usr/bin/lsb_release -d
   Ubuntu 22.04.1 LTS

From /etc/*release* /etc/*version*
   debian_version: bookworm/sid
   os-release:
      PRETTY_NAME="Ubuntu 22.04.1 LTS"
      NAME="Ubuntu"
      VERSION_ID="22.04"
      VERSION="22.04.1 LTS (Jammy Jellyfish)"
      VERSION_CODENAME=jammy
      ID=ubuntu
      ID_LIKE=debian
      HOME_URL="https://www.ubuntu.com/"

uname -a:
   Linux admin1 5.15.0-53-generic #59-Ubuntu SMP Mon Oct 17 18:53:30 UTC 2022 x86_64
   x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                    Not affected
CVE-2018-3620 (L1 Terminal Fault):                 Not affected
Microarchitectural Data Sampling:                  Not affected
CVE-2017-5754 (Meltdown):                          Not affected
mmio_stale_data:                                   Not affected
retbleed:                                          Not affected
CVE-2018-3639 (Speculative Store Bypass):          Mitigation: Speculative Store
                                                   Bypass disabled via prctl and
                                                   seccomp
CVE-2017-5753 (Spectre variant 1):                 Mitigation: usercopy/swapgs
                                                   barriers and __user pointer
                                                   sanitization
CVE-2017-5715 (Spectre variant 2):                 Mitigation: Retpolines, IBPB:
                                                   conditional, IBRS_FW, STIBP:
                                                   disabled, RSB filling,
```

(Continued on next page)

Platform Notes (Continued)

```
                                                    PBRSB-eIBRS: Not affected
 CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
 CVE-2019-11135 (TSX Asynchronous Abort):               Not affected

 run-level 5 Jun 27 18:30

 SPEC is set to: /home/cpu2017
    Filesystem                     Type  Size  Used Avail Use% Mounted on
    /dev/mapper/ubuntu--vg-ubuntu--lv ext4   98G   18G   75G  20% /

 From /sys/devices/virtual/dmi/id
     Vendor:         HPE
     Product:        ProLiant DL345 Gen11
     Product Family: ProLiant
     Serial:         DL3x5GEN11

 Additional information from dmidecode 3.3 follows.  WARNING: Use caution when you
 interpret this section. The 'dmidecode' program reads system data which is "intended to
 allow hardware to be accurately determined", but the intent may not be met, as there are
 frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
   Memory:
     4x Hynix HMCG88AEBRA168N 32 GB 2 rank 4800
     4x Hynix HMCG88MEBRA113N 32 GB 2 rank 4800
     4x Hynix HMCG88MEBRA115N 32 GB 2 rank 4800

 BIOS:
    BIOS Vendor:       HPE
    BIOS Version:      1.12
    BIOS Date:         11/24/2022
    BIOS Revision:     1.12
    Firmware Revision: 1.10

 (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
        | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------
```

(Continued on next page)

Compiler Version Notes (Continued)

```
==============================================================================
C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
        | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------

==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on
  LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64

(Continued on next page)

Base Portability Flags (Continued)

648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc

C++ benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc-ext

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -lflang -lamdalloc

Base Other Flags

C benchmarks:
-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

(Continued on next page)

Base Other Flags (Continued)

Fortran benchmarks:
-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

625.x264_s: basepeak = yes

657.xz_s: basepeak = yes

(Continued on next page)

Peak Optimization Flags (Continued)

C++ benchmarks:

620.omnetpp_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp -flto -finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt -fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang`

623.xalancbmk_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp -flto -finline-aggressive -mllvm -unroll-threshold=100 -mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt -mllvm -do-block-reorder=aggressive -fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp -lomp -lamdlibm -lamdalloc-ext -lflang`

631.deepsjeng_s: `basepeak = yes`

641.leela_s: `basepeak = yes`

Fortran benchmarks:

648.exchange2_s: `basepeak = yes`

Peak Other Flags

C benchmarks:
`-Wno-return-type -Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.html`
`http://www.spec.org/cpu2017/flags/aocc400-flags.html`

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Genoa-rev2.1.xml
http://www.spec.org/cpu2017/flags/aocc400-flags.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2022-06-27 14:44:34-0400.
Report generated on 2023-02-01 18:24:20 by CPU2017 PS formatter v6442.
Originally published on 2023-02-01.