# SPEC CPU®2017 Integer Rate Result

## Dell Inc.

PowerEdge R6625 (AMD EPYC 9354 32-Core Processor)

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>SPECrate®2017_int_base =</th>
<th>SPECrate®2017_int_peak =</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>739</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

### CPU2017 License:
6573

### Test Sponsor:
Dell Inc.

### Tested by:
Dell Inc.

### Copies

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
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<tbody>
<tr>
<td>500.perlbench_r</td>
<td>128</td>
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<td>502.gcc_r</td>
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<td>505.mcf_r</td>
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<tr>
<td>520.omnetpp_r</td>
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<tr>
<td>523.xalancbmk_r</td>
<td>128</td>
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<tr>
<td>525.x264_r</td>
<td>128</td>
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<tr>
<td>531.deepsjeng_r</td>
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<tr>
<td>541.leela_r</td>
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<tr>
<td>548.exchange2_r</td>
<td>128</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** AMD EPYC 9354
- **Max MHz:** 3800
- **Nominal:** 3250
- **Enabled:** 64 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I+ 32 KB D on chip per core
- **Cache L2:** 1 MB I+D on chip per core
- **Cache L3:** 256 MB I+D on chip per chip, 32 MB shared / 4 cores
- **Other:** None
- **Memory:** 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
- **Storage:** 125 GB on tmpfs
- **Other:** None

### Software

- **OS:** Ubuntu 22.04.1 LTS 5.15.0-46-generic
- **Compiler:** C/C++/Fortran: Version 4.0.0 of AOCC
- **Parallel:** No
- **Firmware:** Version 1.1.0 released Nov-2022
- **File System:** tmpfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** Not Applicable
- **Other:** None
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
http://developer.amd.com/amd-aocc/

### Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

### Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit  

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone_reclaim_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

(Continued on next page)
Dell Inc.

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SPEC CPU®2017 Integer Rate Result

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Dell Inc. SPECrate®2017_int_base = 739

Dell Inc. SPECrate®2017_int_peak = Not Run

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Operating System Notes (Continued)

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.8-aocc400-B1b/amd_rate_aocc400_genoa_B_lib/lib
  /mnt/ramdisk/cpu2017-1.1.8-aocc400-B1b/amd_rate_aocc400_genoa_B_lib/lib
  32:"

MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

Platform Notes

BIOS settings:
  DRAM Refresh Delay : Performance
  DIMM Self Healing on
  Uncorrectable Memory Error : Disabled
  Virtualization Technology : Disabled
  NUMA Nodes per Socket : 4
  L3 Cache as NUMA Domain : Enabled

  System Profile : Custom
  Memory Patrol Scrub : Disabled

(Continued on next page)
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**SPECrater®2017_int_base = 739**

**SPECrater®2017_int_peak = Not Run**

**Platform Notes (Continued)**

PCI ASPM L1 Link  
Power Management : Disabled  
Determinism Slider : Power Determinism

Sysinfo program /mnt/ramdisk/cpu2017-1.1.8-aocc400-B1b/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d  
running on amd-sut Thu Jan 19 21:56:17 2023

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo  
model name : AMD EPYC 9354 32-Core Processor  
  2 "physical id"s (chips)  
  128 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 32  
siblings : 64  
physical 0: cores 0 1 2 3 8 9 10 11 16 17 18 19 24 25 26 27 32 33 34 35 40 41 42 43  
  48 49 50 51 56 57 58 59  
physical 1: cores 0 1 2 3 8 9 10 11 16 17 18 19 24 25 26 27 32 33 34 35 40 41 42 43  
  48 49 50 51 56 57 58 59

From lscpu from util-linux 2.37.2:  
Architecture:                    x86_64  
CPU op-mode(s):                  32-bit, 64-bit  
Address sizes:                   52 bits physical, 57 bits virtual  
Byte Order:                      Little Endian  
CPU(s):                          128  
On-line CPU(s) list:             0-127  
Vendor ID:                       AuthenticAMD  
Model name:                      AMD EPYC 9354 32-Core Processor  
CPU family:                      25  
Model:                           17  
Thread(s) per core:              2  
Core(s) per socket:              32  
Socket(s):                       2  
Stepping:                        1  
Frequency boost:                 enabled  
CPU max MHz:                     3800.0000  
CPU min MHz:                     400.0000  
BogoMIPS:                        6502.17  
Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr  
pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr opt  
pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid

(Continued on next page)
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Platform Notes (Continued)

aperfmpereq rapl pni pclmulqdq monitor sse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extatic cr8_legacy abm sse4a
misalignsse 3nowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb
bptext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs
ibpb stibp vmcall fsqsb base bmi1 avx2 smp bmi2 erms invpcid cqm rdt_a avx512f
avx512dq rdseed adv smap avx512ifma clflushopt clwb avx512cd sha_ni avx512bw
avx512vl xsaveopt xsavec xgetbv1 xsavees cqm_llc cqm_occup_llc cqm_mmb_total
cqm_mmb_local avx512_bf16 clzero irperf xsaveerprtr rdpnu wbnoinvd amd_ppin cppc arat
npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassist
pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umipaku
ospke avx512_vbmi2 gfnia vaes vpcm1muldq avx512vnmi avx512_bitalg avx512_vpopcntdq
la57 rdpid overflow_recover succor smca sfsmc flush_l1d

Virtualization: AMD-V
L1d cache: 2 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 64 MiB (64 instances)
L3 cache: 512 MiB (16 instances)
NUMA node(s): 16
NUMA node0 CPU(s): 0-3,64-67
NUMA node1 CPU(s): 16-19,80-83
NUMA node2 CPU(s): 8-11,72-75
NUMA node3 CPU(s): 24-27,88-91
NUMA node4 CPU(s): 12-15,76-79
NUMA node5 CPU(s): 28-31,92-95
NUMA node6 CPU(s): 4-7,68-71
NUMA node7 CPU(s): 20-23,84-87
NUMA node8 CPU(s): 32-35,96-99
NUMA node9 CPU(s): 48-51,112-115
NUMA node10 CPU(s): 40-43,104-107
NUMA node11 CPU(s): 56-59,120-123
NUMA node12 CPU(s): 44-47,108-111
NUMA node13 CPU(s): 60-63,124-127
NUMA node14 CPU(s): 36-39,100-103
NUMA node15 CPU(s): 52-55,116-119

Vulnerability Itlb multihit: Not affected
Vulnerability L1tft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio_stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user
pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBFB conditional, IBRS_FW,
STIBP always-on, RSB filling
Vulnerability Srbd: Not affected

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**Platform Notes (Continued)**

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

<table>
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<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
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<td>32K</td>
<td>2M</td>
<td>8</td>
<td>Data</td>
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<td>64</td>
<td>1</td>
<td>64</td>
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<td>32K</td>
<td>2M</td>
<td>8</td>
<td>Instruction</td>
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<td>2048</td>
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<td>L3</td>
<td>32M</td>
<td>512M</td>
<td>16</td>
<td>Unified</td>
<td>3</td>
<td>32768</td>
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<td>64</td>
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</tbody>
</table>

/proc/cpuinfo cache data  

Cache size: 1024 KB

From numactl --hardware  

Warning: a numactl 'node' might or might not correspond to a physical chip.  

Available: 16 nodes (0-15)

| node 0 cpus: | 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 |
| node 0 size:| 96312 MB |
| node 0 free:| 96404 MB |
| node 1 cpus:| 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 |
| node 1 size:| 96764 MB |
| node 1 free:| 96565 MB |
| node 2 cpus:| 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 |
| node 2 size:| 96765 MB |
| node 3 cpus:| 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 |
| node 3 size:| 96764 MB |
| node 3 free:| 96612 MB |
| node 4 cpus:| 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 |
| node 4 size:| 96765 MB |
| node 5 cpus:| 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 |
| node 5 size:| 96764 MB |
| node 5 free:| 96584 MB |
| node 6 cpus:| 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 |
| node 6 size:| 96765 MB |
| node 6 free:| 96548 MB |
| node 7 cpus:| 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 |
| node 7 size:| 96748 MB |
| node 7 free:| 96546 MB |
| node 8 cpus:| 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 |
| node 8 size:| 96765 MB |
| node 8 free:| 96634 MB |
| node 9 cpus:| 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 |
| node 9 size:| 96764 MB |
| node 9 free:| 96630 MB |
| node 10 cpus:| 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 |
| node 10 size:| 96765 MB |

(Continued on next page)
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Platform Notes (Continued)

node 10 free: 96636 MB  
node 11 cpus: 56 57 58 59 120 121 122 123  
node 11 size: 96729 MB  
node 11 free: 96585 MB  
node 12 cpus: 44 45 46 47 108 109 110 111  
node 12 size: 96765 MB  
node 12 free: 96629 MB  
node 13 cpus: 60 61 62 63 124 125 126 127  
node 13 size: 96764 MB  
node 13 free: 96630 MB  
node 14 cpus: 36 37 38 39 100 101 102 103  
node 14 size: 96765 MB  
node 14 free: 96636 MB  
node 15 cpus: 52 53 54 55 116 117 118 119  
node 15 size: 96739 MB  
node 15 free: 96605 MB  

node distances:

node 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
0: 10 11 12 12 12 12 12 12 32 32 32 32 32 32 32 32
1: 11 10 12 12 12 12 12 12 32 32 32 32 32 32 32 32
2: 12 12 10 11 12 12 12 12 32 32 32 32 32 32 32 32
3: 12 12 11 10 12 12 12 12 32 32 32 32 32 32 32 32
4: 12 12 12 12 10 11 12 12 32 32 32 32 32 32 32 32
5: 12 12 12 12 11 10 12 12 32 32 32 32 32 32 32 32
6: 12 12 12 12 12 12 10 11 32 32 32 32 32 32 32 32
7: 12 12 12 12 12 12 11 10 32 32 32 32 32 32 32 32
8: 32 32 32 32 32 32 32 32 10 11 12 12 12 12 12 12
9: 32 32 32 32 32 32 32 32 11 10 12 12 12 12 12 12
10: 32 32 32 32 32 32 32 32 12 12 10 11 12 12 12 12
11: 32 32 32 32 32 32 32 32 12 12 11 10 12 12 12 12
12: 32 32 32 32 32 32 32 32 12 12 12 12 10 11 12 12
13: 32 32 32 32 32 32 32 32 12 12 12 12 11 10 12 12
14: 32 32 32 32 32 32 32 32 12 12 12 12 12 12 10 11
15: 32 32 32 32 32 32 32 32 12 12 12 12 12 12 11 10

From /proc/meminfo
MemTotal: 1584861020 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sbin/tuned-adm active
Current active profile: latency-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

/usr/bin/lsb_release -d

(Continued on next page)
Platform Notes (Continued)

Ubuntu 22.04.1 LTS

From /etc/*release* /etc/*version*
  debian_version: bookworm/sid
  os-release:
    PRETTY_NAME="Ubuntu 22.04.1 LTS"
    NAME="Ubuntu"
    VERSION_ID="22.04"
    VERSION="22.04.1 LTS (Jammy Jellyfish)"
    VERSION_CODENAME=jammy
    ID=ubuntu
    ID_LIKE=debian
    HOME_URL="https://www.ubuntu.com/"

uname -a:
  Linux amd-sut 5.15.0-46-generic #49-Ubuntu SMP Thu Aug 4 18:03:25 UTC 2022 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
mmio_stale_data: Not affected
retbleed: Not affected
CVE-2018-3639 (Speculative Store Bypass):
  Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):
  Mitigation: usercopy/swapgs barriers and __user pointer sanitation
CVE-2017-5715 (Spectre variant 2):
  Mitigation: Retpolines, IBPB: conditional, IBRS_FW, STIBP: always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jan 19 21:53

SPEC is set to: /mnt/ramdisk/cpu2017-1.1.8-aocc400-B1b
  Filesystem Type Size Used Avail Use% Mounted on
tmpfs  tmpfs  125G  3.4G  122G  3% /mnt/ramdisk

From /sys/devices/virtual/dmi/id
  Vendor: Dell Inc.
  Product: PowerEdge R6625
SPEC CPU®2017 Integer Rate Result

Dell Inc.

PowerEdge R6625 (AMD EPYC 9354 32-Core Processor)

SPECrater®2017_int_base = 739
SPECrater®2017_int_peak = Not Run

CPU2017 License: 6573
Test Date: Jan-2023
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Jan-2023
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Platform Notes (Continued)

Product Family: PowerEdge
Serial: BGP4023

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
24x 80CE000080CE M321R8GA0BB0-CQKDG 64 GB 2 rank 4800

BIOS:
BIOS Vendor: Dell Inc.
BIOS Version: 1.1.0
BIOS Date: 11/25/2022
BIOS Revision: 1.1

(End of data from sysinfo program)

Compiler Version Notes

C
500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)

AMDC clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

C++
520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.jgea_r(base)

AMDC clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Fortran
548.exchange2_r(base)

AMDC clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#389 2022_10_07) (based on...
SPECCPU®2017 Integer Rate Result

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Dell Inc.

PowerEdge R6625 (AMD EPYC 9354 32-Core Processor)

SPECrate®2017_int_base = 739
SPECrate®2017_int_peak = Not Run

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Test Date: Jan-2023

Tested by: Dell Inc.
Hardware Availability: Feb-2023
Software Availability: Nov-2022

Compiler Version Notes (Continued)

LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-4.0-3206-389/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -flto -Wl,-ml1vm -Wl,-align-all-nofallthru-blocks=6
-Wl,-ml1vm -Wl,-reduce-array-computations=3
-Wl,-ml1vm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -ml1vm -unroll-threshold=50
-ml1vm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-ml1vm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdaloc

(Continued on next page)
### SPEC CPU®2017 Integer Rate Result

**Dell Inc.**

**PowerEdge R6625 (AMD EPYC 9354 32-Core Processor)**

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**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.  
**Test Date:** Jan-2023  
**Hardware Availability:** Feb-2023  
**Software Availability:** Nov-2022

#### Base Optimization Flags (Continued)

C++ benchmarks:

- `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
- `-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3`
- `-march=znver4 -fveclib=AMDLIBM -ffast-math`
- `-mllvm -unroll-threshold=100 -finline-aggressive`
- `-mllvm -loop-unsitch-threshold=200000`
- `-mllvm -reduce-array-computations=3 -zopt`
- `-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang`
- `-lamdalloc-ext`

Fortran benchmarks:

- `-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
- `-Wl,-mllvm -Wl,-reduce-array-computations=3`
- `-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop`
- `-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4`
- `-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions`
- `-mllvm -optimize-strided-mem-cost -floop-transform`
- `-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm`
- `-lflang -lamdalloc`

#### Base Other Flags

**C benchmarks:**

- `-Wno-unused-command-line-argument`

**C++ benchmarks:**

- `-Wno-unused-command-line-argument`

**Fortran benchmarks:**

- `-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

Dell Inc. PowerEdge R6625 (AMD EPYC 9354 32-Core Processor)

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