Fujitsu
PRIMERGY TX2550 M7, Intel Xeon Gold 6448Y, 2.10GHz

CPU2017 License: 19  
Test Sponsor: Fujitsu  
Tested by: Fujitsu  

SPECrate®2017_int_base = 564

Test Date: Jan-2023  
Hardware Availability: Mar-2023  
Software Availability: Nov-2022

Hardware

CPU Name: Intel Xeon Gold 6448Y  
Max MHz: 4100  
Nominal: 2100  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1.2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 60 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
Storage: 1 x SATA M.2 SSD, 480GB  
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4  
5.14.21-150400.24.33-default  
Compiler: C/C++, Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Fujitsu BIOS Version V1.0.0.0 R1.10.0 for D3985-A1x. Released Mar-2023; tested as V1.0.0.0 R0.30.2 for D3985-A1x Jan-2023  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage

SPECrate®2017_int_peak = Not Run

500.perlbench_r 128
502.gcc_r 128
505.mcf_r 128
520.omnetpp_r 128
523.xalancbmk_r 128
525.x264_r 128
531.deepsjeng_r 128
541.leela_r 128
548.exchange2_r 128
557.xz_r 128

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500.perlbench_r 128
502.gcc_r 128
505.mcf_r 128
520.omnetpp_r 128
523.xalancbmk_r 128
525.x264_r 128
531.deepsjeng_r 128
541.leela_r 128
548.exchange2_r 128
557.xz_r 128

500.perlbench 443
502.gcc 474
505.mcf 381
520.omnetpp 361
523.xalancbmk 414
525.x264 1030
531.deepsjeng 916
541.leela 390
548.exchange2 1100
557.xz 275

SPECrate®2017_int_base = 564
SPECrate®2017_int_peak = Not Run
SPEC CPU®2017 Integer Rate Result

Fujitsu
PRIMERGY TX2550 M7, Intel Xeon Gold 6448Y, 2.10GHz

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SPECrates
SPECrates®2017_int_base = 564
SPECrates®2017_int_peak = Not Run

Test Date: Jan-2023
Hardware Availability: Mar-2023
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Results Table

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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
cpupower -c all frequency-set -g performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/Benchmark/speccpu/lib/intel64:/home/Benchmark/speccpu/lib/ia32:/home/Benchmark/speccpu/jc5.0.1-32"
MALLOC_CONF = "retain:true"
SPEC CPU®2017 Integer Rate Result

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SPEC®2017_int_base = 564
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General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
    numactl --interleave=all runcpu <etc>
NR: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
DCU Streamer Prefetcher = Disabled
Package C State limit = C0
CPU Performance Boost = Aggressive
SNC (Sub NUMA) = Enable SNC2
FAN Control = Full
Sysinfo program /home/Benchmark/speccpu/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on localhost Mon Jan 30 22:36:54 2023
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6448Y
    2 "physical id"s (chips)
    128 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 32
    siblings : 64
    physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
    25 26 27 28 29 30 31
    physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
    25 26 27 28 29 30 31

From lscpu from util-linux 2.37.2:
    Architecture: x86_64
    CPU op-mode(s): 32-bit, 64-bit
    Address sizes: 46 bits physical, 57 bits virtual
    Byte Order: Little Endian
    CPU(s): 128
    On-line CPU(s) list: 0-127
    Vendor ID: GenuineIntel
    Model name: Intel(R) Xeon(R) Gold 6448Y
    CPU family: 6
    Model: 143
    Thread(s) per core: 2
    Core(s) per socket: 32
    Socket(s): 2

(Continued on next page)
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SPEC CPU®2017 Integer Rate Result
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Platform Notes (Continued)

Stepping: 8
CPU max MHz: 4100.000
CPU min MHz: 800.000
BogoMIPS: 4200.00

Flags:
  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
  pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
  pdemclgb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
  nonstop_tsc cpuid aperf perfctr tsc_known_freq pni pclmulqdq dtes64 monitor ds_cpl vmx
  smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
  tsc_deadline_timer aes xsave avx f16c rdrand lahf_lmahl 3dnowprefetch cpuid_fault
  epb cat_l3 cat_l2 cdp involuntary intel_pni cdllpds asb mba ibrs ibpb stibp
  ibrs enhanced tpr_shadow vmmi flexpriority ept vpid ept_ad fsgsbase tsc_adress bmon
  hle avx2 smep bmi2 erva rmvpcid rtm cd_ka avx512f avx512dq rdseed adx smap
  avx512fma clflushopt clwb intel_pt avx512cd sha_hla mba ibrs ibpb xsaves xsaveopt
  xsavec xgetbv1 xsaves cgm_l1c cqm_occup_l1c cqm_mbm_total cqm_mbm_local
  split_load detect avx_vnni avx512_bf16 wboinnvd dtherm ida arat pnl pts hwp
  hwp_act_window hwp_ep phe hwp_pkg_req avx512vbm vmmi umip pku ospe kwaitpkg avx512v_bvmn2
  gfnv vaes vpcmclqdd axv512_vnni axv512_bitaul tme axv512_vpmcpntdq la57 rdpid
  bus_lock_detect coldemote movdir64b engcmd fsrm md_clear serialize tlsidtrk
  pcontext larch_lbr axv512_fp16 amx_tile flush_lld arch_capabilities

Virtualization: VT-x

L1d cache: 3 MIB (64 instances)
L1i cache: 2 MIB (64 instances)
L2 cache: 128 MIB (64 instances)
L3 cache: 120 MIB (2 instances)

NUMA node(s):
  4

NUMA node0 CPU(s):
  0-15,64-79

NUMA node1 CPU(s):
  16-31,80-95

NUMA node2 CPU(s):
  32-47,96-111

NUMA node3 CPU(s):
  48-63,112-127

Vulnerability L1tib multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds:
  Not affected
Vulnerability Meltdown:
  Not affected
Vulnerability Mmio stale data:
  Not affected
Vulnerability Retbleed:
  Not affected
Vulnerability Spec bypass: Mitigation; Speculative Store Bypass disabled via
  prctl and seccomp
Vulnerability Spectre v1:
  Mitigation; userscopy/swaps barriers and __user
  pointer sanitization
Vulnerability Spectre v2:
  Mitigation; Enhanced IBRS, IBPB conditional, RSB
  filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds:
  Not affected
Vulnerability Tsa async abort:
  Not affected

From lscpu --cache:

<table>
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<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
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<td>48K</td>
<td>3M</td>
<td>12</td>
<td>Data</td>
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<td>64</td>
<td>1</td>
<td>64</td>
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<tr>
<td>L1i</td>
<td>32K</td>
<td>2M</td>
<td>8</td>
<td>Instruction</td>
<td>1</td>
<td>64</td>
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<td>64</td>
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<tr>
<td>L2</td>
<td>2M</td>
<td>128M</td>
<td>16</td>
<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>60M</td>
<td>120M</td>
<td>15</td>
<td>Unified</td>
<td>3</td>
<td>65536</td>
<td>1</td>
<td>64</td>
</tr>
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</table>

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31
node 1 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
node 2 cpus: 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
node 3 cpus: 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127

From /proc/cpuinfo cache data
cache size: 61440 KB

(Continued on next page)
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Platform Notes (Continued)

node 0 size: 257620 MB
node 0 free: 256819 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 80 81 82 83 84 85 86 87 88
89 90 91 92 93 94 95
node 1 size: 258039 MB
node 1 free: 257449 MB
node 2 cpus: 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 96 97 98 99 100 101 102
103 104 105 106 107 108 109 110 111
node 2 size: 258005 MB
node 2 free: 257327 MB
node 3 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 112 113 114 115 116 117
118 119 120 121 122 123 124 125 126 127
node 3 size: 257671 MB
node 3 free: 257003 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10

From /proc/meminfo
MemTotal: 1056089704 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release* /etc/*version*

uname -a:
Linux localhost 5.14.21-150400.24.33-default #1 SMP PREEMPT_DYNAMIC Fri Nov 4 13:55:06
UTC 2022 (76cfe60) x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
mmio_stale_data: Not affected
retbleed: Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swaps
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling,

(Continued on next page)
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Platform Notes (Continued)

PBRSB-eIBRS: SW sequence
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
run-level 3 Jan 30 22:30
SPEC is set to: /home/Benchmark/speccpu
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 445G 50G 396G 12% /

From /sys/devices/virtual/dmi/id
Vendor: FUJITSU
Product: PRIMERGY TX2550 M7
Product Family: SERVER
Serial: EWCCxxxxxx

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
16x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800

BIOS:
BIOS Vendor: FUJITSU
BIOS Version: V1.0.0.0 R0.30.2 for D3985-A1x
BIOS Date: 01/24/2023
BIOS Revision: 0.30
Firmware Revision: 2.0

(End of data from sysinfo program)

Compiler Version Notes

C | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++ | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran | 548.exchange2_r(base)
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
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**Base Compiler Invocation**

C benchmarks:
- icx

C++ benchmarks:
- icpx

Fortran benchmarks:
- ifx

---

**Base Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

---

**Base Optimization Flags**

C benchmarks:
- w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin -lqkmalloc

C++ benchmarks:
- w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin -lqkmalloc

Fortran benchmarks:
## SPEC CPU®2017 Integer Rate Result

### Fujitsu

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The flags files that were used to format this result can be browsed at:

- [Intel ic2022 official linux64 revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

- [Intel ic2022 official linux64 revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml)
- [Fujitsu Platform Settings V1.0 SPR RevA.xml](http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-SPR-RevA.xml)

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