## NEC Corporation

**SPEC CPU®2017 Integer Speed Result**

**NEC Corporation**

**Express5800/R120i-2M (Intel Xeon Platinum 8360Y)**

### SPECspeed®2017_int_base = 11.9

### SPECspeed®2017_int_peak = 12.1

<table>
<thead>
<tr>
<th>Thread Name</th>
<th>Threads</th>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>144</td>
<td>7.19</td>
<td>8.24</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>144</td>
<td>10.9</td>
<td>11.2</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>144</td>
<td>11.8</td>
<td>12.0</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>144</td>
<td>13.4</td>
<td>17.1</td>
</tr>
<tr>
<td>623.xalancbmk_s</td>
<td>144</td>
<td>4.85</td>
<td>4.85</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>144</td>
<td>5.91</td>
<td>5.91</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>144</td>
<td>10.9</td>
<td>10.9</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>144</td>
<td>4.85</td>
<td>4.85</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>144</td>
<td>19.3</td>
<td>19.3</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>144</td>
<td>24.5</td>
<td>24.5</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Platinum 8360Y
- **Max MHz:** 3500
- **Nominal:** 2400
- **Enabled:** 72 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core
- **L2:** 1.25 MB I+D on chip per core
- **L3:** 54 MB I+D on chip per chip
- **Other:** None
- **Memory:** 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R)
- **Storage:** 1 x 800 GB SAS SSD, RAID 0
- **Other:** None

### Software

- **OS:** Red Hat Enterprise Linux release 8.3 (Ootpa) 4.18.0-240.el8.x86_64
- **Compiler:** C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux; Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux; C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
- **Parallel:** Yes
- **Firmware:** NEC BIOS Version U46 v1.40 04/28/2021 released Jul-2021
- **File System:** ext4
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS set to balance power and performance.
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SPEC®CPU2017 Integer Speed Result

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SPECspeed®2017_int_base = 11.9

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>144</td>
<td>247</td>
<td>7.19</td>
<td>247</td>
<td>7.19</td>
<td>246</td>
<td>7.21</td>
<td>144</td>
<td>217</td>
<td>8.18</td>
<td></td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>144</td>
<td>366</td>
<td>10.9</td>
<td>367</td>
<td>10.9</td>
<td>370</td>
<td>10.8</td>
<td>144</td>
<td>358</td>
<td>11.1</td>
<td>355</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>144</td>
<td>241</td>
<td>19.6</td>
<td>239</td>
<td>19.7</td>
<td>240</td>
<td>19.7</td>
<td>144</td>
<td>241</td>
<td>19.6</td>
<td>239</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>144</td>
<td>138</td>
<td>11.8</td>
<td>136</td>
<td>12.0</td>
<td>138</td>
<td>11.8</td>
<td>144</td>
<td>138</td>
<td>11.8</td>
<td>136</td>
</tr>
<tr>
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<td>144</td>
<td>106</td>
<td>13.4</td>
<td>106</td>
<td>13.4</td>
<td>106</td>
<td>13.3</td>
<td>144</td>
<td>106</td>
<td>13.4</td>
<td>106</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>144</td>
<td>103</td>
<td>17.1</td>
<td>103</td>
<td>17.1</td>
<td>103</td>
<td>17.1</td>
<td>144</td>
<td>98.8</td>
<td>17.8</td>
<td>99.1</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>144</td>
<td>242</td>
<td>5.92</td>
<td>242</td>
<td>5.92</td>
<td>242</td>
<td>5.91</td>
<td>144</td>
<td>242</td>
<td>5.92</td>
<td>242</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>144</td>
<td>352</td>
<td>4.85</td>
<td>351</td>
<td>4.85</td>
<td>352</td>
<td>4.85</td>
<td>144</td>
<td>352</td>
<td>4.85</td>
<td>351</td>
</tr>
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<td>144</td>
<td>152</td>
<td>19.3</td>
<td>152</td>
<td>19.4</td>
<td>152</td>
<td>19.3</td>
<td>144</td>
<td>152</td>
<td>19.3</td>
<td>152</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>144</td>
<td>252</td>
<td>24.5</td>
<td>252</td>
<td>24.5</td>
<td>252</td>
<td>24.5</td>
<td>144</td>
<td>252</td>
<td>24.5</td>
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</tr>
</tbody>
</table>

SPECspeed®2017_int_base = 11.9

SPECspeed®2017_int_peak = 12.1

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain: true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM memory using Redhat Enterprise Linux 8.0

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical software and/or firmware described on this result page.

(Continued on next page)
General Notes (Continued)

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, http://www.spec.org/osg/policy.html

This measured result may not be representative of the result that would be measured were this benchmark run with software and firmware available as of the publication date.

Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
    sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation

Platform Notes

BIOS Settings:
    Thermal Configuration: Maximum Cooling
    Workload Profile: General Peak Frequency Compute
    Advanced Memory Protection: Advanced ECC Support
    Memory Patrol Scrubbing: Disabled
    Minimum Processor Idle Power Core C-State: C6 State
    LLC Dead Line Allocation: Disabled
    LLC Prefetch: Enabled
    Enhanced Processor Performance: Enabled
    Workload Profile: Custom
    Minimum Processor Idle Power Package C-State: No Package State
    Energy/Performance Bias: Balanced Power
    Adjacent Sector Prefetch: Disabled
    DCU Stream Prefetcher: Disabled
    Numa Group Size Optimization: Flat

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16aaca64d
running on r120i2m Sat Sep 11 18:10:13 2021

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Platinum 8360Y CPU @ 2.40GHz
    2 "physical id"s (chips)
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NEC Corporation

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SPECspeed®2017_int_base = 11.9
SPECspeed®2017_int_peak = 12.1

CPU2017 License: 9006
Test Sponsor: NEC Corporation
Test Date: Sep-2021
Tested by: NEC Corporation
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Platform Notes (Continued)

144 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 36
siblings : 72
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 144
On-line CPU(s) list: 0-143
Thread(s) per core: 2
Core(s) per socket: 36
NUMA node(s): 2
Vendor ID: GenuineIntel
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Platinum 8360Y CPU @ 2.40GHz
Stepping: 6
CPU MHz: 2442.323
BogoMIPS: 4800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 55296K
NUMA node0 CPU(s): 0-35,72-107
NUMA node1 CPU(s): 36-71,108-143

Flags:
  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
  pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
  lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
  aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
  xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
  avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
  mba ibrs ibpb stibp ibrs_enabled tpr_shadow vnmi flexpriority ept vpid ept_ad
  fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rdt_a avx512_single ssbd
  mba ibrs ibpb stibp ibrs_enabled tpr_shadow vnmi flexpriority ept vpid ept_ad
  fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rdt_a avx512f avx512dq
  rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
  avx512vl xsaveopt xsaves xsavec xgetbv1 xsaves cqm_llc cqm_occupa_llc cqm_mbb_total
  cqm_mbb_local split_lock_detect wbinvd dtherm ida arat pln pts avx512vmbi umip pku
  ospke avx512_vmbi2 gfini vaes vpclmulqdq avx512_vni avx512_bitalg tme
  avx512_vpopcntdq la57 rdpid md_clear pconfld flush_l1d arch_capabilities

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Platform Notes (Continued)

/proc/cpuintinfo cache data
  cache size : 55296 KB

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
  28 29 30 31 32 33 34 35 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92
  93 94 95 96 97 98 99 100 101 102 103 104 105 106 107
  node 0 size: 959090 MB
  node 0 free: 1030463 MB
  node 1 cpus: 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60
  61 62 63 64 65 66 67 68 69 70 71 108 109 110 111 112 113 114 115 116 117 118 119 120
  121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142
  143
  node 1 size: 957826 MB
  node 1 free: 1031499 MB
  node distances:
    node 0 1
    0: 10 20
    1: 20 10

From /proc/meminfo
  MemTotal: 2113468132 kB
  HugePages_Total: 0
  Hugepagesize: 2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

From /etc/*release* /etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.3 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.3"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:
  Linux r120i2m 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020 x86_64 x86_64
NEC Corporation

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Platform Notes (Continued)

x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Sep 11 18:05

SPEC is set to: /home/cpu2017

Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 ext4 724G 133G 554G 20% /

From /sys/devices/virtual/dmi/id
Vendor: NEC
Product: Express5800/R120i-2M
Product Family: Express5800
Serial: CN705114NH

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
32x Hynix HMAA8GR7AJR4N-XN 64 GB 2 rank 3200

BIOS:
BIOS Vendor: NEC
BIOS Version: U46
BIOS Date: 04/28/2021
BIOS Revision: 1.40
Firmware Revision: 2.44

(End of data from sysinfo program)
## Compiler Version Notes

<table>
<thead>
<tr>
<th>Language</th>
<th>Application</th>
<th>Vendor</th>
<th>Version</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>C</td>
<td>600.perlbench_s(peak)</td>
<td>Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000</td>
<td></td>
<td></td>
</tr>
<tr>
<td>C</td>
<td>600.perlbench_s(base)</td>
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<td></td>
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</tr>
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<td>C++</td>
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<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Fortran</td>
<td>648.exchange2_s(base, peak)</td>
<td>Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

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Compiler Version Notes (Continued)

Intel(R) 64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
  icx

C++ benchmarks:
  icpx

Fortran benchmarks:
  ifort

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
  -DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
  -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
  -qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
  -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
  -DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
  -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
  -mbranches-within-32B-boundaries
  -L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/

(Continued on next page)
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Tested by: NEC Corporation

PECs Accelerator:

Test Date: Sep-2021
Hardware Availability: Jul-2021
Software Availability: Dec-2020

Base Optimization Flags (Continued)

C++ benchmarks (continued):
-lqkmalloc

Fortran benchmarks:
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

Peak Compiler Invocation

C benchmarks (except as noted below):
icx
600.perlbench_s: icc

C++ benchmarks:
icpx
Fortran benchmarks:
ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)
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- **Tested by:** NEC Corporation  
- **Software Availability:** Dec-2020

## Peak Optimization Flags (Continued)

- **605.mcf_s:** `basepeak = yes`  
- **625.x264_s:** `--DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -fno-openmp -m64 -Wl,-z,muldefs -qopt-mem-layout-trans=4 -fno-alias -mbranches-within-32B-boundaries -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

**C++ benchmarks:**

- **620.omnetpp_s:** `basepeak = yes`  
- **623.xalancbmk_s:** `basepeak = yes`  
- **631.deepsjeng_s:** `basepeak = yes`  
- **641.leela_s:** `basepeak = yes`

**Fortran benchmarks:**

- **648.exchange2_s:** `basepeak = yes`

The flags files that were used to format this result can be browsed at:


You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120i-RevE.xml](http://www.spec.org/cpu2017/flags/NEC-Platform-Settings-V1.2-R120i-RevE.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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