**SPEC CPU®2017 Integer Speed Result**

**Tyrone Systems**
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(3.20 GHz, Intel Xeon Gold 5315Y)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>11.2</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECspeed®2017_int_peak</td>
<td>11.4</td>
</tr>
</tbody>
</table>

**CPUT2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems

**Hardware**

<table>
<thead>
<tr>
<th>Threads</th>
<th>SPECspeed®2017_int_base (11.2)</th>
<th>SPECspeed®2017_int_peak (11.4)</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>32</td>
<td>6.97</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>32</td>
<td>7.57</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>9.15</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>9.49</td>
</tr>
<tr>
<td>623.xalanchmk_s</td>
<td>32</td>
<td>18.3</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>19.5</td>
</tr>
<tr>
<td>631.deepsjeng_s</td>
<td>32</td>
<td>16.8</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>17.4</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>20.7</td>
</tr>
<tr>
<td>657.xz_s</td>
<td>32</td>
<td>21.6</td>
</tr>
</tbody>
</table>

**CPU Name:** Intel Xeon Gold 5315Y  
**Max MHz:** 3600  
**Nominal:** 3200  
**Enabled:** 16 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 Chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 1.25 MB I+D on chip per core  
**L3:** 12 MB I+D on chip per chip  
**Other:** None  
**Memory:** 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R, running at 2933)  
**Storage:** 1 x 512 GB NVMe SSD  
**Other:** None

**Software**

<table>
<thead>
<tr>
<th>OS:</th>
<th>Red Hat Enterprise Linux release 8.5 (Ootpa)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;</td>
</tr>
<tr>
<td>Parallel:</td>
<td>Yes</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Version SE5C620.86B.01.01.0004.2110190142 released Oct-2021</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage.</td>
</tr>
</tbody>
</table>

**Test Date:** Jan-2023  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

---

**Tyrone Systems**

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SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero ID1100C2R-28
(3.20 GHz, Intel Xeon Gold 5315Y)

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Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
<td>Base</td>
<td></td>
<td>Peak</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>600.perlbvhs</td>
<td>32</td>
<td>254</td>
<td>6.98</td>
<td>255</td>
<td>6.97</td>
<td>258</td>
<td>6.89</td>
<td>32</td>
<td>234</td>
<td>7.57</td>
<td>235</td>
<td>7.56</td>
<td>234</td>
<td>7.58</td>
</tr>
<tr>
<td>605.mcf_s</td>
<td>32</td>
<td>258</td>
<td>18.3</td>
<td>259</td>
<td>18.2</td>
<td>257</td>
<td>18.4</td>
<td>32</td>
<td>258</td>
<td>18.3</td>
<td>259</td>
<td>18.2</td>
<td>257</td>
<td>18.4</td>
</tr>
<tr>
<td>620.omnetpp_s</td>
<td>32</td>
<td>283</td>
<td>5.77</td>
<td>282</td>
<td>5.78</td>
<td>284</td>
<td>5.75</td>
<td>32</td>
<td>283</td>
<td>5.77</td>
<td>282</td>
<td>5.78</td>
<td>284</td>
<td>5.75</td>
</tr>
<tr>
<td>623.xalancbhmk_s</td>
<td>32</td>
<td>73.1</td>
<td>19.4</td>
<td>72.6</td>
<td>19.5</td>
<td>72.1</td>
<td>19.7</td>
<td>32</td>
<td>73.1</td>
<td>19.4</td>
<td>72.6</td>
<td>19.5</td>
<td>72.1</td>
<td>19.7</td>
</tr>
<tr>
<td>625.x264_s</td>
<td>32</td>
<td>105</td>
<td>16.8</td>
<td>105</td>
<td>16.8</td>
<td>105</td>
<td>16.8</td>
<td>32</td>
<td>101</td>
<td>17.4</td>
<td>101</td>
<td>17.4</td>
<td>101</td>
<td>17.4</td>
</tr>
<tr>
<td>641.leela_s</td>
<td>32</td>
<td>337</td>
<td>5.06</td>
<td>339</td>
<td>5.04</td>
<td>337</td>
<td>5.06</td>
<td>32</td>
<td>337</td>
<td>5.06</td>
<td>339</td>
<td>5.04</td>
<td>337</td>
<td>5.06</td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>32</td>
<td>141</td>
<td>20.8</td>
<td>142</td>
<td>20.7</td>
<td>142</td>
<td>20.7</td>
<td>32</td>
<td>141</td>
<td>20.8</td>
<td>142</td>
<td>20.7</td>
<td>142</td>
<td>20.7</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbhmk_r / 623.xalancbhmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain=true"
OMP_STACKSIZE = "192M"
SPEC CPU®2017 Integer Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(3.20 GHz, Intel Xeon Gold 5315Y)

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SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.4

CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
 Prior to runcpu invocation
  Filesystem page cache synced and cleared with:
  sync; echo 3>/proc/sys/vm/drop_caches
  runcpu command invoked through numactl i.e.:
  numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the
system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the
system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the
system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b5891fe0e16acaf64d
running on icelakespec Tue Jan 3 23:46:03 2023

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
    2 "physical id"s (chips)
    32 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
    cpu cores : 8
    siblings : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:
  Architecture: x86_64
  CPU op-mode(s): 32-bit, 64-bit
  Byte Order: Little Endian
  CPU(s): 32
  On-line CPU(s) list: 0-31
  Thread(s) per core: 2
  Core(s) per socket: 8
  Socket(s): 2
  NUMA node(s): 2
  Vendor ID: GenuineIntel
  BIOS Vendor ID: Intel(R) Corporation
  CPU family: 6

(Continued on next page)
<table>
<thead>
<tr>
<th>Platform Notes (Continued)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model: 106</td>
</tr>
<tr>
<td>Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz</td>
</tr>
<tr>
<td>BIOS Model name: Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz</td>
</tr>
<tr>
<td>Stepping: 6</td>
</tr>
<tr>
<td>CPU MHz: 3200.000</td>
</tr>
<tr>
<td>CPU max MHz: 3600.000</td>
</tr>
<tr>
<td>CPU min MHz: 800.000</td>
</tr>
<tr>
<td>BogoMIPS: 6400.00</td>
</tr>
<tr>
<td>Virtualization: VT-x</td>
</tr>
<tr>
<td>L1d cache: 48K</td>
</tr>
<tr>
<td>L1i cache: 32K</td>
</tr>
<tr>
<td>L2 cache: 1280K</td>
</tr>
<tr>
<td>L3 cache: 12288K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s): 0-7,16-23</td>
</tr>
<tr>
<td>NUMA node1 CPU(s): 8-15,24-31</td>
</tr>
<tr>
<td>Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 cflflush dts acpi mpx sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf nni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca asme4_1 mmca4_2 k2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3nowprefetch cpuid_fault epb cat_13 invpcid_single sbd mba ibrs ibpb stibp ibrsenhanced fsgsbase tsc_adjust sgx bmi1 hle avx2 smep bmi2 erms invpcid cmq rdt_a_avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_nl avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cmq_llc cmq_occup_11c cmq_mbm_total cmq_mbm_local split_lock_detect wbninvd dtherm ida arat pln pts hwp hwp_act_window hwp_ep hwp_pkg_req avx512vbmi umip pku ospak avx512_vmbi2 gfnl vaes vpcmnlqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rpdpd sgx_lc fasm md_clear pconfigure flush_l1d arch_capabilities</td>
</tr>
<tr>
<td>/proc/cpuinfo cache data</td>
</tr>
<tr>
<td>cache size: 12288 MB</td>
</tr>
<tr>
<td>From numactl --hardware</td>
</tr>
<tr>
<td>WARNING: a numactl 'node' might or might not correspond to a physical cpu.</td>
</tr>
<tr>
<td>available: 2 nodes (0-1)</td>
</tr>
<tr>
<td>node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23</td>
</tr>
<tr>
<td>node 0 size: 1031812 MB</td>
</tr>
<tr>
<td>node 0 free: 1016024 MB</td>
</tr>
<tr>
<td>node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31</td>
</tr>
<tr>
<td>node 1 size: 1032148 MB</td>
</tr>
<tr>
<td>node 1 free: 1019095 MB</td>
</tr>
<tr>
<td>node distances:</td>
</tr>
<tr>
<td>node 0 1</td>
</tr>
<tr>
<td>0: 10 20</td>
</tr>
<tr>
<td>1: 20 10</td>
</tr>
<tr>
<td>From /proc/meminfo</td>
</tr>
<tr>
<td>MemTotal: 2113495848 kB</td>
</tr>
<tr>
<td>HugePages_Total: 0</td>
</tr>
<tr>
<td>Hugepagesize: 2048 kB</td>
</tr>
<tr>
<td>/sbin/tuned-adm active</td>
</tr>
<tr>
<td>Current active profile: throughput-performance</td>
</tr>
<tr>
<td>/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance</td>
</tr>
<tr>
<td>From /etc/<em>release</em> /etc/<em>version</em></td>
</tr>
<tr>
<td>os-release: NAME=&quot;Red Hat Enterprise Linux&quot;</td>
</tr>
</tbody>
</table>

(Continued on next page)
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**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero IDI100C2R-28**  
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<tr>
<th>SPECspeed®2017_int_base</th>
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</tr>
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<tbody>
<tr>
<td>11.2</td>
<td>11.4</td>
</tr>
</tbody>
</table>

CPU2017 License: 006042  
Test Sponsor: Netweb Pte Ltd  
Tested by: Tyrone Systems

Platform Notes (Continued)

```
VERSION="8.5 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.5"
PLATFORM_ID="platform:e18"
PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
```

uname -a:  
Linux icelakespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64  
x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

- CVE-2018-12207 (iTLB Multihit): Not affected
- CVE-2018-3620 (L1 Terminal Fault): Not affected
- CVE-2017-5754 (Meltdown): Not affected
- CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
- CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swappgs barriers and __user pointer sanitization
- CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling
- CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
- CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Jan 3 06:51

SPEC is set to: /home/cpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 402G 213G 189G 54% /home

From /sys/devices/virtual/dmi/id
Vendor: Tyrone_Systems
Product: Tyrone_Camarero_IDI100C2R-28
Product Family: Family
Serial: 2X22462203

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
- 32x Samsung M393A8G40BB4-CWE 64 GB 2 rank 3200, configured at 2933

BIOS:
- BIOS Vendor: Intel Corporation
- BIOS Version: SE5C620.86B.01.01.0004.2110190142
- BIOS Date: 10/19/2021

(End of data from sysinfo program)
SPECCPU®2017 Integer Speed Result

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(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017_int_base = 11.2
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

Test Date: Jan-2023
Hardware Availability: Apr-2021
Software Availability: May-2022

Compiler Version Notes

C       | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
| 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

C++     | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
| 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
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**CPU2017 License:** 006042  
**Test Date:** Jan-2023  
**Test Sponsor:** Netweb Pte Ltd  
**Hardware Availability:** Apr-2021  
**Tested by:** Tyrone Systems  
**Software Availability:** May-2022

### Base Optimization Flags

C benchmarks:
- `-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp`  
- `-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

C++ benchmarks:
- `-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

Fortran benchmarks:
- `-m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto`  
- `-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4`  
- `-nostandard-realloc-lhs -align array32byte`  
- `-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

### Peak Compiler Invocation

C benchmarks:
- `icx`

C++ benchmarks:
- `icpx`

Fortran benchmarks:
- `ifx`

### Peak Portability Flags

Same as Base Portability Flags

### Peak Optimization Flags

C benchmarks:
- `600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)`  
- `-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3`  
- `-ffast-math -flto -mfpmath=sse -funroll-loops`  
- `-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP`  
- `-fno-strict-overflow -L/usr/local/jemalloc64-5.0.1/lib`  

(Continued on next page)
SPEC CPU®2017 Integer Speed Result
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Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(3.20 GHz, Intel Xeon Gold 5315Y)

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SPECspeed®2017_int_base = 11.2
SPECspeed®2017_int_peak = 11.4

Peak Optimization Flags (Continued)

600.perlbench_s (continued):
-1ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

605.mcf_s: basepeak = yes

625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -O3
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml
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**SPECspeed®2017_int_base = 11.2**  
**SPECspeed®2017_int_peak = 11.4**

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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