## SPEC CPU®2017 Floating Point Speed Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)

**Tyrone Camarero IDI100C2R-28**  
(3.20 GHz, Intel Xeon Gold 5315Y)

<table>
<thead>
<tr>
<th>Test Date:</th>
<th>Jan-2023</th>
</tr>
</thead>
<tbody>
<tr>
<td>Hardware Availability:</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Software Availability:</td>
<td>May-2022</td>
</tr>
</tbody>
</table>

### SPECspeed®2017_fp_base = 123

### SPECspeed®2017_fp_peak = 123

**CPU2017 License:** 006042

**Test Sponsor:** Netweb Pte Ltd

**Tested by:** Tyrone Systems

### Software

- **OS:** Red Hat Enterprise Linux release 8.5 (Ootpa)  
  Kernel 4.18.0-348.el8.x86_64
- **Compiler:** C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;  
  Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
- **Parallel:** Yes
- **Firmware:** Version SE5C620.86B.01.01.0004.2110190142 released Oct-2021
- **File System:** xfs
- **System State:** Run level 3 (multi-user)
- **Base Pointers:** 64-bit
- **Peak Pointers:** 64-bit
- **Other:** jemalloc memory allocator V5.0.1
- **Power Management:** BIOS and OS set to prefer performance at the cost of additional power usage.

### Hardware

- **CPU Name:** Intel Xeon Gold 5315Y  
  **Max MHz:** 3600  
  **Nominal:** 3200
- **Enabled:** 16 cores, 2 chips, 2 threads/core
- **Orderable:** 1.2 Chips
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
  **L2:** 1.25 MB I+D on chip per core  
  **L3:** 12 MB I+D on chip per chip
- **Other:** None
- **Memory:** 2 TB (32 x 64 GB 2Rx4 PC4-3200AA-R, running at 2933)
- **Storage:** 1 x 512 GB NVMe SSD
- **Other:** None

### Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_fp_base</th>
<th>SPECspeed®2017_fp_peak</th>
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</thead>
<tbody>
<tr>
<td>603.bwaves_s</td>
<td>16</td>
<td>123</td>
<td>123</td>
</tr>
<tr>
<td>607.cactuBSSN_s</td>
<td>16</td>
<td>123</td>
<td></td>
</tr>
<tr>
<td>619.lbm_s</td>
<td>16</td>
<td>123</td>
<td></td>
</tr>
<tr>
<td>621.wrf_s</td>
<td>16</td>
<td>123</td>
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<td>628.pop2_s</td>
<td>16</td>
<td>123</td>
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<tr>
<td>638.imagick_s</td>
<td>16</td>
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<tr>
<td>644.nab_s</td>
<td>16</td>
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<td>649.fotonik3d_s</td>
<td>16</td>
<td>78.6</td>
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<tr>
<td>654.roms_s</td>
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Results Table

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<tr>
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<th>Base Seconds</th>
<th>Base Ratio</th>
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<td>619.ibm_s</td>
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<td>621.wrf_s</td>
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<td>628.pop2_s</td>
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<td>66.7</td>
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<td>66.3</td>
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<td>644.nab_s</td>
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<td>78.6</td>
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<td>654.roms_s</td>
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<td>147</td>
<td>107</td>
<td>16</td>
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<td>107</td>
<td>147</td>
</tr>
</tbody>
</table>

SPECspeed®2017_fp_base = 123
SPECspeed®2017_fp_peak = 123

Submit Notes
The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes
Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.: (Continued on next page)
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<td>Hardware Availability: Apr-2021</td>
</tr>
<tr>
<td>Tested by: Tyrone Systems</td>
<td>Software Availability: May-2022</td>
</tr>
</tbody>
</table>

**General Notes (Continued)**

```
numactl --interleave=all runcpu <etc>

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
```

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

**Platform Notes**

BIOS Settings:
Power Technology = Custom
ENERGY_PERF_BIAS_CFG mode = Maximum Performance
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acaf64d
running on icelakespec Wed Jan 4 04:00:20 2023

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
```
model name : Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz
  2 "physical id"s (chips)
  32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 8
  siblings : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu from util-linux 2.32.1:
```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Thread(s) per core: 2
```

(Continued on next page)
## Platform Notes (Continued)

- CPU2017 License: 006042
- Test Sponsor: Netweb Pte Ltd
- Tested by: Tyrone Systems
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### CPU Details

<table>
<thead>
<tr>
<th>Specification</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Core(s) per socket</td>
<td>8</td>
</tr>
<tr>
<td>Socket(s)</td>
<td>2</td>
</tr>
<tr>
<td>NUMA node(s)</td>
<td>2</td>
</tr>
<tr>
<td>Vendor ID</td>
<td>GenuineIntel</td>
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<tr>
<td>BIOS Vendor ID</td>
<td>Intel(R) Corporation</td>
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<tr>
<td>CPU family</td>
<td>6</td>
</tr>
<tr>
<td>Model</td>
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<tr>
<td>Model name</td>
<td>Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz</td>
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<tr>
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<td>Intel(R) Xeon(R) Gold 5315Y CPU @ 3.20GHz</td>
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<tr>
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<td>CPU min MHz</td>
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<td>L2 cache</td>
<td>1280K</td>
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<tr>
<td>L3 cache</td>
<td>12288K</td>
</tr>
<tr>
<td>NUMA node0 CPU(s)</td>
<td>0-7,16-23</td>
</tr>
<tr>
<td>NUMA node1 CPU(s)</td>
<td>8-15,24-31</td>
</tr>
<tr>
<td>Flags</td>
<td>fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdse1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 invpcid_single ssbd mba ibrs ibpb stibp ibrs_enhanced fsqbase tsc_adjust sgx bmi1 hle avx2 smep bmi2 ems invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb intel_pni avx512cd sha_ni avx512bw avx512vl xsaveopt xsaveopt xsavec xsaves cqm_llc cqm_mbm_total cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pcg_req avx512vbmi umip pku ospke avx512_vbmi2 gfnl vaes vpcmulldq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid sgx_lc fsrm md_clear pconfug flush_lld arch_capabilities</td>
</tr>
</tbody>
</table>

From /proc/cpuinfo cache data

- cache size : 12288 KB

WARNING: a numactl 'node' might or might not correspond to a physical chip.

- available: 2 nodes (0-1)
- node 0 cpus: 0 1 2 3 4 5 6 7 16 17 18 19 20 21 22 23
- node 0 size: 1031812 MB
- node 0 free: 1015167 MB
- node 1 cpus: 8 9 10 11 12 13 14 15 24 25 26 27 28 29 30 31
- node 1 size: 1032148 MB
- node 1 free: 1013466 MB

(Continued on next page)
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Tested by: Tyrone Systems

SPECspeed

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Platform Notes (Continued)

node distances:
node  0  1
  0:  10  20
  1:  20  10

From /proc/meminfo
  MemTotal:       2113495848 kB
  HugePages_Total:       0
  Hugepagesize:       2048 kB

/sbin/tuned-adm active
  Current active profile: throughput-performance

/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has performance

From /etc/*release*/etc/*version*
  os-release:
    NAME="Red Hat Enterprise Linux"
    VERSION="8.5 (Ootpa)"
    ID="rhel"
    ID_LIKE="fedora"
    VERSION_ID="8.5"
    PLATFORM_ID="platform:el8"
    PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
    ANSI_COLOR="0;31"
  redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
  system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos

uname -a:
  Linux icelakespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64
  x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2018-3639 (Speculative Store Bypass): Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5753 (Spectre variant 1): Mitigation: Enhanced IBRS, IBPB:
CVE-2017-5715 (Spectre variant 2): (Continued on next page)
Platform Notes (Continued)

conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
run-level 3 Jan 3 06:51
SPEC is set to: /home/cpu2017

Memory:
32x Samsung M393A8G40BB4-CWE 64 GB 2 rank 3200, configured at 2933

BIOS:
BIOS Vendor: Intel Corporation
BIOS Version: SE5C620.86B.01.01.0004.2110190142
BIOS Date: 10/19/2021

(End of data from sysinfo program)

Compiler Version Notes

==============================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
| 644.nab_s(base, peak)
==============================================================================
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
==============================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316

(Continued on next page)
### Base Compiler Invocation

C benchmarks:
- icx

Fortran benchmarks:
- ifx

Benchmarks using both Fortran and C:
- ifx icx

Benchmarks using Fortran, C, and C++:
- icpx icx ifx

---

### Compiler Version Notes (Continued)

- Intel (R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
- Intel (R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316
- Intel (R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316

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<table>
<thead>
<tr>
<th>Base Compiler Invocation</th>
<th>Fortran</th>
<th>Fortran, C</th>
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</thead>
<tbody>
<tr>
<td>icx</td>
<td>603.bwaves_s(base, peak)</td>
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**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems  

**Test Date:** Jan-2023  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

### Base Portability Flags

603.bwaves_s: -DSPEC_LP64  
607.cactuBSSN_s: -DSPEC_LP64  
619.lbm_s: -DSPEC_LP64  
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian  
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG  
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian -assume byterecl  
638.imagick_s: -DSPEC_LP64  
644.nab_s: -DSPEC_LP64  
649.fotonik3d_s: -DSPEC_LP64  
654.roms_s: -DSPEC_LP64

### Base Optimization Flags

#### C benchmarks:

```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Fortran benchmarks:

```bash
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Benchmarks using both Fortran and C:

```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

#### Benchmarks using Fortran, C, and C++:

```bash
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

### Peak Compiler Invocation

#### C benchmarks:

```bash
icx
```

(Continued on next page)
Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
619.lbm_s: basepeak = yes
638.imagick_s: basepeak = yes
644.nab_s: basepeak = yes

Fortran benchmarks:
649.fotonik3d_s: basepeak = yes
654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:
621.wrf_s: basepeak = yes
627.cam4_s: -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP

(Continued on next page)
SPEC CPU®2017 Floating Point Speed Result

Tyrone Systems
(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero IDI100C2R-28
(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017_fp_base = 123
SPECspeed®2017_fp_peak = 123

Tyrone Systems

(3.20 GHz, Intel Xeon Gold 5315Y)

SPECspeed®2017_fp_base = 123
SPECspeed®2017_fp_peak = 123

Peak Optimization Flags (Continued)

627.cam4_s (continued):
- nostandard-realloc-lhs -align array32byte -auto
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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