SPEC CPU®2017 Floating Point Rate Result

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ASUSTeK Computer Inc.

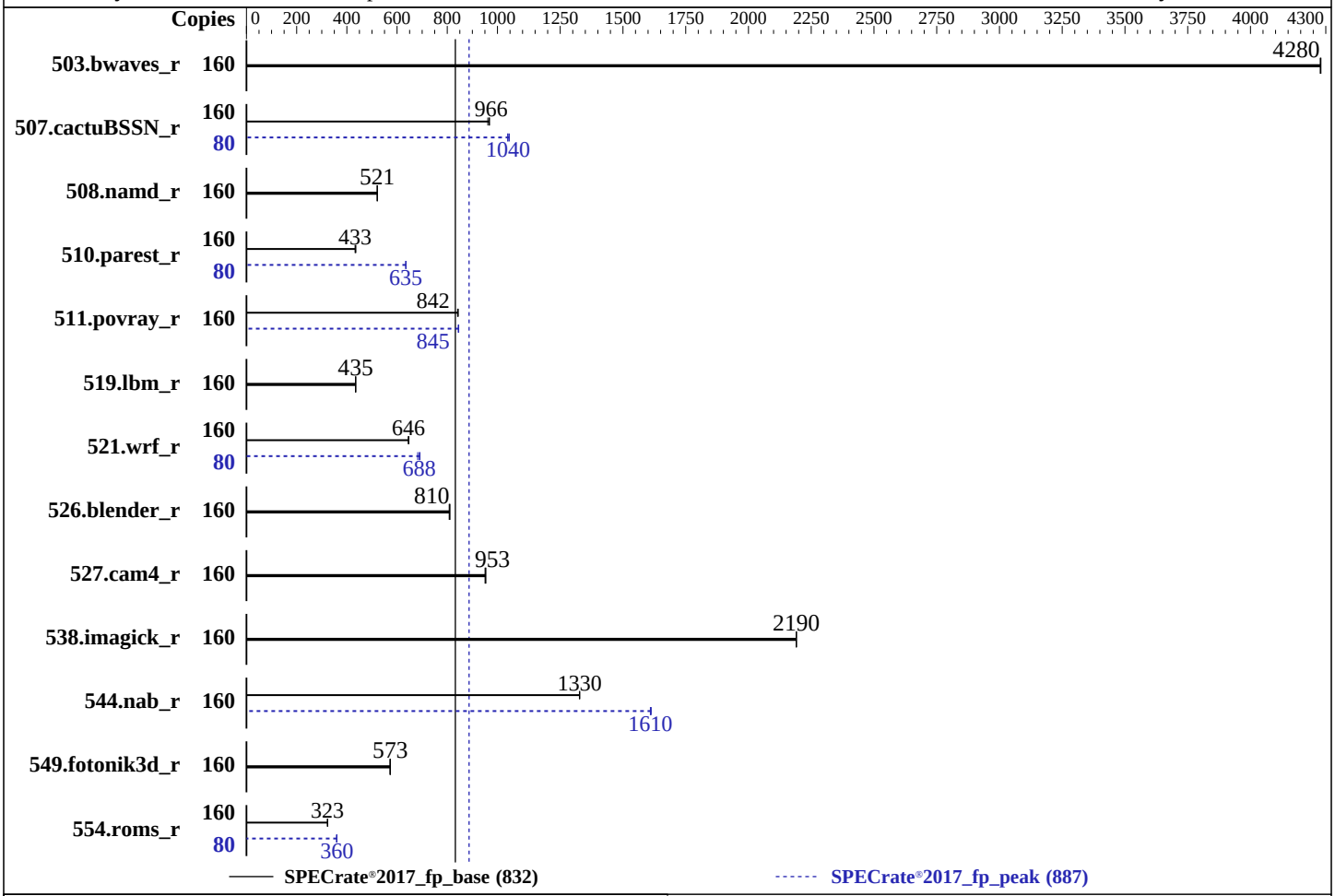
ASUS RS720-E11-RS12U
(2.00 GHz, Intel Xeon Platinum 8460Y+)

SPECrate®2017_fp_base = 832

SPECrate®2017_fp_peak = 887

CPU2017 License: 9016
Test Sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

Test Date: Mar-2023
Hardware Availability: Feb-2023
Software Availability: Dec-2022



Hardware

CPU Name:	Intel Xeon Platinum 8460Y+
Max MHz:	3700
Nominal:	2000
Enabled:	80 cores, 2 chips, 2 threads/core
Orderable:	1, 2 chip(s)
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	105 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage:	1 x 1.6 TB PCIE NVME SSD
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP4 (x86_64) Kernel 5.14.21-150400.22-default
Compiler:	C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 0503 released Feb-2023
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	160	**375**	**4280**	375	4280	375	4280	160	**375**	**4280**	375	4280	375	4280
507.cactuBSSN_r	160	209	969	211	961	**210**	**966**	80	96.7	1050	97.3	1040	**97.1**	**1040**
508.namd_r	160	292	521	**292**	**521**	292	521	160	292	521	**292**	**521**	292	521
510.parest_r	160	**966**	**433**	966	433	960	436	80	330	635	329	636	**330**	**635**
511.povray_r	160	443	844	**444**	**842**	444	841	160	442	846	443	844	**442**	**845**
519.lbm_r	160	388	435	**388**	**435**	387	436	160	388	435	**388**	**435**	387	436
521.wrf_r	160	556	645	554	647	**555**	**646**	80	263	682	259	691	**260**	**688**
526.blender_r	160	302	808	301	810	**301**	**810**	160	302	808	301	810	**301**	**810**
527.cam4_r	160	**294**	**953**	294	951	294	953	160	**294**	**953**	294	951	294	953
538.imagick_r	160	182	2190	182	2190	**182**	**2190**	160	182	2190	182	2190	**182**	**2190**
544.nab_r	160	203	1330	203	1330	**203**	**1330**	160	167	1610	**167**	**1610**	167	1610
549.fotonik3d_r	160	1087	574	**1088**	**573**	1091	571	160	1087	574	**1088**	**573**	1091	571
554.roms_r	160	788	322	786	324	**787**	**323**	80	**353**	**360**	353	360	355	358

SPECrate®2017_fp_base = 832

SPECrate®2017_fp_peak = 887

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
OS set to performance mode via cpupower frequency-set -g performance
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/cpu119/lib/intel64:/cpu119/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
```

(Continued on next page)

General Notes (Continued)

```
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Configuration:
VT-d = Disabled
Patrol Scrub = Disabled
SNC = Enable SNC4 (4-clusters)
Engine Boost = Aggressive
SR-IOV Support = Disabled
BMC Configuration:
Fan mode = Full speed mode

 Sysinfo program /cpu119/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Wed Mar  1 21:50:23 2023

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
```

(Continued on next page)

Platform Notes (Continued)

```
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   21:50:23 up  6:16,  2 users,  load average: 119.09, 149.50, 154.72
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                15:34    6:15m  1.01s  0.04s -bash
  root     tty2     -                15:34    5:56m  0.01s  0.01s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 4126744
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 4126744
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 -bash
 -bash
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 -c
   ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
    --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=160 --configfile
   ic2023.0-lin-core-avx512-rate-20221201.cfg --define smt-on --define cores=80 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
    --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.087/templogs/preenv.fprate.087.0.log --lognum 087.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /cpu119

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) Platinum 8460Y+
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 8
    microcode       : 0x2b000161
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
    cpu cores       : 40
    siblings        : 80
    2 physical ids (chips)
    160 processors (hardware threads)
    physical id 0: core ids 0-39
    physical id 1: core ids 0-39
    physical id 0: apicids 0-79
    physical id 1: apicids 128-207
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   46 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
```

(Continued on next page)

Platform Notes (Continued)

```
CPU(s):                          160
On-line CPU(s) list:             0-159
Vendor ID:                       GenuineIntel
Model name:                      Intel(R) Xeon(R) Platinum 8460Y+
CPU family:                      6
Model:                           143
Thread(s) per core:              2
Core(s) per socket:              40
Socket(s):                       2
Stepping:                        8
CPU max MHz:                     3700.0000
CPU min MHz:                     800.0000
BogoMIPS:                        4000.00
Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                 lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                 nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                 ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                 lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                 invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                                 tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                                 avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                 avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                 xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                 cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                 arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                                 ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                 tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                 enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                                 amx_tile flush_l1d arch_capabilities
Virtualization:                  VT-x
L1d cache:                       3.8 MiB (80 instances)
L1i cache:                       2.5 MiB (80 instances)
L2 cache:                        160 MiB (80 instances)
L3 cache:                        210 MiB (2 instances)
NUMA node(s):                    8
NUMA node0 CPU(s):               0-9,80-89
NUMA node1 CPU(s):               10-19,90-99
NUMA node2 CPU(s):               20-29,100-109
NUMA node3 CPU(s):               30-39,110-119
NUMA node4 CPU(s):               40-49,120-129
NUMA node5 CPU(s):               50-59,130-139
NUMA node6 CPU(s):               60-69,140-149
NUMA node7 CPU(s):               70-79,150-159
Vulnerability Itlb multihit:     Not affected
Vulnerability L1tf:              Not affected
```

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Platform Notes (Continued)

```
   Vulnerability Mds:                Not affected
   Vulnerability Meltdown:           Not affected
   Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
   Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
   Vulnerability Srbds:              Not affected
   Vulnerability Tsx async abort:    Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
      L1d       48K     3.8M   12 Data            1     64        1             64
      L1i       32K     2.5M    8 Instruction     1     64        1             64
      L2         2M     160M   16 Unified         2   2048        1             64
      L3       105M     210M   15 Unified         3 114688        1             64

 ------------------------------------------------------------
 8. numactl --hardware
 NOTE: a numactl 'node' might or might not correspond to a physical chip.
   available: 8 nodes (0-7)
   node 0 cpus: 0-9,80-89
   node 0 size: 128659 MB
   node 0 free: 115670 MB
   node 1 cpus: 10-19,90-99
   node 1 size: 129018 MB
   node 1 free: 120716 MB
   node 2 cpus: 20-29,100-109
   node 2 size: 129018 MB
   node 2 free: 120724 MB
   node 3 cpus: 30-39,110-119
   node 3 size: 129018 MB
   node 3 free: 120776 MB
   node 4 cpus: 40-49,120-129
   node 4 size: 129018 MB
   node 4 free: 120826 MB
   node 5 cpus: 50-59,130-139
   node 5 size: 129018 MB
   node 5 free: 120836 MB
   node 6 cpus: 60-69,140-149
   node 6 size: 128984 MB
   node 6 free: 120799 MB
   node 7 cpus: 70-79,150-159
   node 7 size: 128971 MB
   node 7 free: 120795 MB
   node distances:
   node   0   1   2   3   4   5   6   7
     0:  10  12  12  12  21  21  21  21
     1:  12  10  12  12  21  21  21  21
```

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Platform Notes (Continued)

```
     2:  12  12  10  12  21  21  21  21
     3:  12  12  12  10  21  21  21  21
     4:  21  21  21  21  10  12  12  12
     5:  21  21  21  21  12  10  12  12
     6:  21  21  21  21  12  12  10  12
     7:  21  21  21  21  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056471056 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 1 15:34

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ haveged
                   irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvmefc-boot-connections
                   postfix purge-kernels rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4
                   wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
                   firewalld gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmievd
                   issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap
                   nvmf-autoconnect rdisc rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts
                   snmpd snmptrapd svnserve systemd-boot-check-no-failures systemd-network-generator
                   systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
  indirect         wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
  root=UUID=1821a225-9785-4821-9a33-99bd3ded8cae
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
 14. cpupower frequency-info
   analyzing CPU 0:
     current policy: frequency should be within 800 MHz and 3.70 GHz.
                     The governor "performance" may decide which speed to use
                     within this range.
     boost state support:
       Supported: yes
       Active: yes

 ------------------------------------------------------------
 15. sysctl
   kernel.numa_balancing              1
   kernel.randomize_va_space          2
   vm.compaction_proactiveness       20
   vm.dirty_background_bytes          0
   vm.dirty_background_ratio         10
   vm.dirty_bytes                     0
   vm.dirty_expire_centisecs       3000
   vm.dirty_ratio                    20
   vm.dirty_writeback_centisecs     500
   vm.dirtytime_expire_seconds    43200
   vm.extfrag_threshold             500
   vm.min_unmapped_ratio              1
   vm.nr_hugepages                    0
   vm.nr_hugepages_mempolicy          0
   vm.nr_overcommit_hugepages         0
   vm.swappiness                     60
   vm.watermark_boost_factor      15000
   vm.watermark_scale_factor         10
   vm.zone_reclaim_mode               0

 ------------------------------------------------------------
 16. /sys/kernel/mm/transparent_hugepage
   defrag          always defer defer+madvise [madvise] never
   enabled         [always] madvise never
   hpage_pmd_size  2097152
   shmem_enabled   always within_size advise [never] deny force

 ------------------------------------------------------------
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs   60000
   defrag                      1
   max_ptes_none             511
   max_ptes_shared           256
   max_ptes_swap              64
   pages_to_scan            4096
   scan_sleep_millisecs    10000
```

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Platform Notes (Continued)

```
------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP4

------------------------------------------------------------
19. Disk information
SPEC is set to: /cpu119
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p8 xfs   1.3T   83G  1.2T   7% /

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         ASUSTeK COMPUTER INC.
    Product:        RS720-E11-RS12U
    Product Family: Server

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     16x Samsung M321R8GA0BB0-CQKVG 64 GB 2 rank 4800


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       American Megatrends Inc.
   BIOS Version:      0503
   BIOS Date:         01/31/2023
   BIOS Revision:     5.3
```

Compiler Version Notes

```
==============================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak)
                | 544.nab_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

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Compiler Version Notes (Continued)

```
==============================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak)
                | 554.roms_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
  2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
```

(Continued on next page)

Compiler Version Notes (Continued)

```
  2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
  Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:
```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:
```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int
-qopt-zmm-usage=high -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

(Continued on next page)

Peak Optimization Flags (Continued)

549.fotonik3d_r: `basepeak = yes`

554.roms_r: `-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

Benchmarks using both Fortran and C:

521.wrf_r: `-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

527.cam4_r: `basepeak = yes`

Benchmarks using both C and C++:

511.povray_r: `-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate`(pass 1) `-fprofile-use=default.profdata`(pass 2) `-xCORE-AVX2`(pass 1) `-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

526.blender_r: `basepeak = yes`

Benchmarks using Fortran, C, and C++:
`-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z13-V1.0.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml
http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-z13-V1.0.xml

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Tested with SPEC CPU®2017 v1.1.9 on 2023-03-01 08:50:23-0500.
Report generated on 2023-03-29 00:31:52 by CPU2017 PS formatter v6442.
Originally published on 2023-03-28.