### SPEC CPU®2017 Integer Rate Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
**Tyrone Camarero TDI100C3R-212**  
(2.00 GHz, Intel Xeon Gold 6338)

<table>
<thead>
<tr>
<th>Software</th>
<th>Hardware</th>
</tr>
</thead>
</table>
| OS: Red Hat Enterprise Linux release 8.5 (Ootpa)  
4.18.0-348.el8.x86_64 | CPU Name: Intel Xeon Gold 6338  
Max MHz: 3200  
Nominal: 2000 |
| Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2022.1 of Intel Fortran Compiler for Linux; | Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1.2 Chips |
| Parallel: No | Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 1.25 MB I+D on chip per core  
L3: 48 MB I+D on chip per chip |
| Firmware: Version PEGC0042 released Jan-2023 | Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
Storage: 1 x 512 GB NVMe SSD  
Other: None |
| File System: xfs | System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit |
| Power Management: BIOS and OS set to prefer performance at the cost of additional power usage. |

**CPU2017 License:** 006042  
**Test Sponsor:** Netweb Pte Ltd  
**Tested by:** Tyrone Systems  
**Test Date:** Mar-2023  
**Hardware Availability:** Apr-2021  
**Software Availability:** May-2022

| SPECrate®2017_int_base = 395 |
| SPECrate®2017_int_peak = 405 |

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>272</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>293</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>346</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>240</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>612</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>776</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>582</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>776</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>881</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>215</td>
</tr>
</tbody>
</table>

### Hardware

- **CPU Name:** Intel Xeon Gold 6338  
- **Max MHz:** 3200  
- **Nominal:** 2000  
- **Enabled:** 64 cores, 2 chips, 2 threads/core  
- **Orderable:** 1.2 Chips  
- **Cache L1:** 32 KB I + 48 KB D on chip per core  
- **Cache L2:** 1.25 MB I+D on chip per core  
- **Cache L3:** 48 MB I+D on chip per chip  
- **Other:** None  
- **Memory:** 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
- **Storage:** 1 x 512 GB NVMe SSD  
- **Other:** None
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>128</td>
<td>748</td>
<td>272</td>
<td>748</td>
<td>272</td>
<td>753</td>
<td>270</td>
<td>128</td>
<td>694</td>
<td>294</td>
<td>696</td>
<td>293</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>128</td>
<td>598</td>
<td>303</td>
<td>596</td>
<td>304</td>
<td>597</td>
<td>304</td>
<td>128</td>
<td>523</td>
<td>346</td>
<td>523</td>
<td>346</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>128</td>
<td>338</td>
<td>612</td>
<td>338</td>
<td>612</td>
<td>339</td>
<td>610</td>
<td>128</td>
<td>338</td>
<td>612</td>
<td>338</td>
<td>612</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>128</td>
<td>700</td>
<td>240</td>
<td>700</td>
<td>240</td>
<td>706</td>
<td>238</td>
<td>128</td>
<td>700</td>
<td>240</td>
<td>700</td>
<td>240</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>128</td>
<td>233</td>
<td>581</td>
<td>232</td>
<td>582</td>
<td>232</td>
<td>582</td>
<td>128</td>
<td>233</td>
<td>581</td>
<td>232</td>
<td>582</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>128</td>
<td>288</td>
<td>777</td>
<td>289</td>
<td>776</td>
<td>290</td>
<td>773</td>
<td>128</td>
<td>277</td>
<td>809</td>
<td>277</td>
<td>808</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>128</td>
<td>484</td>
<td>303</td>
<td>484</td>
<td>303</td>
<td>487</td>
<td>301</td>
<td>128</td>
<td>484</td>
<td>303</td>
<td>484</td>
<td>303</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>128</td>
<td>723</td>
<td>293</td>
<td>720</td>
<td>295</td>
<td>726</td>
<td>292</td>
<td>128</td>
<td>723</td>
<td>293</td>
<td>720</td>
<td>295</td>
</tr>
<tr>
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<td>128</td>
<td>381</td>
<td>881</td>
<td>380</td>
<td>882</td>
<td>381</td>
<td>880</td>
<td>128</td>
<td>381</td>
<td>881</td>
<td>380</td>
<td>882</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
<td>638</td>
<td>217</td>
<td>643</td>
<td>215</td>
<td>648</td>
<td>213</td>
<td>128</td>
<td>638</td>
<td>217</td>
<td>643</td>
<td>215</td>
</tr>
</tbody>
</table>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk_r / 623.xalancbmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
Malloc_CONF = "retain:true"
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CPU2017 License: 006042
Test Sponsor: Netweb Pte Ltd
Tested by: Tyrone Systems

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the
system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the
system as tested and documented.
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the
system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS Settings:
Power Technology = Custom
Power Performance Tuning = BIOS Controls EPB
ENETF⽐E_PERF_BIAS_CFG mode = Extreme Performance
NUMA (Sub NUMA) = Enable
KTI Prefetch = Enable
LLC Dead Line Alloc = Disable
Hyper-Threading = Enabled

SysInfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 0e91c89b7e5d5c36ae2c92cc097bec197
running on Tyronespec Mon Mar  6 13:28:00 2023

SUT (System Under Test) info as seen by some common utilities.

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10. who -r
11. systemd service manager version: systemd 239 (239-51.el8)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
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16. sysctl
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(Test Sponsor: Netweb Pte Ltd)
Tyrone Camarero TD1100C3R-212
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Test Sponsor: Netweb Pte Ltd
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Test Date: Mar-2023
Hardware Availability: Apr-2021
Software Availability: May-2022

Platform Notes (Continued)

22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

25. uname -a
   Linux Tyronespec 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021 x86_64 x86_64 x86_64 GNU/Linux

26. w
   13:28:00 up 10 min, 1 user, load average: 0.01, 0.01, 0.00
   USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
   root     tty1     -                13:19    6.00s  1.27s  0.01s -bash

27. Username
   From environment variable $USER: root

28. ulimit -a
   core file size          (blocks, -c) 0
   data seg size           (kbytes, -d) unlimited
   scheduling priority     (-e) 0
   file size               (blocks, -f) unlimited
   pending signals         (-I) 4126627
   max locked memory       (kbytes, -l) 64
   max memory size         (kbytes, -m) unlimited
   open files              (-n) 1024
   pipe size               (512 bytes, -p) 8
   POSIX message queues    (bytes, -q) 819200
   real-time priority      (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time                (seconds, -t) unlimited
   max user processes      (-u) 4126627
   virtual memory          (kbytes, -v) unlimited
   file locks              (-x) unlimited

29. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 18
   login -- root
   -bash
   -bash
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
   ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=64 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base, peak -o all intrate
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
   ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=64 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base, peak --output_format all --nopower
   --runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
   specperl $SPEC/bin/sysinfo
   $SPEC = /home/cpu2017

30. /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 6338 CPU @ 2.00GHz
   vendor_id : GenuineIntel
   cpu family : 6

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

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SPECrate®2017_int_base = 395
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Platform Notes (Continued)

model : 106
stepping : 6
microcode : 0xd0002e0
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores : 32
siblings : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.32.1:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Byte Order: Little Endian
CPU(s): 128
On-line CPU(s) list: 0-127
Thread(s) per core: 2
Core(s) per socket: 32
Socket(s): 2
NUMA node(s): 2
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
CPU family: 6
Model: 106
Model name: Intel(R) Xeon(R) Gold 6338 CPU @ 2.00GHz
BIOS Model name: Intel(R) Xeon(R) Gold 6338 CPU @ 2.00GHz
Stepping: 6
CPU MHz: 2000.000
CPU max MHz: 3200.0000
CPU min MHz: 800.0000
BogoMIPS: 800.00
Virtualization: VT-x
L1d cache: 48K
L1i cache: 32K
L2 cache: 1280K
L3 cache: 49152K
NUMA node0 CPU(s): 0-31,64-95
NUMA node1 CPU(s): 32-63,96-127
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts efer ts efer tc efer tseg kseg kgdb realridge xtopology

(Continued on next page)
Platform Notes (Continued)

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
   available: 2 nodes (0-1)
       node 0 cpus: 0-31,64-95
       node 0 size: 515673 MB
       node 0 free: 514444 MB
       node 1 cpus: 32-63,96-127
       node 1 size: 516042 MB
       node 1 free: 515209 MB
       node distances:
            node   0   1
        0:  10  20
        1:  20  10

9. /proc/meminfo
    MemTotal:          1056476736 kB

10. who -r
    run-level 3 Mar 6 13:18

11. Systemd service manager version: systemd 239 (239-51.el8)
    Default Target:    Status
        multi-user    running

12. Services, from systemctl list-unit-files
    STATE     UNIT FILES
    enabled   ModemManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon
              atd auditd autovt@ avahi-daemon bluetooth chrony crond cups display-manager firewall-d gdm
              getty@ import-state intel-smb-hbbalance iscsi iscsi-onboot kdump ksm ksmtuned
              libbusorg trigger loadmodules lvm2-monitor mcelog mdmonitor microcode multipath
              nis-domainname nvidia-hibernate nvidia-resume nvidia-suspend nvmefc-boot-connections
              ostree-remount qemu-guest-agent rshremote rdpcbind rsyslog rtkit-daemon sarinux-autorelabel-mark
              sep5 smartd sssd syslog timedatex tuned ulidisk2 vdo vgauthd vntool
    disabled   arp-esters blk-availability brutty canberra-system-bootup canberra-system-shutdown
              canberra-system-shutdown-reboot chrony-wait console-getty cpupower cups-browsed debug-shell
              dnsmasq ebtables gasproxy httpd httpd@ initial-setup initial-setup-reconfiguration iprump
t              iprumpdate iscsid icontol iscsiui kshat kvm_stat load mon-db restart cache update
              ndct1-monitor netcf-transaction nfs-blkmap nfs-convert nfs-server nttables numad nvidia-powerd
              nvmf-ssdconnect oddjobd podman podman-auto-update podman-restart psacct radvd ras-mc-ctl
              rasdaemon rdisc rhcd rhsm-facts saslauthd serial-getty@ snmpd snmptramp speech-dispatcher
              ssdhd-keygen@ switcheroo-control systemd-npawm@ systemd-resolved tcsh tog-pegasus upower
              virtinterfaced virtnetworkd virtnodeodev virtnfilterd virtproxyd virtqemuede virtsecreted
              virtstorage wpa_supplicant
    generated  SystemTap compile-server gcc-toolset-10-stap-server gcc-toolset-10-systemtap
              gcc-toolset-11-stap-server gcc-toolset-11-systemtap gcc-toolset-9-stap-server
              gcc-toolset-9-systemtap scripts startup
    indirect  spice-vdagentd ssad-autofs ssad-kcm ssad-nss ssad-pac ssad-pam ssad-ssh ssad-sudo virtlockd
              virtlogd
    masked   systemd-timedated

13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/dev/mapper/rhel-root
    root=/dev/mapper/rhel-root
Platform Notes (Continued)

```
resume=/dev/mapper/rhel-swap
rhgb
quiet
```

14. cpupower frequency-info
   analyzing CPU 0:
   current policy: frequency should be within 800 MHz and 3.20 GHz.
   The governor "performance" may decide which speed to use
   within this range.
   boost state support:
   Supported: yes
   Active: yes

15. tuned-adm active
   Current active profile: throughput-performance

16. sysct1
   kernel.numa_balancing               1
   kernel.randomize_va_space           2
   vm.compaction_proactiveness         0
   vm.dirty_background_bytes          0
   vm.dirty_background_ratio          10
   vm.dirty_bytes                     0
   vm.dirty_expire_centisecs          3000
   vm.dirty_ratio                     40
   vm.dirty_writeback_centisecs       500
   vm.dirtytime_expire_seconds        43200
   vm.extfrag_threshold               500
   vm.min_unmapped_ratio              1
   vm.nr_hugepages                    0
   vm.nr_hugepages_mempolicy          0
   vm.nr_overcommit_hugepages         0
   vm.swappiness                      10
   vm.watermark_boost_factor          15000
   vm.watermark_scale_factor          10
   vm.zone_reclaim_mode               0

17. /sys/kernel/mm/transparent_hugepage
   defrag always defer defer+madvise [madvise] never
   enabled [always] madvise never
   hpage_pmd_size 2097152
   shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/hugepaged
   alloc_sleep_millisecs 600000
   defrag 1
   max_ptes_none 511
   max_ptes_swap 64
   pages_to_scan 4096
   scan_sleep_millisecs 10000
```

19. OS release

(Continued on next page)
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Platform Notes (Continued)

20. Kernel self-reported vulnerability status, from /sys/devices/system/cpu/vulnerabilities

<table>
<thead>
<tr>
<th>Vulnerability</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>itlb_multihit</td>
<td>Not affected</td>
</tr>
<tr>
<td>l1tf</td>
<td>Not affected</td>
</tr>
<tr>
<td>meltdown</td>
<td>Not affected</td>
</tr>
<tr>
<td>spec_store_bypass</td>
<td>Mitigation: Speculative Store Bypass disabled via prctl and seccomp</td>
</tr>
<tr>
<td>spectre_v1</td>
<td>Mitigation: usercopy/swapgs barriers and __user pointer sanitization</td>
</tr>
<tr>
<td>spectre_v2</td>
<td>Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling</td>
</tr>
<tr>
<td>srbds</td>
<td>Not affected</td>
</tr>
<tr>
<td>tsx_async_abort</td>
<td>Not affected</td>
</tr>
</tbody>
</table>

For more information, see the Linux documentation on hardware vulnerabilities, for example https://www.kernel.org/doc/html/latest/admin-guide/hw-vuln/index.html

21. Disk information

SPEC is set to: /home/cpu2017
Filesystem            Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home  xfs   402G  209G  194G  52% /home

22. /sys/devices/virtual/dmi/id

Vendor:       Tyrone Systems
Product:      TDI100C3R-212
Product Family: Family
Serial:       2X20022302

23. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.

Memory:
16x Micron 36ASF8G72PZ-3G2F1 64 GB 2 rank 3200

24. BIOS

This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: American Megatrends International, LLC.
BIOS Version: PEGC0042
BIOS Date: 01/16/2023
BIOS Revision: 5.22

Compiler Version Notes

C | 502.gcc_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

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### Compiler Version Notes (Continued)

```
C       500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)  
--------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

--------------------------------------------
C       502.gcc_r(peak)  
--------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316  
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--------------------------------------------
C       500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak) 557.xz_r(base, peak)  
--------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
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--------------------------------------------
C++     520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepjeng_r(base, peak) 541.leela_r(base, peak)  
--------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
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--------------------------------------------
Fortran  548.exchange2_r(base, peak)  
--------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
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```

### Base Compiler Invocation

- **C benchmarks:** icx
- **C++ benchmarks:** icpx
- **Fortran benchmarks:** ifx
## SPEC CPU®2017 Integer Rate Result

**Tyrone Systems**  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero TDI100C3R-212  
(2.00 GHz, Intel Xeon Gold 6338)  

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>395</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>405</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>CPU2017 License</th>
<th>006042</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor</td>
<td>Netweb Pte Ltd</td>
</tr>
<tr>
<td>Tested by</td>
<td>Tyrone Systems</td>
</tr>
<tr>
<td>Test Date</td>
<td>Mar-2023</td>
</tr>
<tr>
<td>Hardware Availability</td>
<td>Apr-2021</td>
</tr>
<tr>
<td>Software Availability</td>
<td>May-2022</td>
</tr>
</tbody>
</table>

### Base Portability Flags

- 500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
- 502.gcc_r: -DSPEC_LP64
- 505.mcf_r: -DSPEC_LP64
- 520.omnetpp_r: -DSPEC_LP64
- 523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
- 525.x264_r: -DSPEC_LP64
- 531.deepsjeng_r: -DSPEC_LP64
- 541.leela_r: -DSPEC_LP64
- 548.exchange2_r: -DSPEC_LP64
- 557.xz_r: -DSPEC_LP64

### Base Optimization Flags

**C benchmarks:**  
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin -lqkmalloc

**C++ benchmarks:**  
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin -lqkmalloc

**Fortran benchmarks:**  

### Peak Compiler Invocation

**C benchmarks:**  
icx

**C++ benchmarks:**  
icpx

**Fortran benchmarks:**  
ifx
**SPEC CPU®2017 Integer Rate Result**

Tyrone Systems  
(Test Sponsor: Netweb Pte Ltd)  
Tyrone Camarero TDI100C3R-212  
(2.00 GHz, Intel Xeon Gold 6338)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 395</th>
<th>SPECrate®2017_int_peak = 405</th>
</tr>
</thead>
</table>

CPU2017 License: 006042  
Test Sponsor: Netweb Pte Ltd  
Tested by: Tyrone Systems

<table>
<thead>
<tr>
<th>Test Date: Mar-2023</th>
<th>Hardware Availability: Apr-2021</th>
<th>Software Availability: May-2022</th>
</tr>
</thead>
</table>

**Peak Portability Flags**

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64

**Peak Optimization Flags**

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile=generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc

502.gcc_r: -m32  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin  
-std=gnu89 -Wl,-z,muldefs -fprofile=generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib  
-ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

(Continued on next page)
SPEC CPU®2017 Integer Rate Result
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Tyrone Camarero TDI100C3R-212
(2.00 GHz, Intel Xeon Gold 6338)

SPECrate®2017_int_base = 395
SPECrate®2017_int_peak = 405

Download information:
https://www.spec.org/cpu2017

CPU2017 License: 006042
Test Date: Mar-2023
Test Sponsor: Netweb Pte Ltd
Hardware Availability: Apr-2021
Tested by: Tyrone Systems
Software Availability: May-2022

Peak Optimization Flags (Continued)

520.omnetpp_r: basepeak = yes
523.xalancbmk_r: basepeak = yes
531.deepsjeng_r: basepeak = yes
541.leela_r: basepeak = yes

Fortran benchmarks:
548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-ICX-revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-03-06 08:27:59-0500.
Originally published on 2023-03-28.