SPEC CPU®2017 Floating Point Rate Result

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Cisco Systems

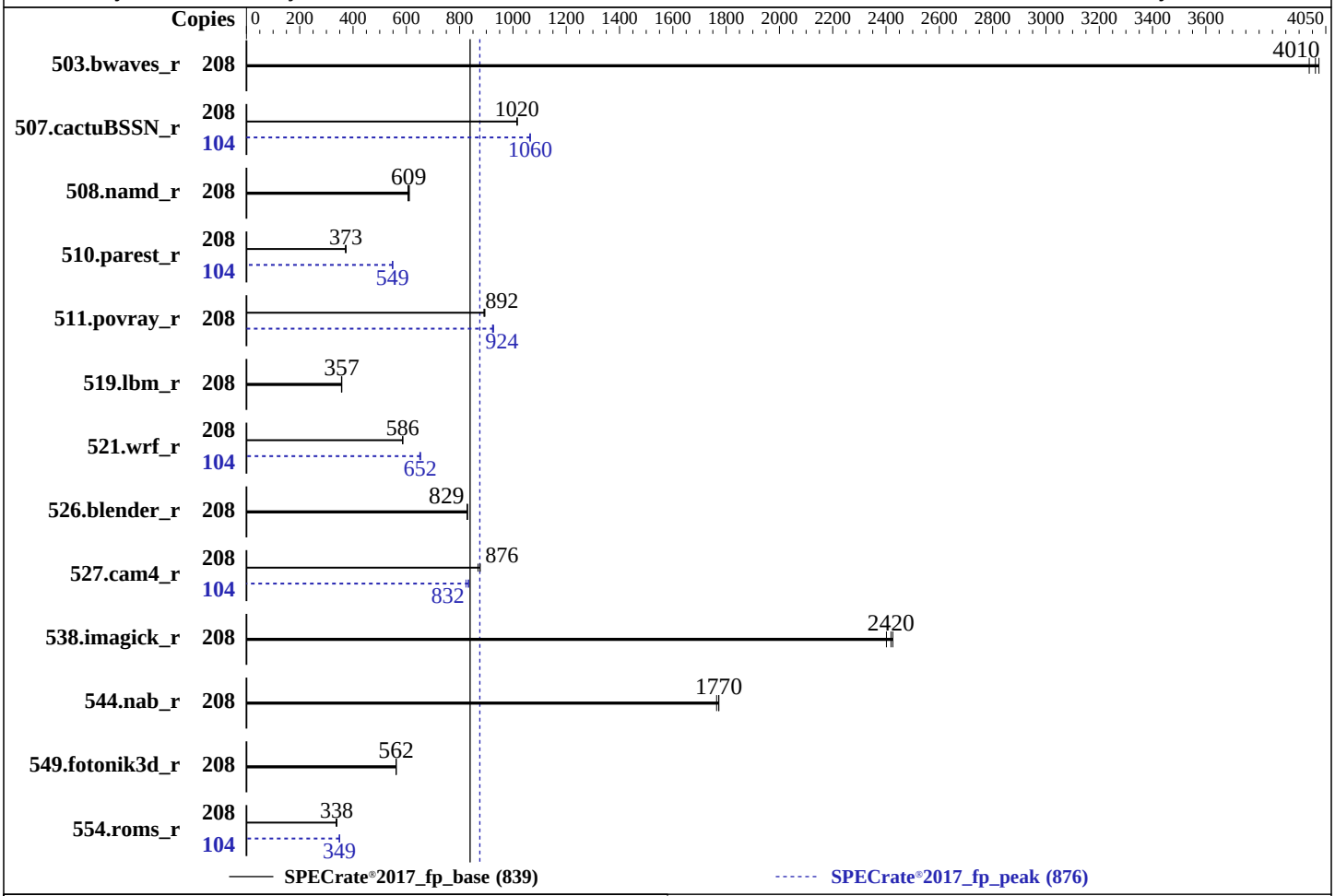
Cisco UCS X210C M7 (Intel Xeon Platinum 8470N, 1.70GHz)

SPECrate®2017_fp_base = 839

SPECrate®2017_fp_peak = 876

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Aug-2023
Hardware Availability: Mar-2023
Software Availability: Dec-2022



Hardware

CPU Name:	Intel Xeon Platinum 8470N
Max MHz:	3600
Nominal:	1700
Enabled:	104 cores, 2 chips, 2 threads/core
Orderable:	1,2 Chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	97.5 MB I+D on chip per chip
Other:	None
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
Storage:	1 x 240 GB M.2 SSD SATA
Other:	None

Software

OS:	SUSE Linux Enterprise Server 15 SP4 5.14.21-150400.22-default
Compiler:	C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 4.3.2a released Jul-2023
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	208	518	4020	523	3990	**520**	**4010**	208	518	4020	523	3990	**520**	**4010**
507.cactuBSSN_r	208	259	1020	**259**	**1020**	259	1020	104	**124**	**1060**	124	1060	124	1070
508.namd_r	208	**325**	**609**	323	611	326	606	208	**325**	**609**	323	611	326	606
510.parest_r	208	1465	371	**1458**	**373**	1458	373	104	497	547	**496**	**549**	496	549
511.povray_r	208	543	895	**544**	**892**	545	892	208	524	927	**526**	**924**	526	923
519.lbm_r	208	**613**	**357**	615	357	613	358	208	**613**	**357**	615	357	613	358
521.wrf_r	208	794	586	795	586	**795**	**586**	104	357	653	**357**	**652**	358	650
526.blender_r	208	382	830	383	828	**382**	**829**	208	382	830	383	828	**382**	**829**
527.cam4_r	208	419	869	415	877	**415**	**876**	104	221	824	**219**	**832**	218	833
538.imagick_r	208	213	2430	215	2400	**214**	**2420**	208	213	2430	215	2400	**214**	**2420**
544.nab_r	208	198	1770	198	1760	**198**	**1770**	208	198	1770	198	1760	**198**	**1770**
549.fotonik3d_r	208	1444	561	**1443**	**562**	1443	562	208	1444	561	**1443**	**562**	1443	562
554.roms_r	208	982	336	975	339	**977**	**338**	104	473	349	474	349	**474**	**349**

SPECrate®2017_fp_base = 839

SPECrate®2017_fp_peak = 876

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
 Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
 memory using Red Hat Enterprise Linux 8.4
 Transparent Huge Pages enabled by default
 Prior to runcpu invocation
 Filesystem page cache synced and cleared with:
 sync; echo 3>       /proc/sys/vm/drop_caches
 runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
```

(Continued on next page)

General Notes (Continued)

```
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Settings:
Adjacent Cache Line Prefetcher set to Enabled
DCU streamer Prefetch set to Enabled
Enhanced CPU Performance set to Auto
LLC Dead Line set to Disabled
ADDDC Sparing set to Disabled
Processor C6 Report set to Enabled


 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Wed Aug  2 21:12:34 2023

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. sysctl
 16. /sys/kernel/mm/transparent_hugepage
 17. /sys/kernel/mm/transparent_hugepage/khugepaged
 18. OS release
 19. Disk information
 20. /sys/devices/virtual/dmi/id
 21. dmidecode
 22. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
   x86_64 x86_64 x86_64 GNU/Linux

 ------------------------------------------------------------
 2. w
```

(Continued on next page)

Platform Notes (Continued)

```
   21:12:34 up 1 min,  1 user,  load average: 2.98, 1.55, 0.59
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                21:11    9.00s  1.45s  0.31s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 4126730
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 4126730
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 -bash
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 -c
   ic2023.0-lin-sapphirerapids-rate-20221201.cfg --reportable --iterations 3 --define smt-on --define
   cores=104 --define physicalfirst --define invoke_with_interleave --define drop_caches --tune all -o all
   fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 --configfile
   ic2023.0-lin-sapphirerapids-rate-20221201.cfg --reportable --iterations 3 --define smt-on --define
   cores=104 --define physicalfirst --define invoke_with_interleave --define drop_caches --tune all
   --output_format all --nopower --runmode rate --tune base:peak --size refrate fprate --nopreenv
   --note-preenv --logfile $SPEC/tmp/CPU2017.022/templogs/preenv.fprate.022.0.log --lognum 022.0
   --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) Platinum 8470N
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 143
    stepping        : 8
    microcode       : 0x2b000461
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
    cpu cores       : 52
    siblings        : 104
    2 physical ids (chips)
    208 processors (hardware threads)
    physical id 0: core ids 0-51
```

(Continued on next page)

Platform Notes (Continued)

```
    physical id 1: core ids 0-51
    physical id 0: apicids 0-103
    physical id 1: apicids 128-231
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.2:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   46 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          208
  On-line CPU(s) list:             0-207
  Vendor ID:                       GenuineIntel
  Model name:                      Intel(R) Xeon(R) Platinum 8470N
  CPU family:                      6
  Model:                           143
  Thread(s) per core:              2
  Core(s) per socket:              52
  Socket(s):                       2
  Stepping:                        8
  CPU max MHz:                     3600.0000
  CPU min MHz:                     800.0000
  BogoMIPS:                        3400.00
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                                   sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                                   lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                                   invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
                                   tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
                                   avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                                   avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                                   xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                   cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                   arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku
                                   ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                                   tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                                   enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                                   amx_tile flush_l1d arch_capabilities
  Virtualization:                  VT-x
  L1d cache:                       4.9 MiB (104 instances)
  L1i cache:                       3.3 MiB (104 instances)
  L2 cache:                        208 MiB (104 instances)
  L3 cache:                        195 MiB (2 instances)
  NUMA node(s):                    8
  NUMA node0 CPU(s):               0-12,104-116
  NUMA node1 CPU(s):               13-25,117-129
  NUMA node2 CPU(s):               26-38,130-142
  NUMA node3 CPU(s):               39-51,143-155
  NUMA node4 CPU(s):               52-64,156-168
  NUMA node5 CPU(s):               65-77,169-181
  NUMA node6 CPU(s):               78-90,182-194
  NUMA node7 CPU(s):               91-103,195-207
  Vulnerability Itlb multihit:     Not affected
```

(Continued on next page)

Platform Notes (Continued)

```
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     4.9M   12 Data            1     64        1             64
     L1i       32K     3.3M    8 Instruction     1     64        1             64
     L2         2M     208M   16 Unified         2   2048        1             64
     L3      97.5M     195M   15 Unified         3 106496        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 8 nodes (0-7)
  node 0 cpus: 0-12,104-116
  node 0 size: 128666 MB
  node 0 free: 127561 MB
  node 1 cpus: 13-25,117-129
  node 1 size: 129017 MB
  node 1 free: 128539 MB
  node 2 cpus: 26-38,130-142
  node 2 size: 129017 MB
  node 2 free: 128562 MB
  node 3 cpus: 39-51,143-155
  node 3 size: 129017 MB
  node 3 free: 128507 MB
  node 4 cpus: 52-64,156-168
  node 4 size: 129017 MB
  node 4 free: 128634 MB
  node 5 cpus: 65-77,169-181
  node 5 size: 129017 MB
  node 5 free: 128556 MB
  node 6 cpus: 78-90,182-194
  node 6 size: 128982 MB
  node 6 free: 128581 MB
  node 7 cpus: 91-103,195-207
  node 7 size: 128970 MB
  node 7 free: 128354 MB
  node distances:
  node   0   1   2   3   4   5   6   7
    0:  10  12  12  12  21  21  21  21
    1:  12  10  12  12  21  21  21  21
    2:  12  12  10  12  21  21  21  21
    3:  12  12  12  10  21  21  21  21
    4:  21  21  21  21  10  12  12  12
    5:  21  21  21  21  12  10  12  12
    6:  21  21  21  21  12  12  10  12
    7:  21  21  21  21  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1056467280 kB

------------------------------------------------------------
```

(Continued on next page)

Platform Notes (Continued)

```
10. who -r
  run-level 3 Aug 2 21:11

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
                   issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                   smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
                   firewalld gpm grub2-once haveged-switch-root ipmi ipmievd issue-add-ssh-keys kexec-load
                   ksm kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck
                   rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd svnserve
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2
  indirect         wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
  root=UUID=105eebe6-343f-430f-8199-ebf93f502b47
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 3.60 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
```

(Continued on next page)

Platform Notes (Continued)

```
  vm.swappiness                     1
  vm.watermark_boost_factor     15000
  vm.watermark_scale_factor        10
  vm.zone_reclaim_mode              0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP4

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017
  Filesystem     Type   Size  Used Avail Use% Mounted on
  /dev/sda2      btrfs  222G   13G  208G   6% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Cisco Systems Inc
    Product:        UCSX-210C-M7
    Serial:         FCH270978FN

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x 0xCE00 M321R8GA0BB0-CQKDG 64 GB 2 rank 4800


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Cisco Systems, Inc.
   BIOS Version:      X210M7.4.3.2a.0.0710230213
   BIOS Date:         07/10/2023
   BIOS Revision:     5.29
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
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--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

(Continued on next page)

Base Compiler Invocation (Continued)

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-SPR-revJ.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml
http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-V1.0-SPR-revJ.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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