IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

<table>
<thead>
<tr>
<th>SPECspeed®2017_int_base</th>
<th>SPECspeed®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>15.0</td>
<td>15.2</td>
</tr>
</tbody>
</table>

CPU2017 License: 3358
Test Sponsor: IEIT Systems Co., Ltd.
Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2023
Hardware Availability: Apr-2023
Software Availability: Dec-2022

Threads

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>SPECspeed®2017_int_peak</th>
<th>SPECspeed®2017_int_base</th>
</tr>
</thead>
<tbody>
<tr>
<td>600.perlbench_s</td>
<td>16</td>
<td>10.6</td>
<td>9.95</td>
</tr>
<tr>
<td>602.gcc_s</td>
<td>16</td>
<td>12.0</td>
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<td>605.mcf_s</td>
<td>16</td>
<td>23.5</td>
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</tr>
<tr>
<td>620.omnetpp_s</td>
<td>16</td>
<td>9.92</td>
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</tr>
<tr>
<td>623.xalancbmk_s</td>
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<tr>
<td>625.x264_s</td>
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<tr>
<td>631.deepsjeng_s</td>
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<tr>
<td>641.leela_s</td>
<td>16</td>
<td>6.12</td>
<td></td>
</tr>
<tr>
<td>648.exchange2_s</td>
<td>16</td>
<td>27.2</td>
<td></td>
</tr>
<tr>
<td>657.xz_s</td>
<td>16</td>
<td>23.8</td>
<td></td>
</tr>
</tbody>
</table>

**Hardware**

CPU Name: Intel Xeon Gold 6434
Max MHz: 4100
Nominal: 3700
Enabled: 16 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 22.5 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 1 TB NVME SSD
Other: None

**Software**

OS: Red Hat Enterprise Linux 9.0 (Plow) 5.14.0-70.22.1.el9_0.x86_64
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Version 03.01.00 released Dec-2022
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.
### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Threads</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
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<tbody>
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<td>9.96</td>
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<tr>
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<td><strong>12.0</strong></td>
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<td>332</td>
<td>12.0</td>
<td>16</td>
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<td><strong>12.4</strong></td>
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<td><strong>23.5</strong></td>
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<tr>
<td>620.omnetpp_s</td>
<td>16</td>
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<td>9.95</td>
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<tr>
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<td>641.leela_s</td>
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<td><strong>27.2</strong></td>
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<td>657.xz_s</td>
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<td>23.8</td>
<td>259</td>
<td>23.8</td>
<td><strong>260</strong></td>
</tr>
</tbody>
</table>

**Compiler Notes**

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalanchmk_r / 623.xalanchmk_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

**Operating System Notes**

Stack size set to unlimited using "ulimit -s unlimited"

**Environment Variables Notes**

Environment variables set by runcpu before the start of the run:
- KMP_AFFINITY = "granularity=fine,scatter"
- LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
- MALLOC_CONF = "retain:true"
- OMP_STACKSIZE = "192M"

**General Notes**

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

(Continued on next page)
IEIT Systems Co., Ltd.
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General Notes (Continued)

Filesystem page cache synced and cleared with:
```
sync; echo 3 > /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.


Platform Notes

BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
Hyper Threading set to disable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Oct 10 11:44:57 2023

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numacl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.e19_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

---

1. uname -a
Linux localhost 5.14.0-70.22.1.e19_0.x86_64 #1 SMP PREEMPT Tue Aug 2 10:02:12 EDT 2022 x86_64 x86_64 x86_64

(Continued on next page)
IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

CPU2017 License: 3358
Test Sponsor: IEIT Systems Co., Ltd.
Tested by: IEIT Systems Co., Ltd.

Platform Notes (Continued)

GNU/Linux

2. w

11:44:57 up 2 min, 1 user, load average: 0.05, 0.02, 0.00
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty2 11:44 9.00s 0.73s 0.00s sh
reportable-ic2023.0-lin-sapphirerapids-speed-smt-off-20221201.sh

3. Username

From environment variable $USER: root

4. ulimit -a

real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
file size (blocks, -f) unlimited
pending signals (-i) 2062294
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062294
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry

/usr/lib/systemd/systemd --switched-root --system --deserialize 28
login root
bash
sh reportable-ic2023.0-lin-sapphirerapids-speed-smt-off-20221201.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak -o all --define
intspeedaffinity --define drop_caches intspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak --output_format all
--define intspeedaffinity --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed
intspeed --nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.004/templogs/preenv.intspeed.004.0.log
--lognum 004.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

6. /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6434
vendor_id : GenuineIntel
cpu family : 6
model : 143
stepping : 7
microcode : 0x2b000130
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores : 8
Platform Notes (Continued)

siblings : 8
2 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apic ids 0,2,4,6,8,10,12,14
physical id 1: apic ids 128,130,132,134,136,138,140,142
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.4:

Architecture:                    x86_64
CPU op-mode(s):                  32-bit, 64-bit
Address sizes:                   52 bits physical, 57 bits virtual
Byte Order:                      Little Endian
CPU(s):                          16
On-line CPU(s) list:             0-15
Vendor ID:                       GenuineIntel
BIOS Vendor ID:                  Intel(R) Corporation
Model name:                      Intel(R) Xeon(R) Gold 6434
BIOS Model name:                 Intel(R) Xeon(R) Gold 6434
CPU family:                      6
Model:                           143
Thread(s) per core:              1
Core(s) per socket:              8
Socket(s):                       2
Stepping:                        7
CPU max MHz:                     4100.0000
CPU min MHz:                     800.0000
BogoMIPS:                        7400.00
Flags:
  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
  clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
  lm constant_tsc arch_perfmon pebs bts rep_good nopl xtopology
  nonstop_tsc tsc_deadline_timer aes xsave avx f16c rdrand
  lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_d3 cpd lpcc
  invpcid_single intel_pni cpd_c1 lrs bb mca ibpb stdbp ibs_vms enabled
  tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2
  smep bmi2 erms invpcid cmqm cmq rol2 pmcpix
  cmov pdcm pdcm vse8 avx512f avx512dq avx512cf16 wbnoinvd dtherm ida
  arat prlts hwp hwp_act_window hwp_epp hwp_apic_reg avx512ebi umip pku
  ospke waite pkg avx512_vmbi2 gfnl vaes vperm128dq avx512_512d tme
  avx512_vpcomment a157 rdpd bus_lock_detect cidemote movdiri movdiri64b
  enqcmd fism md_clear serialize tsxidtrk pconfig arch_lbr avx512_fp16
  amx_tile flush_l1d arch_capabilities

Virtualization:                  VT-x
L1d cache:                       768 KB (16 instances)
L1i cache:                       512 KB (16 instances)
L2 cache:                        32 MiB (16 instances)
L3 cache:                        45 MiB (2 instances)
NUMA node(s):                    2
NUMA node0 CPU(s):               0-7
NUMA node1 CPU(s):               8-15
Vulnerability Itlb multihit:     Not affected

(Continued on next page)
### SPEC CPU®2017 Integer Speed Result

**IEIT Systems Co., Ltd.**

NF5280M7 (Intel Xeon Gold 6434)

**SPECspeed®2017_int_base = 15.0**

**SPECspeed®2017_int_peak = 15.2**

**CPU2017 License:** 3358

**Test Sponsor:** IEIT Systems Co., Ltd.

**Tested by:** IEIT Systems Co., Ltd.

**Test Date:** Oct-2023

**Hardware Availability:** Apr-2023

**Software Availability:** Dec-2022

---

### Platform Notes (Continued)

<table>
<thead>
<tr>
<th>Vulnerability</th>
<th>Status</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1f</td>
<td>Not affected</td>
</tr>
<tr>
<td>Mds</td>
<td>Not affected</td>
</tr>
<tr>
<td>Meltdown</td>
<td>Not affected</td>
</tr>
<tr>
<td>Spec store bypass</td>
<td>Mitigation; Speculative Store Bypass disabled via prctl</td>
</tr>
<tr>
<td>Spectre v1</td>
<td>Mitigation; usercopy/swapgs barriers and __user pointer sanitization</td>
</tr>
<tr>
<td>Spectre v2</td>
<td>Mitigation; Enhanced IBRS, IBPB conditional, RSB filling</td>
</tr>
<tr>
<td>Srbs</td>
<td>Not affected</td>
</tr>
<tr>
<td>Tax async abort</td>
<td>Not affected</td>
</tr>
</tbody>
</table>

---

From `lscpu --cache`:

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<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
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<tr>
<td>L1d</td>
<td>48K</td>
<td>768K</td>
<td>12</td>
<td>Data</td>
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<td>64</td>
<td>1</td>
<td>64</td>
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<tr>
<td>L1i</td>
<td>32K</td>
<td>512K</td>
<td>8</td>
<td>Instruction</td>
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<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L2</td>
<td>2M</td>
<td>32M</td>
<td>16</td>
<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>22.5M</td>
<td>45M</td>
<td>15</td>
<td>Unified</td>
<td>3</td>
<td>24576</td>
<td>1</td>
<td>64</td>
</tr>
</tbody>
</table>

---

8. `numactl --hardware`

NOTE: a numactl 'node' might or might not correspond to a physical chip.

<table>
<thead>
<tr>
<th>AVAILABLE</th>
<th>NODE</th>
<th>CPUS</th>
<th>SIZE</th>
<th>FREE</th>
<th>SIZE</th>
</tr>
</thead>
<tbody>
<tr>
<td>2 nodes</td>
<td>0</td>
<td>0-7</td>
<td>257614 MB</td>
<td>256950 MB</td>
<td></td>
</tr>
<tr>
<td>12 MB</td>
<td>1</td>
<td>8-15</td>
<td>257998 MB</td>
<td>257553 MB</td>
<td></td>
</tr>
</tbody>
</table>

---

9. `/proc/meminfo`

- `MemTotal: 527988320 kB`

---

10. `/proc/meminfo`

- `MemTotal: 527988320 kB`

---

11. `/proc/meminfo`

- `MemTotal: 527988320 kB`

---

12. `/proc/meminfo`

- `MemTotal: 527988320 kB`

---

13. Linux kernel boot-time arguments, from `/proc/cmdline`

(Continued on next page)
Platform Notes (Continued)

14. cpupower frequency-info
   analyzing CPU 0:
   current policy: frequency should be within 800 MHz and 4.10 GHz.
   The governor "performance" may decide which speed to use
   within this range.
   boost state support:
   Supported: yes
   Active: yes

15. tuned-adm active
   Current active profile: throughput-performance

16. sysctl
   kernel.numa_balancing           1
   kernel.randomize_va_space       2
   vm.compaction_proactiveness     20
   vm.dirty_background_bytes       0
   vm.dirty_background_ratio      10
   vm.dirty_bytes                   0
   vm.dirty_expire_centisecs      3000
   vm.dirty_ratio                   40
   vm.dirty_writeback_centisecs   500
   vm.dirtytime_expire_seconds   43200
   vm.extrfrag_threshold          500
   vm.min_unmapped_ratio           1
   vm.nr_hugepages                 0
   vm.nr_hugepages_mempolicy      0
   vm.nr_overcommit_hugepages     0
   vm.swappiness                   10
   vm.watermark_boost_factor     15000
   vm.watermark_scale_factor      10
   vm.zone_reclaim_mode           0

17. /sys/kernel/mm/transparent_hugepage
   defrag always defer defer+madvise [madvise] never
   enabled [always] madvise never
   hpage_pmd_size       2097152
   shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs     60000
   defrag                   1
   max_ptes_none            511
   max_ptes_shared          256
   max_ptes_swap            64
   pages_to_scan            4096
   scan_sleep_millisecs     10000

(Continued on next page)
IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

CPU2017 License: 3358
Test Sponsor: IEIT Systems Co., Ltd.
Tested by: IEIT Systems Co., Ltd.

Test Date: Oct-2023
Hardware Availability: Apr-2023
Software Availability: Dec-2022

Platform Notes (Continued)

19. OS release
   From /etc/*-release /etc/*-version
   os-release Red Hat Enterprise Linux 9.0 (Plow)
   redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
   system-release Red Hat Enterprise Linux release 9.0 (Plow)

20. Disk information
   SPEC is set to: /home/CPU2017
   Filesystem            Type  Size  Used  Avail Use% Mounted on
   /dev/mapper/rhel-home  xfs   819G  209G  611G  26%  /home

21. /sys/devices/virtual/dmi/id
   Vendor: IEI
   Product: NF5280M7
   Product Family: Not specified
   Serial: 000000000

22. dmidecode
   Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
   Memory:
   16x Samsung M321R4GA3BB6-CQKVG 32 GB 2 rank 4800

23. BIOS
   (This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor: American Megatrends International, LLC.
   BIOS Version: 03.01.00
   BIOS Date: 12/29/2022

Compiler Version Notes

<table>
<thead>
<tr>
<th>C</th>
<th>600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak) 657.xz_s(base, peak)</th>
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</tr>
</tbody>
</table>

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
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(Continued on next page)
IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

SPECspeed®2017_int_base = 15.0
SPECspeed®2017_int_peak = 15.2

CPU2017 License: 3358
Test Sponsor: IEIT Systems Co., Ltd.
Tested by: IEIT Systems Co., Ltd.
Test Date: Oct-2023
Hardware Availability: Apr-2023
Software Availability: Dec-2022

Compiler Version Notes (Continued)
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation
C benchmarks:
icx
C++ benchmarks:
icpx
Fortran benchmarks:
ifx

Base Portability Flags
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

C++ benchmarks:
-m64 -std=c++14 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

Fortran benchmarks:
-m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4

(Continued on next page)
IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

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</tr>
</tbody>
</table>

**Base Optimization Flags (Continued)**

Fortran benchmarks (continued):
- nostandard-realloc-lhs -align array32byte
- L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

**Peak Compiler Invocation**

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

**Peak Portability Flags**

Same as Base Portability Flags

**Peak Optimization Flags**

C benchmarks:

600.perlbench_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -fno-strict-overflow
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast(pass 1) -xCORE-AVX512 -O3 -ffast-math
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-fiopenmp -DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

605.mcf_s: basepeak = yes

(Continued on next page)
IEIT Systems Co., Ltd.
NF5280M7 (Intel Xeon Gold 6434)

SPECspeed®2017_int_base = 15.0
SPECspeed®2017_int_peak = 15.2

CPU2017 License: 3358
Test Sponsor: IEIT Systems Co., Ltd.
Test Date: Oct-2023
Tested by: IEIT Systems Co., Ltd.
Hardware Availability: Apr-2023
Software Availability: Dec-2022

Peak Optimization Flags (Continued)

625.x264_s: -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -fno-alias -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

657.xz_s: basepeak = yes

C++ benchmarks:

620.omnetpp_s: basepeak = yes
623.xalancbmk_s: basepeak = yes
631.deepsjeng_s: basepeak = yes
641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml
http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-intel-V3.4.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-10-10 11:44:56-0400.
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