# SPEC CPU®2017 Floating Point Rate Result

## Lenovo Global Technology

ThinkSystem SR630 V3  
(2.00 GHz, Intel Xeon Gold 6443N)

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_fp_base =</th>
<th>SPECrate®2017_fp_peak =</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>333</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

### CPU2017 License:
9017

**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Jan-2024  
**Hardware Availability:** Feb-2024  
**Software Availability:** Dec-2023

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>SPECrate®2017_fp_base (333)</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>64</td>
<td>432</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>64</td>
<td>199</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>64</td>
<td>162</td>
</tr>
<tr>
<td>510.parest_r</td>
<td>64</td>
<td>316</td>
</tr>
<tr>
<td>511.povray_r</td>
<td>64</td>
<td>181</td>
</tr>
<tr>
<td>519.lbm_r</td>
<td>64</td>
<td>255</td>
</tr>
<tr>
<td>521.wrf_r</td>
<td>64</td>
<td>304</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>64</td>
<td>336</td>
</tr>
<tr>
<td>527.cam4_r</td>
<td>64</td>
<td>866</td>
</tr>
<tr>
<td>538.imagick_r</td>
<td>64</td>
<td>624</td>
</tr>
<tr>
<td>544.nab_r</td>
<td>64</td>
<td>249</td>
</tr>
<tr>
<td>549.fotonik3d_r</td>
<td>64</td>
<td>142</td>
</tr>
<tr>
<td>554.roms_r</td>
<td>64</td>
<td></td>
</tr>
</tbody>
</table>

### Hardware

<table>
<thead>
<tr>
<th>CPU Name:</th>
<th>Intel Xeon Gold 6443N</th>
</tr>
</thead>
<tbody>
<tr>
<td>Max MHz:</td>
<td>3600</td>
</tr>
<tr>
<td>Nominal:</td>
<td>2000</td>
</tr>
<tr>
<td>Enabled:</td>
<td>32 cores, 1 chip, 2 threads/core</td>
</tr>
<tr>
<td>Orderable:</td>
<td>1 chip</td>
</tr>
<tr>
<td>Cache L1:</td>
<td>32 KB I + 48 KB D on chip per core</td>
</tr>
<tr>
<td>L2:</td>
<td>2 MB I+D on chip per core</td>
</tr>
<tr>
<td>L3:</td>
<td>60 MB I+D on chip per chip</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
<tr>
<td>Memory:</td>
<td>256 GB (8 x 32 GB 2Rx8 PC5-4800B-R, running at 4400)</td>
</tr>
<tr>
<td>Storage:</td>
<td>1 x 960 GB SATA SSD</td>
</tr>
<tr>
<td>Other:</td>
<td>None</td>
</tr>
</tbody>
</table>

### Software

<table>
<thead>
<tr>
<th>OS:</th>
<th>SUSE Linux Enterprise Server 15 SP4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compiler:</td>
<td>C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;</td>
</tr>
<tr>
<td>Parallel:</td>
<td>No</td>
</tr>
<tr>
<td>Firmware:</td>
<td>Lenovo BIOS Version ESE121T 3.10 released Dec-2023</td>
</tr>
<tr>
<td>File System:</td>
<td>xfs</td>
</tr>
<tr>
<td>System State:</td>
<td>Run level 3 (multi-user)</td>
</tr>
<tr>
<td>Base Pointers:</td>
<td>64-bit</td>
</tr>
<tr>
<td>Peak Pointers:</td>
<td>Not Applicable</td>
</tr>
<tr>
<td>Other:</td>
<td>jemalloc memory allocator V5.0.1</td>
</tr>
<tr>
<td>Power Management:</td>
<td>BIOS and OS set to prefer performance at the cost of additional power usage</td>
</tr>
</tbody>
</table>
Lenovo Global Technology
ThinkSystem SR630 V3
(2.00 GHz, Intel Xeon Gold 6443N)

SPECrate®2017_fp_base = 333
SPECrate®2017_fp_peak = Not Run

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>503.bwaves_r</td>
<td>64</td>
<td>411</td>
<td>1560</td>
<td>411</td>
<td>1560</td>
<td>411</td>
<td>1560</td>
</tr>
<tr>
<td>507.cactuBSSN_r</td>
<td>64</td>
<td>188</td>
<td>432</td>
<td>188</td>
<td>431</td>
<td>187</td>
<td>432</td>
</tr>
<tr>
<td>508.namd_r</td>
<td>64</td>
<td>305</td>
<td>199</td>
<td>305</td>
<td>199</td>
<td>305</td>
<td>199</td>
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<tr>
<td>510.parest_r</td>
<td>64</td>
<td>1033</td>
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<td>473</td>
<td>316</td>
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<tr>
<td>519.lbm_r</td>
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<td>181</td>
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<tr>
<td>521.wrf_r</td>
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<td>558</td>
<td>257</td>
<td>561</td>
<td>255</td>
<td>563</td>
<td>255</td>
</tr>
<tr>
<td>526.blender_r</td>
<td>64</td>
<td>322</td>
<td>303</td>
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<td>304</td>
<td>321</td>
<td>304</td>
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<tr>
<td>527.cam4_r</td>
<td>64</td>
<td>333</td>
<td>336</td>
<td>336</td>
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<tr>
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<td>64</td>
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<td>544.nab_r</td>
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<td>173</td>
<td>624</td>
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<tr>
<td>554.roms_r</td>
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<td>718</td>
<td>142</td>
<td>717</td>
<td>142</td>
<td>718</td>
<td>142</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
Malloc_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>/proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
SPEC CPU®2017 Floating Point Rate Result

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ThinkSystem SR630 V3
(2.00 GHz, Intel Xeon Gold 6143N)

SPECrate®2017_fp_base = 333
SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Jan-2024
Hardware Availability: Feb-2024
Software Availability: Dec-2023

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance
SNC set to SNC2
LLC Prefetch set to Disabled
AMP Prefetch set to Enable

Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c6ae2c92cc097bec197
running on localhost Sat Jan 24 07:24:40 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux

2. w
07:24:40 up 5:15,  1 user,  load average: 18.12, 49.31, 58.26
USER   TTY     FROM       LOGIN@     IDLE   JCPU   PCPU WHAT
root    tty1     -           02:10      5:12m  1.15s  0.01s /bin/bash ./speccpu_ny.sh

(Continued on next page)
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**ThinkSystem SR630 V3**  
(2.00 GHz, Intel Xeon Gold 6443N)

---

**SPEC CPU®2017 Floating Point Rate Result**  
Copyright 2017-2024 Standard Performance Evaluation Corporation

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### Lenovo Global Technology

**Test Date:** Jan-2024  
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**Hardware Availability:** Feb-2024  
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**Software Availability:** Dec-2023

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### Platform Notes (Continued)

7. **lscpu**

From `lscpu` from `util-linux 2.37.2`:

- **Architecture:** x86_64
- **CPU op-mode(s):** 32-bit, 64-bit
- **Address sizes:** 46 bits physical, 57 bits virtual
- **Byte Order:** Little Endian
- **CPU(s):** 64
- **On-line CPU(s) list:** 0-63
- **Vendor ID:** GenuineIntel
- **Model name:** Intel(R) Xeon(R) Gold 6443N
- **CPU family:** 6
- **Model:** 143
- **Thread(s) per core:** 2
- **Core(s) per socket:** 32
- **Stepping:** 8
- **BogoMIPS:** 4000.00
- **Flags:**

  - fpu vme de pse tsc msr pae mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
  - mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
  - mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
  - mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
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  - mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
  - mca cmov pat pse36
  - mtrr pge mca cmov pat pse36
  - mca cmov pat pse36

**Virtualization:** VT-x

**L1d cache:** 1.5 MiB (32 instances)

**L1i cache:** 1 MiB (32 instances)

**L2 cache:** 64 MiB (32 instances)

**L3 cache:** 60 MiB (1 instance)

**NUMA node(s):** 2

**NUMA node0 CPU(s):** 0-15,32-47

**NUMA node1 CPU(s):** 16-31,48-63

**Vulnerability L1tf:** Not affected

**Vulnerability Mds:** Not affected

**Vulnerability Meltdown:** Not affected

**Vulnerability Spectre v1:** Mitigation; usercopy/swappgs barriers and __user pointer sanitization

**Vulnerability Spectre v2:** Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

**Vulnerability Tlb multihit:** Not affected

**Vulnerability Spec store bypass:** Mitigation; Speculative Store Bypass disabled via prctl and seccomp

**Vulnerability Tsx async abort:** Not affected

---

From `lscpu --cache`:

<table>
<thead>
<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1d</td>
<td>48K</td>
<td>1.5M</td>
<td>12 Data</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L1</td>
<td>32K</td>
<td>1M</td>
<td>1 Instruction</td>
<td>8</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L2</td>
<td>2M</td>
<td>64M</td>
<td>16 Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>60M</td>
<td>60M</td>
<td>15 Unified</td>
<td>3</td>
<td>65536</td>
<td>1</td>
<td>64</td>
</tr>
</tbody>
</table>

(Continued on next page)
8. numactl --hardware
   NOTE: a numactl 'node' might or might not correspond to a physical chip.
   available: 2 nodes (0-1)
   node 0 cpus: 0-15,32-47
   node 0 size: 128680 MB
   node 0 free: 127539 MB
   node 1 cpus: 16-31,48-63
   node 1 size: 128952 MB
   node 1 free: 128216 MB
   node distances:
   node   0   1
   0:  10  12
   1:  12  10

9. /proc/meminfo
   MemTotal: 263816812 kB

10. who -r
    run-level 3 Jan 6 02:10

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
    Default Target Status
    multi-user running

12. Services, from systemctl list-unit-files
    STATE   UNIT        FILES
    enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance
             issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
             smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled autosfs autostart-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
             chronynd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
             firewall gpm grub2-once haveged-switch-root hwloc-dump-hwdata ipmi ipmtime
             issue-ssh-keys kexec-load lnumask man-db-create multipathd nfs nfs-blkmap rdisc
             rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
             systemd-boot-check-no-failures systemd-network-generator systemd-sysvext
             systemd-time-wait-sync systemd-timesyncd
    generated ntp_sync
    indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
    root=UUID=efe3d3bb-d17b-48bc-af3c-7ee429916327
    splash=silent
    mitigations=auto
    quiet
    security=apparmor

14. cpupower frequency-info
    analyzing CPU 0:
    Unable to determine current policy
    boost state support:

(Continued on next page)
15. `sysctl`

```
kernel.numa_balancing               1
kernel.randomize_va_space           2
vm.compaction_proactiveness         20
vm.dirty_background_bytes           10
vm.dirty_background_ratio           1
vm.dirty_bytes                     0
vm.dirty_expire_centisecs           3000
vm.dirty_ratio                     1
vm.dirty_writeback_centisecs        500
vm.dirtytime_expire_seconds        43200
vm.extfrag_threshold               500
vm.min_unmapped_ratio               1
vm.nr_hugepages                    0
vm.nr_hugepages_mempolicy           0
vm.nr_overcommit_hugepages          0
vm.swappiness                      60
vm.watermark_boost_factor          15000
vm.watermark_scale_factor           10
vm.zone_reclaim_mode               0
```

16. `/sys/kernel/mm/transparent_hugepage`

```
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

17. `/sys/kernel/mm/transparent_hugepage/khugepaged`

```
alloc_sleep_millisecs   60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs    10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

19. Disk information

```
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3
```

```
Filesystem     Type  Size  Used Avail Use% Mounted on
/dev/sda2      xfs  894G  141G  754G  16% /
```

20. `/sys/devices/virtual/dmi/id`

```
Vendor:         Lenovo
Product:        ThinkSystem SR630 V3 MB,EGS,DDR5,NY,1U
Product Family: ThinkSystem
Serial:         1234567890
```

(Continued on next page)
Platform Notes (Continued)

21. dmidecode
Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
8x Samsung M321R4GA3BB6-CQKVG 32 GB 2 rank 4800, configured at 4400

22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: Lenovo
BIOS Version: ESE121T-3.10
BIOS Date: 12/20/2023
BIOS Revision: 3.10
Firmware Revision: 3.90

Compiler Version Notes

C               | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++             | 508.namd_r(base) 510.parest_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++, C          | 511.povray_r(base) 526.blender_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++, Fortran    | 507.cactuBSSN_r(base)
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran         | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
(Continued on next page)
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ThinkSystem SR630 V3
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SPECrater®2017_fp_base = 333
SPECrater®2017_fp_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.ibm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64

(Continued on next page)
## Base Portability Flags (Continued)

549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

### Base Optimization Flags

#### C benchmarks:

```bash
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-fflat -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

#### C++ benchmarks:

```bash
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -fflat -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

#### Fortran benchmarks:

```bash
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

#### Benchmarks using both Fortran and C:

```bash
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-fflat -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

#### Benchmarks using both C and C++:

```bash
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

#### Benchmarks using Fortran, C, and C++:

```bash
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```
## Lenovo Global Technology

ThinkSystem SR630 V3
(2.00 GHz, Intel Xeon Gold 6443N)

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**Tested by:** Lenovo Global Technology  
**Test Date:** Jan-2024  
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**Software Availability:** Dec-2023

The flags files that were used to format this result can be browsed at:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html)

You can also download the XML flags sources by saving the following links:

- [http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml](http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml)

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