**Lenovo Global Technology**  
ThinkSystem SR630 V3  
(2.10 GHz, Intel Xeon Gold 6538N)

**SPEC CPU®2017 Integer Rate Result**  
Copyright 2017-2024 Standard Performance Evaluation Corporation

SPECrade®2017_int_base = 552  
SPECrade®2017_int_peak = Not Run

**Hardware**

**CPU Name:** Intel Xeon Gold 6538N  
**Max MHz:** 4100  
**Nominal:** 2100  
**Enabled:** 64 cores, 2 chips, 2 threads/core  
**Orderable:** 1.2 chips  
**Cache L1:** 32 KB I + 48 KB D on chip per core  
**L2:** 2 MB I+D on chip per core  
**L3:** 60 MB I+D on chip per chip  
**Other:** None  
**Memory:** 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 5200)  
**Storage:** 1 x 960 GB SATA SSD  
**Other:** CPU Cooling: Air

**Software**

**OS:** SUSE Linux Enterprise Server 15 SP4  
**Kernel:** 5.14.21-150400.22-default  
**Compiler:** C/C++, Version 2024.0.2 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.0.2 of Intel Fortran Compiler for Linux;  
**Parallel:** No  
**Firmware:** Lenovo BIOS Version ESE123C 3.12 released Feb-2024  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 64-bit  
**Peak Pointers:** Not Applicable  
**Other:** None  
**Power Management:** BIOS set to prefer performance at the cost of additional power usage

---

**Lenovo Global Technology**  
ThinkSystem SR630 V3  
(2.10 GHz, Intel Xeon Gold 6538N)  
**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology  
**Test Date:** Mar-2024  
**Hardware Availability:** Feb-2024  
**Software Availability:** Mar-2024

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Specs</th>
<th>SPECrate®2017_int_base</th>
<th>SPECrate®2017_int_peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perbench_r</td>
<td>128</td>
<td>414</td>
<td></td>
<td></td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>128</td>
<td>460</td>
<td></td>
<td></td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>128</td>
<td>878</td>
<td></td>
<td></td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>128</td>
<td>373</td>
<td></td>
<td></td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>128</td>
<td>733</td>
<td></td>
<td></td>
</tr>
<tr>
<td>525.x264_r</td>
<td>128</td>
<td>1130</td>
<td></td>
<td></td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>128</td>
<td>403</td>
<td></td>
<td></td>
</tr>
<tr>
<td>541.leela_r</td>
<td>128</td>
<td>384</td>
<td></td>
<td></td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>128</td>
<td>1190</td>
<td></td>
<td></td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
<td>274</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

---

**500.perbench_r**  
**502.gcc_r**  
**505.mcf_r**  
**520.omnetpp_r**  
**523.xalancbmk_r**  
**525.x264_r**  
**531.deepsjeng_r**  
**541.leela_r**  
**548.exchange2_r**  
**557.xz_r**
Lenovo Global Technology
ThinkSystem SR630 V3
(2.10 GHz, Intel Xeon Gold 6538N)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>128</td>
<td>492</td>
<td>415</td>
<td>492</td>
<td>414</td>
<td>492</td>
<td>414</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>128</td>
<td>394</td>
<td>460</td>
<td>394</td>
<td>460</td>
<td>395</td>
<td>459</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>128</td>
<td>235</td>
<td>882</td>
<td>236</td>
<td>878</td>
<td>236</td>
<td>878</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>128</td>
<td>451</td>
<td>372</td>
<td>450</td>
<td>374</td>
<td>450</td>
<td>373</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>128</td>
<td>184</td>
<td>733</td>
<td>184</td>
<td>735</td>
<td>185</td>
<td>731</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>128</td>
<td>199</td>
<td>1130</td>
<td>198</td>
<td>1130</td>
<td>199</td>
<td>1130</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>128</td>
<td>364</td>
<td>403</td>
<td>364</td>
<td>403</td>
<td>363</td>
<td>404</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>128</td>
<td>552</td>
<td>384</td>
<td>552</td>
<td>384</td>
<td>552</td>
<td>384</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>128</td>
<td>282</td>
<td>1190</td>
<td>281</td>
<td>1190</td>
<td>282</td>
<td>1190</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>128</td>
<td>502</td>
<td>275</td>
<td>504</td>
<td>274</td>
<td>506</td>
<td>273</td>
</tr>
</tbody>
</table>

**SPECrati02017_int_base = 552**

**SPECrati02017_int_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor.

For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2024.0.2/lib/intel64:/home/cpu2017-1.1.9-ic2024.0.2/lib/ia32:/home/cpu2017-1.1.9-ic2024.0.2/lib/j6/5.0.1-32"
MALLOC_CONF = "retain:true"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu < etc
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)
Lenovo Global Technology  
ThinkSystem SR630 V3  
(2.10 GHz, Intel Xeon Gold 6538N)  

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology  

Test Date: Mar-2024  
Hardware Availability: Feb-2024  
Software Availability: Mar-2024

---

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

---

Platform Notes

BIOS configuration:
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
SNC set to SNC2  
LLC Prefetch set to Disabled  
UPI Link Disable set to Minimum Number of Links Enabled

Sysinfo program /home/cpu2017-1.1.9-ic2024.0.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c6ae2c92cc097bec197 running on test1 Wed Mar 13 01:44:27 2024

SUT (System Under Test) info as seen by some common utilities.

---

Table of contents

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. systemctl service manager version: systemctl 249 (249.11+suse.124.g2bc0b2c447)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline  
14. cpupower frequency-info  
15. syslog  
16. /sys/kernel/mm/transparent_hugepage  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS

---

1. uname -a  
   Linux test1 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64 x86_64 GNU/Linux

---

2. w  
   01:44:27 up 1 min,  1 user,  load average: 2.43, 1.11, 0.41  
   USER   TTY     FROM       LOGIN@   IDLE   JCPU   PCPU  WHAT  
   root   tty1   -           01:44   11.00s  1.10s  0.01s  -bash

---

3. Username  
   From environment variable $USER: root

(Continued on next page)
Platform Notes (Continued)

4. ulimit --a
   core file size          (blocks, -c) unlimited
   data seg size           (kbytes, -d) unlimited
   scheduling priority     (-e) 0
   file size               (blocks, -f) unlimited
   pending signals         (-l) 4126871
   max locked memory       (kbytes, -l) 64
   max memory size         (kbytes, -m) unlimited
   open files              (-n) 1024
   pipe size               (512 bytes, -p) 8
   POSIX message queues    (bytes, -q) 819200
   real-time priority      (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time                (seconds, -t) unlimited
   max user processes      (-u) 4126871
   virtual memory          (kbytes, -v) unlimited
   file locks              (-x) unlimited

5. sysinfo process ancestry
   /usr/lib/systemd/systemd --switched-root --system --deserialize 30
   login -- root
   -bash
   -bash
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --c
   ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base -o all intrate
   runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
   ic2024.0.2-lin-sapphirerapids-rate-20231213.cfg --define smt-on --define cores=64 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
   rate --tune base --size refrain intrate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.036/templogs/preenv.intrate.036.0.log --lognum 036.0 --from_runcpu 2
   specperl $SPEC/bin/sysinfo
   $SPEC = /home/cpu2017-1.1.9-ic2024.0.2

6. /proc/cpuinfo
   model name      : INTEL(R) XEON(R) GOLD 6538N
   vendor_id       : GenuineIntel
   cpu family      : 6
   model           : 207
   stepping        : 2
   microcode       : 0x21000200
   bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs
   cpu cores       : 32
   siblings        : 64
   2 physical ids (chips)
   128 processors (hardware threads)
   physical id 0: core ids 0-31
   physical id 1: core ids 0-31
   physical id 0: apicids 0-63
   physical id 1: apicids 128-191
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:
Platform Notes (Continued)

Virtualization:
VT-x

L1d cache:
3 MiB (64 instances)

L1i cache:
2 MiB (64 instances)

L2 cache:
128 MiB (64 instances)

L3 cache:
120 MiB (2 instances)

NUMA node(s):
4

NUMA node0 CPU(s):
0-15, 64-79

NUMA node1 CPU(s):
16-31, 80-95

NUMA node2 CPU(s):
32-47, 96-111

NUMA node3 CPU(s):
48-63, 112-127

Vulnerability Itlb multihit:
Not affected

Vulnerability Ltft:
Not affected

Vulnerability Mds:
Not affected

Vulnerability Meltdown:
Not affected

Vulnerability Spec store bypass:
Mitigation; Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1:
Mitigation; usercopy/swaps barriers and __user pointer sanitization

Vulnerability Spectre v2:
Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Srbd:
Not affected

Vulnerability Tsx async abort:
Not affected

From lscpu --cache:

<table>
<thead>
<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1d</td>
<td>48K</td>
<td>3M</td>
<td>12</td>
<td>Data</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L1i</td>
<td>32K</td>
<td>2M</td>
<td>8</td>
<td>Instruction</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L2</td>
<td>2M</td>
<td>120M</td>
<td>16</td>
<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>60M</td>
<td>120M</td>
<td>15</td>
<td>Unified</td>
<td>3</td>
<td>65536</td>
<td>1</td>
<td>64</td>
</tr>
</tbody>
</table>

(Continued on next page)
Platform Notes (Continued)

8. numactl --hardware
   NOTE: a numactl 'node' might or might not correspond to a physical chip.
   available: 4 nodes (0-3)
   node 0 size: 257703 MB
   node 0 free: 256466 MB
   node 1 size: 258039 MB
   node 1 free: 257516 MB
   node 2 size: 258039 MB
   node 2 free: 257650 MB
   node 3 size: 257958 MB
   node 3 free: 257573 MB
   node distances:
   node   0   1   2   3
   0:  10  12  21  21
   1:  12  10  21  21
   2:  21  21  10  12
   3:  21  21  12  10

9. /proc/meminfo
   MemTotal:       1056503584 kB

10. who -r
    run-level 3 Mar 13 01:44

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
    Default Target Status
    multi-user running

12. Services, from systemctl list-unit-files
    STATE UNIT FILES
    enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ hovedge irbalance iscsi
    issue-generator kbsettings klog lvm2-monitor nscd postfix purge-kernels rollback rayslog
    smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled autofs autoyaist-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
    chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
    firewalld gpm grub2-once hovedge-switch-root ipmi ipmienvd iscsi-init iscsid isciuioc
    issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-bikmap nmb rdisc
    rpmbind rmpconfigcheck rsync serial-getty@ smartd_generate_opts smp snmpd snmptrapd
    systemd-boot-check-no-failures systemd-network-generator systemd-sysext
    systemd-boot-run-time systemd-boot-run-time systemd-syslogd
    generated ntp_sync
    indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
    root=UUID=f976c541-a329-4c54-ba84-4be16556ee18
    splash=silent
    mitigations=auto
    quiet
    security=apparmor

(Continued on next page)
Lenovo Global Technology

ThinkSystem SR630 V3
(2.10 GHz, Intel Xeon Gold 6538N)

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

**SPECrater®2017_int_base = 552**
**SPECrater®2017_int_peak = Not Run**

---

**Platform Notes (Continued)**

14. cpupower frequency-info
   analyzing CPU 0:
   Unable to determine current policy
   boost state support:
   Supported: yes
   Active: yes

15. sysctl
   kernel numa balancing 1
   kernel randomize va space 2
   vm compaction proactiveness 20
   vm dirty background bytes 0
   vm dirty background ratio 10
   vm dirty bytes 0
   vm dirty expire centisecs 3000
   vm dirty ratio 20
   vm dirty writeback centisecs 500
   vm dirtytime expire seconds 43200
   vm extrag frag threshold 500
   vm min unmapped ratio 1
   vm nr hippocages 0
   vm nr hippocages mempolicy 0
   vm nr overcommit hippocages 0
   vm swappiness 60
   vm watermark boost factor 15000
   vm watermark scale factor 10
   vm zone reclaim mode 0

16. /sys/kernel/mm/transparent hugepage
   defrag always defer defer madvise [madvise] never
   enabled [always] madvise never
   hpage pmd size 2097152
   shmem enabled always within size advise [never] deny force

17. /sys/kernel/mm/transparent hugepage/khugepaged
   alloc sleep milliseconds 60000
   defrag 1
   max ptes none 511
   max ptes shared 256
   max ptes swap 64
   pages to scan 4096
   scan sleep milliseconds 10000

18. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP4

19. Disk information
   SPEC is set to: /home/cpu2017-1.1.9-ic2024.0.2
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sdc2 xfs 894G 253G 642G 29% /

(Continued on next page)
SPEC CPU®2017 Integer Rate Result

Lenovo Global Technology
ThinkSystem SR630 V3
(2.10 GHz, Intel Xeon Gold 6538N)

SPECraten®2017_int_base = 552
SPECraten®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2024
Hardware Availability: Feb-2024
Software Availability: Mar-2024

Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id
   Vendor: Lenovo
   Product: ThinkSystem SR630 V3
   Product Family: ThinkSystem
   Serial: 1234567890

21. dmidecode
   Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
   Memory:
   14x Samsung M321R8GAPB0-CWMKH 64 GB 2 rank 5600, configured at 5200
   2x Samsung M321R8GAPB0-CWMXH 64 GB 2 rank 5600, configured at 5200

22. BIOS
   (This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor: Lenovo
   BIOS Version: ESE123C-3.12
   BIOS Date: 02/22/2024
   BIOS Revision: 3.12
   Firmware Revision: 12.6

Compiler Version Notes

<table>
<thead>
<tr>
<th>C</th>
<th>500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>C++</th>
<th>520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Fortran</th>
<th>548.exchange2_r(base)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213 Copyright (C) 1985-2023 Intel Corporation. All rights reserved.</td>
<td></td>
</tr>
</tbody>
</table>

Base Compiler Invocation

C benchmarks:
icx

(Continued on next page)
Lenovo Global Technology
ThinkSystem SR630 V3
(2.10 GHz, Intel Xeon Gold 6538N)

SPECrater®2017_int_base = 552
SPECrater®2017_int_peak = Not Run

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Test Date: Mar-2024
Hardware Availability: Feb-2024
Tested by: Lenovo Global Technology
Software Availability: Mar-2024

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Base Portability Flags
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.ommnetpp_r: -DSPEC_LP64
523.xalanchbk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags
C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html
Lenovo Global Technology
ThinkSystem SR630 V3
(2.10 GHz, Intel Xeon Gold 6538N)

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base = 552</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak = Not Run</td>
</tr>
</tbody>
</table>

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Mar-2024
Hardware Availability: Feb-2024
Software Availability: Mar-2024

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-03-12 13:44:26-0400.
Originally published on 2024-04-09.