



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Nettrix

R620 G50 (Intel Xeon Silver 4510, 2.40 GHz)

SPECSpeed®2017_fp_base = 211

SPECSpeed®2017_fp_peak = 211

CPU2017 License: 6138

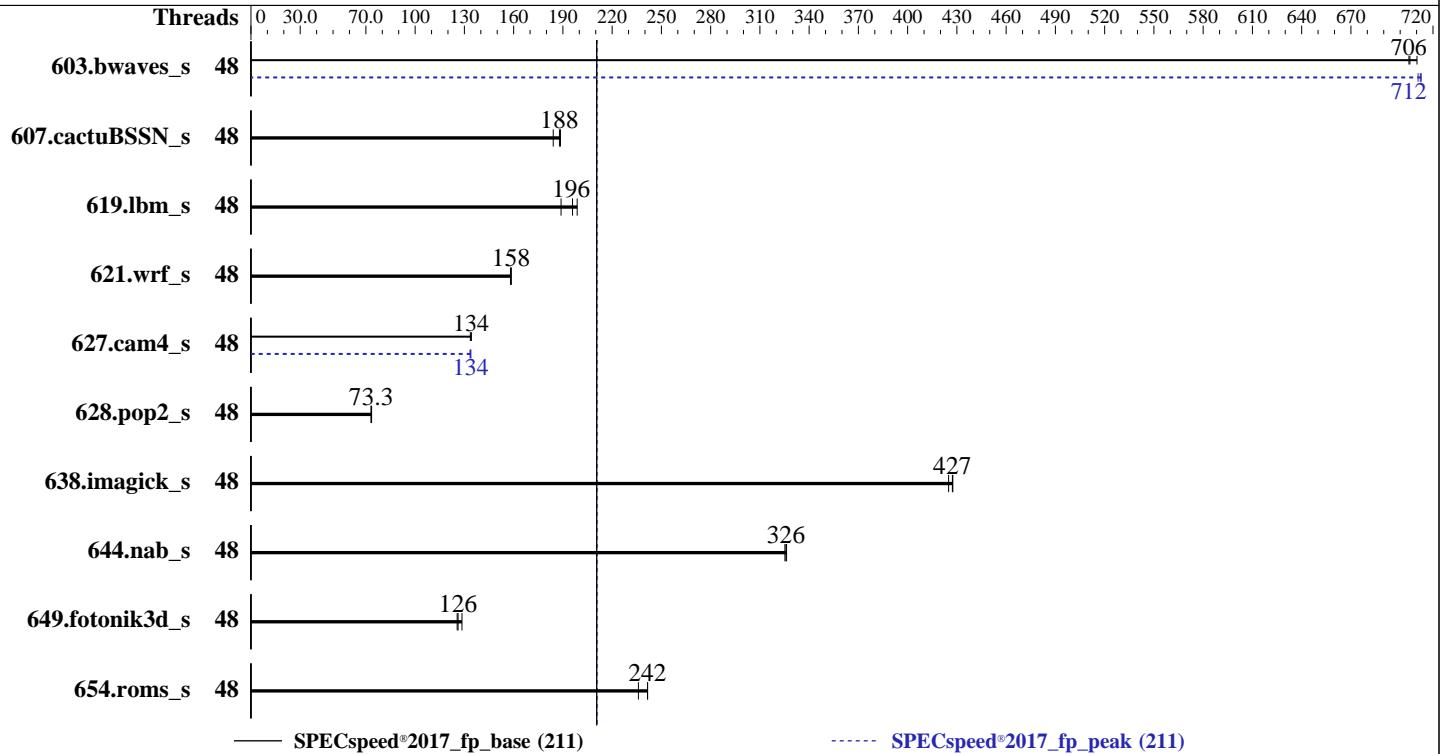
Test Date: Apr-2024

Test Sponsor: Nettrix

Hardware Availability: Dec-2023

Tested by: Nettrix

Software Availability: Jan-2024



Hardware		Software	
CPU Name:	Intel Xeon Silver 4510	OS:	SUSE Linux Enterprise Server 15 SP5
Max MHz:	4100	Compiler:	5.14.21-150500.53-default
Nominal:	2400	Parallel:	C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
Enabled:	24 cores, 2 chips, 2 threads/core	Firmware:	Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
Orderable:	1,2 chips	File System:	Yes
Cache L1:	32 KB I + 48 KB D on chip per core	System State:	Nettrix BIOS Version NNH1041261 released Dec-2023
L2:	2 MB I+D on chip per core	Base Pointers:	xfs
L3:	30 MB I+D on chip per chip	Peak Pointers:	Run level 3 (multi-user)
Other:	None	Other:	64-bit
Memory:	1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)	Power Management:	64-bit
Storage:	1 x 16 TB SATA HDD (7200 rpm)		jemalloc memory allocator V5.0.1
Other:	CPU Cooling: Air		BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	48	83.6	705	83.6	706	83.1	710	48	83.0	711	82.8	713	82.8	712
607.cactuBSSN_s	48	88.4	189	90.5	184	88.7	188	48	88.4	189	90.5	184	88.7	188
619.lbm_s	48	27.7	189	26.7	196	26.4	199	48	27.7	189	26.7	196	26.4	199
621.wrf_s	48	83.4	159	83.7	158	83.5	158	48	83.4	159	83.7	158	83.5	158
627.cam4_s	48	66.1	134	66.3	134	66.0	134	48	66.2	134	66.4	133	66.2	134
628.pop2_s	48	162	73.2	162	73.3	162	73.5	48	162	73.2	162	73.3	162	73.5
638.imagick_s	48	33.9	425	33.8	427	33.7	428	48	33.9	425	33.8	427	33.7	428
644.nab_s	48	53.6	326	53.6	326	53.7	325	48	53.6	326	53.6	326	53.7	325
649.fotonik3d_s	48	70.9	129	72.6	126	72.3	126	48	70.9	129	72.6	126	72.3	126
654.roms_s	48	65.2	242	66.7	236	65.2	242	48	65.2	242	66.7	236	65.2	242

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
OS set to performance mode via cpupower frequency-set -g performance

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/SPECcpu_20231121/lib/intel64:/home/SPECcpu_20231121/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0

NA : The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
```

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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Platform Notes

BIOS Configuration:

LLC Prefetch set to Enabled
SNC (Sub NUMA) set to Enable SNC2 (2-clusters)
Patrol Scrub set to Disabled
LLC dead line alloc set to Disabled
XPT Prefetch set to Enabled
DCU Streamer Prefetcher set to Disabled
Hardware P-States set to Native Mode

Sysinfo program /home/SPECcpu_20231121/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Apr 29 17:15:32 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent_hugepage
- 18. /sys/kernel/mm/transparent_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

2. w
17:15:32 up 1:26, 1 user, load average: 0.28, 15.31, 31.40
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/0 10.32.5.20 15:49 1:24m 0.96s 0.06s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a

(Continued on next page)



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Platform Notes (Continued)

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals          (-i) 4125266
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 4125266
virtual memory            (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root@pts/0
-bash
sh ww-speed-test.sh
runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=48 --tune base,peak -o all --define
  drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=48 --tune base,peak --output_format all
  --define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
  --note-preenv --logfile $SPEC/tmp/CPU2017.135/templogs/preenv.fpspeed.135.0.log --lognum 135.0
  --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/SPECcpu_20231121
```

6. /proc/cpuinfo

```
model name      : INTEL(R) XEON(R) SILVER 4510
vendor_id       : GenuineIntel
cpu family      : 6
model          : 143
stepping        : 8
microcode       : 0x2b000461
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb
cpu cores       : 12
siblings        : 24
2 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-11
physical id 1: core ids 0-11
physical id 0: apicids 0-23
physical id 1: apicids 64-87
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
```

(Continued on next page)



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Platform Notes (Continued)

```

CPU op-mode(s):           32-bit, 64-bit
Address sizes:            52 bits physical, 57 bits virtual
Byte Order:               Little Endian
CPU(s):                  48
On-line CPU(s) list:     0-47
Vendor ID:                GenuineIntel
Model name:              INTEL(R) XEON(R) SILVER 4510
CPU family:               6
Model:                   143
Thread(s) per core:      2
Core(s) per socket:       12
Socket(s):                2
Stepping:                 8
CPU max MHz:              4100.0000
CPU min MHz:              800.0000
BogoMIPS:                 4800.00
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                           clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                           lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                           nonstop_tsc cpuid aperf fm perf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
                           vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                           x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                           abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
                           intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnni
                           flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2
                           erms invpcid rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                           clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaved
                           xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
                           split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                           hwp hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke waitpkg
                           avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                           avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                           enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16
                           amx_tile flush_lll arch_capabilities

Virtualization:          VT-x
L1d cache:                1.1 MiB (24 instances)
L1i cache:                768 KiB (24 instances)
L2 cache:                 48 MiB (24 instances)
L3 cache:                 60 MiB (2 instances)
NUMA node(s):              4
NUMA node0 CPU(s):        0-5,24-29
NUMA node1 CPU(s):        6-11,30-35
NUMA node2 CPU(s):        12-17,36-41
NUMA node3 CPU(s):        18-23,42-47
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:       Not affected
Vulnerability Mds:        Not affected
Vulnerability Meltdown:   Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed:   Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:  Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:  Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds:      Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
  NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
  L1d      48K      1.1M    12 Data          1       64          1           64

```

(Continued on next page)



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Platform Notes (Continued)

L1i	32K	768K	8	Instruction	1	64	1	64
L2	2M	48M	16	Unified	2	2048	1	64
L3	30M	60M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0-5,24-29

node 0 size: 257568 MB

node 0 free: 256508 MB

node 1 cpus: 6-11,30-35

node 1 size: 258044 MB

node 1 free: 256895 MB

node 2 cpus: 12-17,36-41

node 2 size: 258010 MB

node 2 free: 256458 MB

node 3 cpus: 18-23,42-47

node 3 size: 257716 MB

node 3 free: 249854 MB

node distances:

node 0 1 2 3

0: 10 12 21 21

1: 12 10 21 21

2: 21 21 10 12

3: 21 21 12 10

9. /proc/meminfo

MemTotal: 1056092508 kB

10. who -r

run-level 3 Apr 29 15:49

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)

Default Target Status

multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron getty@ haveged irqbalance issue-generator kbdsettings kdump kdump-early postfix purge-kernels rollback smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables firewalld grub2-once haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@ smartd_generate_opts systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default

root=UUID=c7ada9bf-a7a2-4807-aaee-b29cd1084e31

splash=silent

resume=/dev/disk/by-uuid/51c1f63c-3687-478c-9194-590f668c08a9

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Platform Notes (Continued)

```
mitigations=auto
quiet
security=apparmor
crashkernel=316M,high
crashkernel=72M,low
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
    current policy: frequency should be within 800 MHz and 4.10 GHz.  
                The governor "performance" may decide which speed to use  
                within this range.  
    boost state support:  
        Supported: yes  
        Active: yes
```

```
-----  
15. tuned-adm active  
Current active profile: throughput-performance
```

```
-----  
16. sysctl  
kernel.numa_balancing          1  
kernel.randomize_va_space       2  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes      0  
vm.dirty_background_ratio     10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs     3000  
vm.dirty_ratio                 20  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold           500  
vm.min_unmapped_ratio          1  
vm.nr_hugepages                 0  
vm.nr_hugepages_mempolicy      0  
vm.nr_overcommit_hugepages    0  
vm.swappiness                   10  
vm.watermark_boost_factor     15000  
vm.watermark_scale_factor      10  
vm.zone_reclaim_mode            0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage  
defrag           always defer+madvise [madvise] never  
enabled          [always] madvise never  
hpage_pmd_size  2097152  
shmem_enabled   always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged  
alloc_sleep_millisecs  60000  
defrag                  1  
max_ptes_none           511  
max_ptes_shared          256  
max_ptes_swap             64  
pages_to_scan            4096  
scan_sleep_millisecs    10000
```

(Continued on next page)



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Platform Notes (Continued)

19. OS release

```
From /etc/*-release /etc/*-version
os-release openSUSE Leap 15.5
```

20. Disk information

SPEC is set to: /home/SPECcpu_20231121

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdc3	xfs	14T	89G	14T	1%	/home

21. /sys/devices/virtual/dmi/id

Vendor:	Nettrix
Product:	R620 G50
Product Family:	Rack
Serial:	6101810603448188

22. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
16x Samsung M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4400
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	American Megatrends International, LLC.
BIOS Version:	NNH1041261
BIOS Date:	12/13/2023
BIOS Revision:	5.32

Compiler Version Notes

```
=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
=====
C++, C, Fortran | 607.cactusBSSN_s(base, peak)
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----
```

```
=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
-----
```

```
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
-----
```

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Compiler Version Notes (Continued)

Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math

(Continued on next page)



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Base Optimization Flags (Continued)

C benchmarks (continued):

```
-futto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids -Ofast  
-ffast-math -futto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-futto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp  
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -futto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int  
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte  
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx



SPEC CPU®2017 Floating Point Speed Result

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Nettrix

R620 G50 (Intel Xeon Silver 4510, 2.40 GHz)

SPECSpeed®2017_fp_base = 211

SPECSpeed®2017_fp_peak = 211

CPU2017 License: 6138

Test Sponsor: Nettrix

Tested by: Nettrix

Test Date: Apr-2024

Hardware Availability: Dec-2023

Software Availability: Jan-2024

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -w -m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsapphirerapids  
-Ofast -ffast-math -fsto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs  
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc
```

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

```
627.cam4_s: -w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -fsto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP  
-Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Nettrix-Platform-Settings-V1.3-SPR-revA.xml>

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