SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

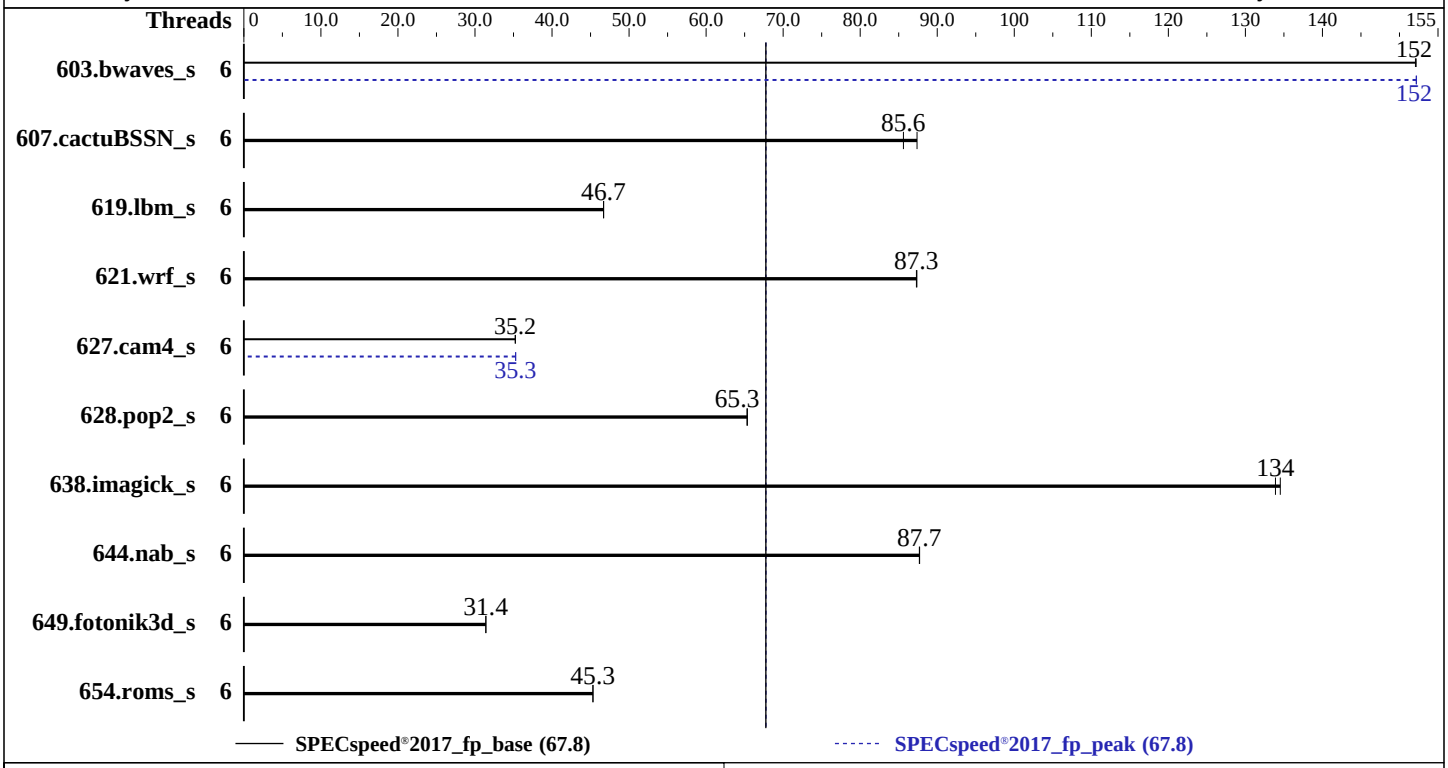
PowerEdge R260 (Intel Xeon E-2456)

SPECspeed®2017_fp_base = 67.8

SPECspeed®2017_fp_peak = 67.8

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2024
Hardware Availability: May-2024
Software Availability: Jun-2023



Hardware

CPU Name:	Intel Xeon E-2456
Max MHz:	5100
Nominal:	3300
Enabled:	6 cores, 1 chip, 2 threads/core
Orderable:	1 chip
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	18 MB I+D on chip per chip
Other:	None
Memory:	64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)
Storage:	40 GB on tmpfs
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default
Compiler:	C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
Parallel:	Yes
Firmware:	Version 1.2.2 released Mar-2024
File System:	tmpfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R260 (Intel Xeon E-2456)

SPECspeed®2017_fp_base = 67.8

SPECspeed®2017_fp_peak = 67.8

CPU2017 License: 6573
Test Sponsor: Dell Inc.
Tested by: Dell Inc.

Test Date: Mar-2024
Hardware Availability: May-2024
Software Availability: Jun-2023

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	6	388	152	**388**	**152**			6	388	152	**388**	**152**		
607.cactuBSSN_s	6	**195**	**85.6**	191	87.4			6	**195**	**85.6**	191	87.4		
619.lbm_s	6	112	46.7	**112**	**46.7**			6	112	46.7	**112**	**46.7**		
621.wrf_s	6	151	87.4	**152**	**87.3**			6	151	87.4	**152**	**87.3**		
627.cam4_s	6	**252**	**35.2**	251	35.2			6	251	35.3	**251**	**35.3**		
628.pop2_s	6	182	65.4	**182**	**65.3**			6	182	65.4	**182**	**65.3**		
638.imagick_s	6	107	135	**108**	**134**			6	107	135	**108**	**134**		
644.nab_s	6	199	87.7	**199**	**87.7**			6	199	87.7	**199**	**87.7**		
649.fotonik3d_s	6	290	31.4	**290**	**31.4**			6	290	31.4	**290**	**31.4**		
654.roms_s	6	**348**	**45.3**	347	45.3			6	**348**	**45.3**	347	45.3		

SPECspeed®2017_fp_base = 67.8

SPECspeed®2017_fp_peak = 67.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib_2023.0/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.
     1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"
```

Platform Notes

```
BIOS settings:
        DIMM Self Healing on
    Uncorrectable Memory Error : Disabled

     Virtualization Technology : Disabled

                System Profile : Custom
          CPU Power Management : Maximum Performance
                           C1E : Disabled
                       C States : Autonomous
            PCI ASPM L1 Link
              Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Tue Mar 26 02:44:57 2024

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043/lp)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   02:44:57 up  2:50,  1 user,  load average: 2.17, 4.87, 3.54
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                00:04    2:40m  1.05s  0.00s /bin/bash ./dell-run-speccpu.sh speed
  --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
  html,pdf,txt --define DL-LQC=1
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 254888
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 254888
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 /bin/bash ./DELL_speed.sh
 /bin/bash ./dell-run-main.sh speed
 /bin/bash ./dell-run-main.sh speed
 /bin/bash ./dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
   --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
 /bin/bash ./dell-run-speccpu.sh speed --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
   --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
 runcpu --nobuild --action validate --define default-platform-flags -c
   ic2023.0-lin-core-avx2-speed-20221201.cfg --define cores=6 --tune base,peak -o all --define smt-on
   --define drop_caches --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
   --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1 fpspeed
 runcpu --nobuild --action validate --define default-platform-flags --configfile
   ic2023.0-lin-core-avx2-speed-20221201.cfg --define cores=6 --tune base,peak --output_format all --define
   smt-on --define drop_caches --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define
   DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1 --nopower
   --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) E E-2456
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x121
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 6
    siblings        : 12
    1 physical ids (chips)
```

(Continued on next page)

Platform Notes (Continued)

```
    12 processors (hardware threads)
    physical id 0: core ids 0-5
    physical id 0: apicids 0-11
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   42 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          12
  On-line CPU(s) list:             0-11
  Vendor ID:                       GenuineIntel
  Model name:                      Intel(R) Xeon(R) E E-2456
  CPU family:                      6
  Model:                           183
  Thread(s) per core:              2
  Core(s) per socket:              6
  Socket(s):                       1
  Stepping:                        1
  BogoMIPS:                        6604.80
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic
                                   movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                   3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp
                                   ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed
                                   adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                                   split_lock_detect avx_vnni dtherm ida arat pln pts hfi umip pku ospke
                                   waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear
                                   serialize pconfig arch_lbr flush_l1d arch_capabilities
  L1d cache:                       288 KiB (6 instances)
  L1i cache:                       192 KiB (6 instances)
  L2 cache:                        12 MiB (6 instances)
  L3 cache:                        18 MiB (1 instance)
  NUMA node(s):                    1
  NUMA node0 CPU(s):               0-11
  Vulnerability Itlb multihit:     Not affected
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
  Vulnerability Mmio stale data:   Not affected
  Vulnerability Retbleed:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                   sequence
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     288K   12 Data            1    64        1             64
     L1i       32K     192K    8 Instruction     1    64        1             64
```

(Continued on next page)

Platform Notes (Continued)

```
     L2         2M      12M   16 Unified         2  2048        1             64
     L3        18M      18M    9 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-11
  node 0 size: 63756 MB
  node 0 free: 47922 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       65286804 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 25 23:54

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
                   bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings kdump
                   kdump-early klog lvm2-monitor nscd nvmefc-boot-connections postfix purge-kernels rollback
                   rsyslog sapconf smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4
                   wickedd-dhcp6 wickedd-nanny wpa_supplicant xrdp
  enabled-runtime  systemd-remount-fs
  disabled         NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
                   autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
                   chrony-wait chronyd console-getty cups cups-browsed debug-shell dmraid-activation dnsmasq
                   ebtables exchange-bmc-os-info firewalld gpm grub2-once haveged haveged-switch-root
                   hwloc-dump-hwdata ipmi ipmievd irqbindall issue-add-ssh-keys kexec-load ksm kvm_stat
                   lunmask man-db-create multipathd nfs nfs-blkmap nmb nvmf-autoconnect openvpn@
                   ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon saprouter saptune serial-getty@
                   set_kthread_prio smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd svnserve
                   sysstat systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
                   wpa_supplicant@ xrdp-sesman
  indirect         pcscd saned@ uuidd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
  root=/dev/mapper/system-root
  splash=silent
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=308M,high
  crashkernel=72M,low
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing                  0
  kernel.randomize_va_space              2
  vm.compaction_proactiveness           20
  vm.dirty_background_bytes       314572800
  vm.dirty_background_ratio              0
  vm.dirty_bytes                  629145600
  vm.dirty_expire_centisecs            3000
  vm.dirty_ratio                         0
  vm.dirty_writeback_centisecs         500
  vm.dirtytime_expire_seconds        43200
  vm.extfrag_threshold                 500
  vm.min_unmapped_ratio                  1
  vm.nr_hugepages                        0
  vm.nr_hugepages_mempolicy              0
  vm.nr_overcommit_hugepages             0
  vm.swappiness                         60
  vm.watermark_boost_factor          15000
  vm.watermark_scale_factor             10
  vm.zone_reclaim_mode                   0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         always madvise [never]
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5

------------------------------------------------------------
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   40G   10G   31G  25% /mnt/ramdisk

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
```

(Continued on next page)

Platform Notes (Continued)

```
     Vendor:         Dell Inc.
     Product:        PowerEdge R260
     Product Family: PowerEdge
     Serial:         1234567

 ------------------------------------------------------------
 21. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     2x 00AD00001201 HMCG88AEBEA081N 32 GB 2 rank 4800, configured at 4400


 ------------------------------------------------------------
 22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Dell Inc.
    BIOS Version:      1.2.2
    BIOS Date:         03/21/2024
    BIOS Revision:     1.2
```

Compiler Version Notes

```
==============================================================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -g -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib
-ljemalloc

Fortran benchmarks:
-m64 -g -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:
-m64 -g -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math

(Continued on next page)

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using Fortran, C, and C++:
-m64 -g -std=c++14 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Peak Compiler Invocation

C benchmarks:
icx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

(Continued on next page)

Peak Optimization Flags (Continued)

Fortran benchmarks:

603.bwaves_s: `-m64 -g -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs -align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

649.fotonik3d_s: `basepeak = yes`

654.roms_s: `basepeak = yes`

Benchmarks using both Fortran and C:

621.wrf_s: `basepeak = yes`

627.cam4_s: `-m64 -g -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc`

628.pop2_s: `basepeak = yes`

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml`

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-03-25 22:44:56-0400.
Report generated on 2024-06-03 08:33:57 by CPU2017 PS formatter v6716.
Originally published on 2024-06-01.