# SPEC CPU®2017 Integer Rate Result

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## Dell Inc.

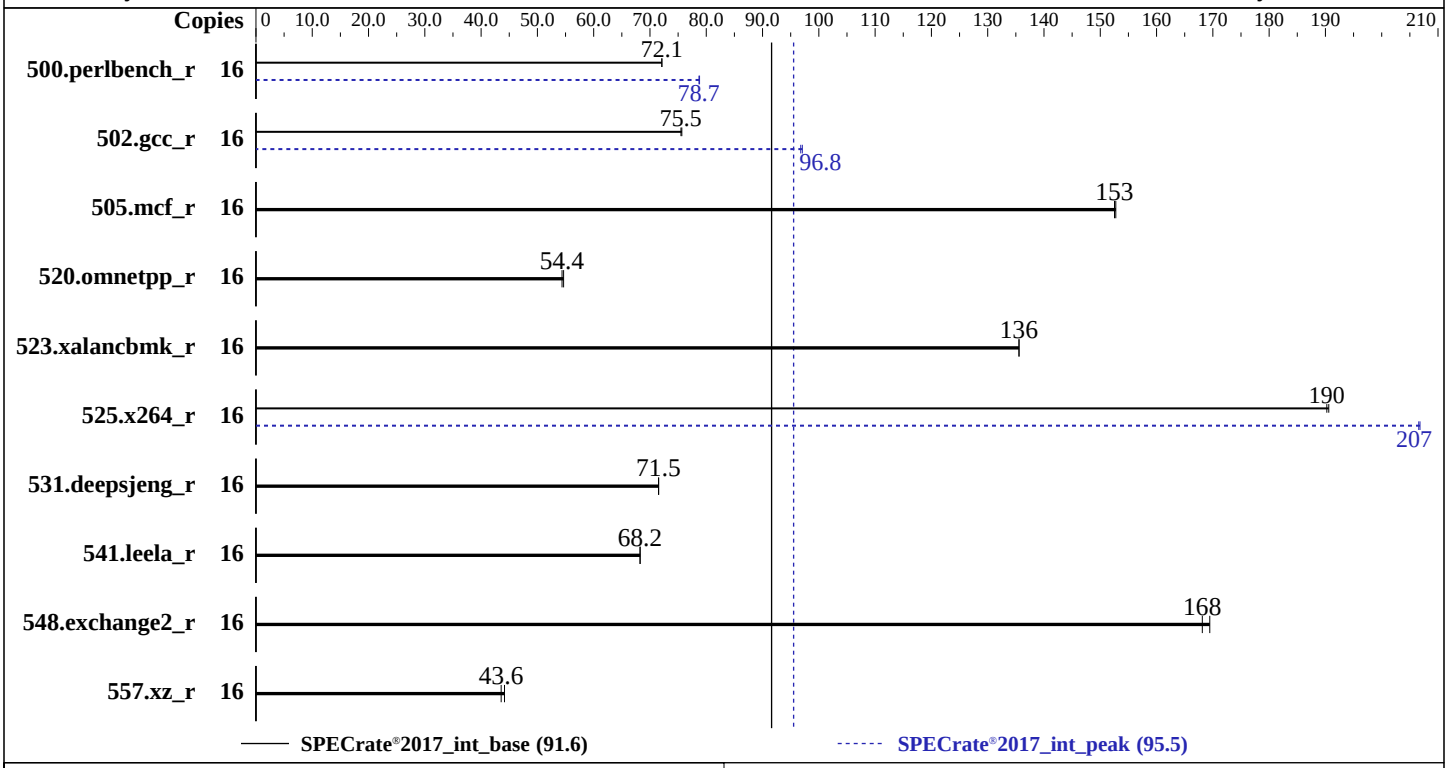
## PowerEdge T160 (Intel Xeon E-2488)

**SPECrate®2017_int_base = 91.6**

**SPECrate®2017_int_peak = 95.5**

**CPU2017 License:** 6573
**Test Sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test Date:** Mar-2024
**Hardware Availability:** May-2024
**Software Availability:** Dec-2023




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E-2488 |
| Max MHz: | 5600 |
| Nominal: | 3200 |
| Enabled: | 8 cores, 1 chip, 2 threads/core |
| Orderable: | 1 chip |
| Cache L1: | 32 KB I + 48 KB D on chip per core |
| L2: | 2 MB I+D on chip per core |
| L3: | 24 MB I+D on chip per chip |
| Other: | None |
| Memory: | 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400) |
| Storage: | 40 GB on tmpfs |
| Other: | CPU Cooling: Air |

## Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default |
| Compiler: | C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux; |
| Parallel: | No |
| Firmware: | Version 1.2.2 released Mar-2024 |
| File System: | tmpfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 32/64-bit |
| Other: | jemalloc memory allocator V5.0.1 |
| Power Management: | BIOS and OS set to prefer performance at the cost of additional power usage. |

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 500.perlbench_r | 16 | 353 | 72.1 | **353** | **72.1** | | | 16 | 323 | 78.8 | **324** | **78.7** | | |
| 502.gcc_r | 16 | **300** | **75.5** | 299 | 75.7 | | | 16 | 233 | 97.1 | **234** | **96.8** | | |
| 505.mcf_r | 16 | **170** | **153** | 169 | 153 | | | 16 | **170** | **153** | 169 | 153 | | |
| 520.omnetpp_r | 16 | **386** | **54.4** | 384 | 54.7 | | | 16 | **386** | **54.4** | 384 | 54.7 | | |
| 523.xalancbmk_r | 16 | 125 | 136 | **125** | **136** | | | 16 | 125 | 136 | **125** | **136** | | |
| 525.x264_r | 16 | **147** | **190** | 147 | 191 | | | 16 | 135 | 207 | **136** | **207** | | |
| 531.deepsjeng_r | 16 | **256** | **71.5** | 256 | 71.5 | | | 16 | **256** | **71.5** | 256 | 71.5 | | |
| 541.leela_r | 16 | 388 | 68.3 | **388** | **68.2** | | | 16 | 388 | 68.3 | **388** | **68.2** | | |
| 548.exchange2_r | 16 | 247 | 169 | **249** | **168** | | | 16 | 247 | 169 | **249** | **168** | | |
| 557.xz_r | 16 | 391 | 44.2 | **397** | **43.6** | | | 16 | 391 | 44.2 | **397** | **43.6** | | |

**SPECrate®2017_int_base = 91.6**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The taskset mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate taskset commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/mnt
     /ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

## General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
```

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## General Notes (Continued)

```
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

Benchmark run from a 40 GB ramdisk created with the cmd: "mount -t tmpfs -o size=40G tmpfs /mnt/ramdisk"
```

## Platform Notes

```
BIOS settings:
        DIMM Self Healing on
    Uncorrectable Memory Error : Disabled

     Virtualization Technology : Disabled
                  LLC Prefetch : Disabled
           Dead Line LLC Alloc : Disabled

                System Profile : Custom
          CPU Power Management : Maximum Performance
                        C1E : Disabled
                    C States : Autonomous
            PCI ASPM L1 Link
              Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Mar 29 23:02:13 2024

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
```

**(Continued on next page)**

## Platform Notes (Continued)

```
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043/lp)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   23:02:13 up 6 min,  1 user,  load average: 0.12, 0.05, 0.01
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                23:01   29.00s  0.79s  0.00s /bin/bash ./dell-run-speccpu.sh rate
  --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
  html,pdf,txt --define DL-LQC=1

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 256416
  max locked memory       (kbytes, -l) 64
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 256416
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 /bin/bash ./DELL_rate.sh
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-main.sh rate
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
   --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
 /bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1
   --define DL-VERS=4.9.2 --output_format html,pdf,txt --define DL-LQC=1
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 -c
   ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=8 --define physicallogical
   --define no-numa --tune base,peak -o all --define drop_caches --iterations 2 --define
   DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
   html,pdf,txt --define DL-LQC=1 intrate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 --configfile
   ic2023.2.3-lin-core-avx2-rate-20231121.cfg --define smt-on --define cores=8 --define physicallogical
   --define no-numa --tune base,peak --output_format all --define drop_caches --iterations 2 --define
   DL-BIOSinc=Dell-BIOS_Xeon-4E.inc --define DL-BIOS-LogProc=1 --define DL-VERS=4.9.2 --output_format
   html,pdf,txt --define DL-LQC=1 --nopower --runmode rate --tune base:peak --size refrate intrate --nopreenv
   --note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0
   --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
```

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## Platform Notes (Continued)

```
------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) E E-2488
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 183
    stepping        : 1
    microcode       : 0x121
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
    cpu cores       : 8
    siblings        : 16
    1 physical ids (chips)
    16 processors (hardware threads)
    physical id 0: core ids 0-7
    physical id 0: apicids 0-15
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   42 bits physical, 48 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          16
  On-line CPU(s) list:             0-15
  Vendor ID:                       GenuineIntel
  Model name:                      Intel(R) Xeon(R) E E-2488
  CPU family:                      6
  Model:                           183
  Thread(s) per core:              2
  Core(s) per socket:              8
  Socket(s):                       1
  Stepping:                        1
  BogoMIPS:                        6374.40
  Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                   clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                                   lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                                   nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                                   ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2 x2apic
                                   movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                   3dnowprefetch cpuid_fault invpcid_single ssbd ibrs ibpb stibp
                                   ibrs_enhanced fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed
                                   adx smap clflushopt clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves
                                   split_lock_detect avx_vnni dtherm ida arat pln pts hfi umip pku ospke
                                   waitpkg gfni vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear
                                   serialize pconfig arch_lbr flush_l1d arch_capabilities
  L1d cache:                       384 KiB (8 instances)
  L1i cache:                       256 KiB (8 instances)
  L2 cache:                        16 MiB (8 instances)
  L3 cache:                        24 MiB (1 instance)
  NUMA node(s):                    1
  NUMA node0 CPU(s):               0-15
  Vulnerability Itlb multihit:     Not affected
  Vulnerability L1tf:              Not affected
  Vulnerability Mds:               Not affected
  Vulnerability Meltdown:          Not affected
```

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## Platform Notes (Continued)

```
  Vulnerability Mmio stale data:   Not affected
  Vulnerability Retbleed:          Not affected
  Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
  Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:        Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
                                   sequence
  Vulnerability Srbds:             Not affected
  Vulnerability Tsx async abort:   Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     384K   12 Data            1    64        1             64
     L1i       32K     256K    8 Instruction     1    64        1             64
     L2         2M      16M   16 Unified         2  2048        1             64
     L3        24M      24M   12 Unified         3 32768        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-15
  node 0 size: 64135 MB
  node 0 free: 55171 MB
  node distances:
  node   0
    0:  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       65674916 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 29 22:56

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
                   avahi-daemon bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings
                   klog lvm2-monitor nscd nvmefc-boot-connections postfix purge-kernels rollback rsyslog
                   smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
                   wpa_supplicant
  enabled-runtime  systemd-remount-fs
  disabled         NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
                   autoyast-initscripts avahi-dnsconfd blk-availability bluetooth-mesh boot-sysctl
                   ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell
                   dmraid-activation dnsmasq ebtables exchange-bmc-os-info firewalld gpm grub2-once haveged
                   haveged-switch-root hwloc-dump-hwdata ipmi ipmievd irqbindall issue-add-ssh-keys
                   kexec-load ksm kvm_stat lunmask man-db-create multipathd nfs nfs-blkmap nmb
                   nvmf-autoconnect openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon
                   serial-getty@ set_kthread_prio smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2 update-system-flatpaks upower vncserver@
                   wpa_supplicant@
```

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## Platform Notes (Continued)

```
  indirect         pcscd saned@ wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
  root=UUID=0d71c0ba-5906-4cee-82bd-5483a200921b
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
```

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## Platform Notes (Continued)

```
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5

------------------------------------------------------------
19. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
  Filesystem     Type   Size  Used Avail Use% Mounted on
  tmpfs          tmpfs   40G  4.1G   36G  11% /mnt/ramdisk

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Dell Inc.
    Product:        PowerEdge T160
    Product Family: PowerEdge
    Serial:         1234567

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    1x 00AD00001201 HMCG88AEBEA081N 32 GB 2 rank 4800, configured at 4400
    1x 00AD00001201 HMCG88AEBEA084N 32 GB 2 rank 4800, configured at 4400


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Dell Inc.
   BIOS Version:      1.2.2
   BIOS Date:         03/21/2024
   BIOS Revision:     1.2
```

## Compiler Version Notes

```
==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C       | 502.gcc_r(peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

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## Compiler Version Notes (Continued)

```
==============================================================================================================
C       | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
        | 557.xz_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++     | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
        | 541.leela_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran | 548.exchange2_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

## Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
```
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:
```
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:
```
-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:
```
icx
```

C++ benchmarks:
```
icpx
```

Fortran benchmarks:
```
ifx
```

## Peak Portability Flags

500.perlbench_r: `-DSPEC_LP64 -DSPEC_LINUX_X64`
502.gcc_r: `-D_FILE_OFFSET_BITS=64`
505.mcf_r: `-DSPEC_LP64`
520.omnetpp_r: `-DSPEC_LP64`
523.xalancbmk_r: `-DSPEC_LP64 -DSPEC_LINUX`
525.x264_r: `-DSPEC_LP64`
531.deepsjeng_r: `-DSPEC_LP64`
541.leela_r: `-DSPEC_LP64`
548.exchange2_r: `-DSPEC_LP64`

**(Continued on next page)**

## Peak Portability Flags (Continued)

557.xz_r: -DSPEC_LP64

## Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib
-ljemalloc

505.mcf_r: basepeak = yes

525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

**(Continued on next page)**

# Peak Optimization Flags (Continued)

548.exchange2_r: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml`
`http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml`


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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