SPEC CPU®2017 Floating Point Speed Result

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Cisco Systems

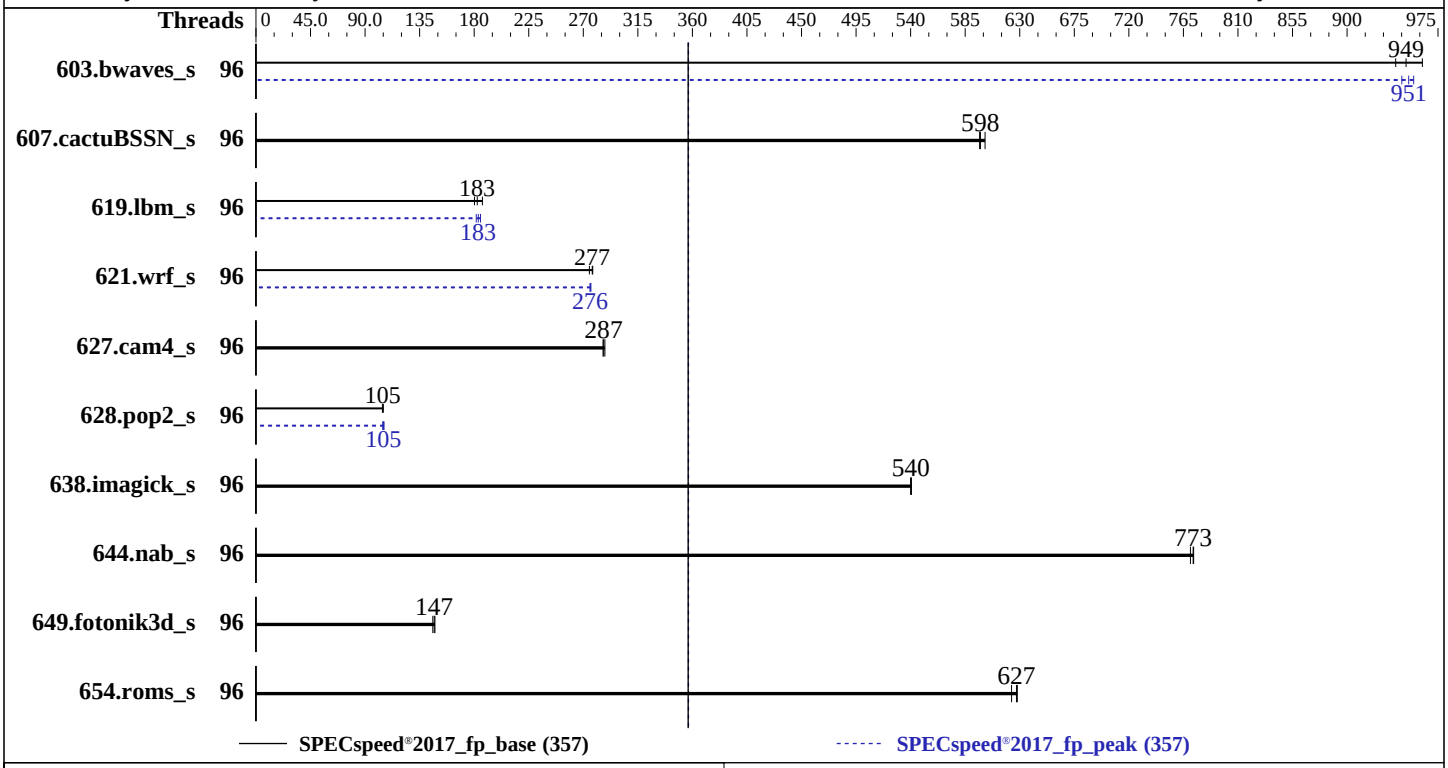
Cisco UCS C245 M8 (AMD EPYC 9684X 96-Core Processor)

SPECspeed®2017_fp_base = 357

SPECspeed®2017_fp_peak = 357

CPU2017 License: 9019
Test Sponsor: Cisco Systems
Tested by: Cisco Systems

Test Date: Apr-2024
Hardware Availability: Jun-2024
Software Availability: Feb-2024



Hardware

CPU Name:	AMD EPYC 9684X
Max MHz:	3700
Nominal:	2550
Enabled:	96 cores, 1 chip
Orderable:	1 chips
Cache L1:	32 KB I + 32 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	1152 MB I+D on chip per chip, 96 MB shared / 8 cores
Other:	None
Memory:	1536 GB (12 x 128 GB 2Rx4 PC5-5600B-R, running at 4800)
Storage:	1 x 1.6 TB NVME SSD
Other:	CPU Cooling: Air

Software

OS:	SUSE Linux Enterprise Server 15 SP5 5.14.21-150500.53-default
Compiler:	C/C++/Fortran: Version 4.2.0 of AOCC
Parallel:	Yes
Firmware:	Version 4.3.4.295 released Apr-2024
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage.

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	96	62.8	940	**62.2**	**949**	61.3	962	96	62.4	945	61.8	955	**62.0**	**951**
607.cactuBSSN_s	96	**27.9**	**598**	27.7	601	27.9	597	96	**27.9**	**598**	27.7	601	27.9	597
619.lbm_s	96	29.1	180	**28.7**	**183**	28.0	187	96	28.3	185	**28.6**	**183**	28.8	182
621.wrf_s	96	47.6	278	**47.7**	**277**	48.1	275	96	48.0	276	47.9	276	**48.0**	**276**
627.cam4_s	96	**30.9**	**287**	31.0	286	30.8	288	96	**30.9**	**287**	31.0	286	30.8	288
628.pop2_s	96	114	104	**114**	**105**	113	105	96	114	105	**113**	**105**	112	106
638.imagick_s	96	**26.7**	**540**	26.7	540	26.7	541	96	**26.7**	**540**	26.7	540	26.7	541
644.nab_s	96	22.6	773	**22.6**	**773**	22.7	771	96	22.6	773	**22.6**	**773**	22.7	771
649.fotonik3d_s	96	**62.0**	**147**	61.7	148	62.5	146	96	**62.0**	**147**	61.7	148	62.5	146
654.roms_s	96	25.1	628	**25.1**	**627**	25.3	623	96	25.1	628	**25.1**	**627**	25.3	623

SPECspeed®2017_fp_base = 357

SPECspeed®2017_fp_peak = 357

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
To always enable THP for peak runs of:
603.bwaves_s, 607.cactuBSSN_s, 619.lbm_s, 627.cam4_s, 628.pop2_s, 638.imagick_s, 644.nab_s, 649.fotonik3d_s:
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'
```

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Operating System Notes (Continued)

```
run as root.
To disable THP for peak runs of 621.wrf_s:
'echo never > /sys/kernel/mm/transparent_hugepage/enabled; echo always > /sys/kernel/mm/transparent_hugepage/defrag'
run as root.
To enable THP only on request for peak runs of 654.roms_s:
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled; echo madvise > /sys/kernel/mm/transparent_hugepage/defrag'
run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-95"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc402_znver4_A_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "592M"
OMP_THREAD_LIMIT = "96"

Environment variables set by runcpu during the 603.bwaves_s peak run:
GOMP_CPU_AFFINITY = "0-95"

Environment variables set by runcpu during the 619.lbm_s peak run:
GOMP_CPU_AFFINITY = "0-95"

Environment variables set by runcpu during the 621.wrf_s peak run:
GOMP_CPU_AFFINITY = "0-95"

Environment variables set by runcpu during the 628.pop2_s peak run:
GOMP_CPU_AFFINITY = "0-95"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS settings:
SMT Mode set to Disabled
NUMA nodes per socket set to NPS1
Determinism Slider set to Power
DF C-States set to Disabled
TDP set to 400
PPT set to 400
ACPI SRAT L3 Cache as NUMA Domain set to Enabled
```

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Platform Notes (Continued)

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Sun May  5 20:27:07 2024

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   20:27:07 up  3:08,  2 users,  load average: 75.03, 88.67, 86.21
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                17:18    3.00s  1.83s  0.32s -bash
  root     pts/0    10.110.140.78    20:08   37.00s  0.03s  0.03s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 6191339
  max locked memory       (kbytes, -l) 64
```

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Platform Notes (Continued)

```
   max memory size         (kbytes, -m) unlimited
   open files                      (-n) 1024
   pipe size            (512 bytes, -p) 8
   POSIX message queues     (bytes, -q) 819200
   real-time priority              (-r) 0
   stack size              (kbytes, -s) unlimited
   cpu time               (seconds, -t) unlimited
   max user processes              (-u) 6191339
   virtual memory          (kbytes, -v) unlimited
   file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 30
 login -- root
 -bash
 -bash
 runcpu --nobuild --action validate -c amd_speed_aocc402_znver4_A1.cfg --define cores=48 --tune base,peak
   --output_format all --define fpspeedaffinity --define drop_caches --nopower --runmode speed --size
   refspeed fpspeed
 runcpu --nobuild --action validate --configfile amd_speed_aocc402_znver4_A1.cfg --define cores=48 --tune
   base,peak --output_format all --define fpspeedaffinity --define drop_caches --nopower --runmode speed
   --size refspeed --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
   --note-preenv --logfile $SPEC/tmp/CPU2017.072/templogs/preenv.fpspeed.072.0.log --lognum 072.0
   --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9684X 96-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 25
    model           : 17
    stepping        : 2
    microcode       : 0xa101248
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 3584 4K pages
    cpu cores       : 96
    siblings        : 96
    1 physical ids (chips)
    96 processors (hardware threads)
    physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
    physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                    x86_64
  CPU op-mode(s):                  32-bit, 64-bit
  Address sizes:                   52 bits physical, 57 bits virtual
  Byte Order:                      Little Endian
  CPU(s):                          96
  On-line CPU(s) list:             0-95
  Vendor ID:                       AuthenticAMD
  Model name:                      AMD EPYC 9684X 96-Core Processor
  CPU family:                      25
```

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Platform Notes (Continued)

```
   Model:                           17
   Thread(s) per core:              1
   Core(s) per socket:              96
   Socket(s):                       1
   Stepping:                        2
   Frequency boost:                 enabled
   CPU max MHz:                     3715.4290
   CPU min MHz:                     1500.0000
   BogoMIPS:                        5092.34
   Flags:                           fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                    clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                                    constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                                    aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                                    x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
                                    extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt
                                    tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
                                    cdp_l3 invpcid_single hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                                    vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
                                    avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni
                                    avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
                                    cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru
                                    wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
                                    vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic
                                    v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2
                                    gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
                                    overflow_recov succor smca fsrm flush_l1d
   Virtualization:                  AMD-V
   L1d cache:                       3 MiB (96 instances)
   L1i cache:                       3 MiB (96 instances)
   L2 cache:                        96 MiB (96 instances)
   L3 cache:                        1.1 GiB (12 instances)
   NUMA node(s):                    12
   NUMA node0 CPU(s):               0-7
   NUMA node1 CPU(s):               8-15
   NUMA node2 CPU(s):               16-23
   NUMA node3 CPU(s):               24-31
   NUMA node4 CPU(s):               32-39
   NUMA node5 CPU(s):               40-47
   NUMA node6 CPU(s):               48-55
   NUMA node7 CPU(s):               56-63
   NUMA node8 CPU(s):               64-71
   NUMA node9 CPU(s):               72-79
   NUMA node10 CPU(s):              80-87
   NUMA node11 CPU(s):              88-95
   Vulnerability Itlb multihit:     Not affected
   Vulnerability L1tf:              Not affected
   Vulnerability Mds:               Not affected
   Vulnerability Meltdown:          Not affected
   Vulnerability Mmio stale data:   Not affected
   Vulnerability Retbleed:          Not affected
   Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
   Vulnerability Spectre v1:        Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:        Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP disabled, RSB
                                    filling, PBRSB-eIBRS Not affected
   Vulnerability Srbds:             Not affected
   Vulnerability Tsx async abort:   Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       32K       3M    8 Data            1    64        1             64
```

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Platform Notes (Continued)

```
      L1i       32K       3M    8 Instruction     1    64        1             64
      L2         1M      96M    8 Unified         2  2048        1             64
      L3        96M     1.1G   16 Unified         3 98304        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 12 nodes (0-11)
  node 0 cpus: 0-7
  node 0 size: 128720 MB
  node 0 free: 128478 MB
  node 1 cpus: 8-15
  node 1 size: 129021 MB
  node 1 free: 128788 MB
  node 2 cpus: 16-23
  node 2 size: 129021 MB
  node 2 free: 128783 MB
  node 3 cpus: 24-31
  node 3 size: 129021 MB
  node 3 free: 128702 MB
  node 4 cpus: 32-39
  node 4 size: 129021 MB
  node 4 free: 128763 MB
  node 5 cpus: 40-47
  node 5 size: 129021 MB
  node 5 free: 128803 MB
  node 6 cpus: 48-55
  node 6 size: 129021 MB
  node 6 free: 128695 MB
  node 7 cpus: 56-63
  node 7 size: 128987 MB
  node 7 free: 128703 MB
  node 8 cpus: 64-71
  node 8 size: 128960 MB
  node 8 free: 128697 MB
  node 9 cpus: 72-79
  node 9 size: 129021 MB
  node 9 free: 128720 MB
  node 10 cpus: 80-87
  node 10 size: 129021 MB
  node 10 free: 128771 MB
  node 11 cpus: 88-95
  node 11 size: 129021 MB
  node 11 free: 128733 MB
  node distances:
  node   0   1   2   3   4   5   6   7   8   9  10  11
    0:  10  11  11  11  11  11  11  11  11  11  11  11
    1:  11  10  11  11  11  11  11  11  11  11  11  11
    2:  11  11  10  11  11  11  11  11  11  11  11  11
    3:  11  11  11  10  11  11  11  11  11  11  11  11
    4:  11  11  11  11  10  11  11  11  11  11  11  11
    5:  11  11  11  11  11  10  11  11  11  11  11  11
    6:  11  11  11  11  11  11  10  11  11  11  11  11
    7:  11  11  11  11  11  11  11  10  11  11  11  11
    8:  11  11  11  11  11  11  11  11  10  11  11  11
    9:  11  11  11  11  11  11  11  11  11  10  11  11
   10:  11  11  11  11  11  11  11  11  11  11  10  11
   11:  11  11  11  11  11  11  11  11  11  11  11  10

------------------------------------------------------------
```

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Platform Notes (Continued)

```
 9. /proc/meminfo
    MemTotal:       1585013792 kB

------------------------------------------------------------
10. who -r
  run-level 3 May 5 17:18

------------------------------------------------------------
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance
                   issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                   smartd sshd systemd-pstore tuned wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                   wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                   chronyd console-getty cups cups-browsed debug-shell ebtables exchange-bmc-os-info
                   firewalld gpm grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievd
                   issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind
                   rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
                   systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd udisks2 vncserver@
  indirect         wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
  root=UUID=071246f7-b717-41ba-8fcd-11daa59ff776
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 0:
    current policy: frequency should be within 1.50 GHz and 2.55 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. tuned-adm active
  Current active profile: throughput-performance

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               0
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
```

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Platform Notes (Continued)

```
  vm.dirty_expire_centisecs       10000
  vm.dirty_ratio                     40
  vm.dirty_writeback_centisecs     1500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      10
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5

------------------------------------------------------------
20. Disk information
SPEC is set to: /home/cpu2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda2      xfs   221G   66G  156G  30% /

------------------------------------------------------------
21. /sys/devices/virtual/dmi/id
    Vendor:         Cisco Systems Inc
    Product:        UCSC-C245-M8SX
    Serial:         WZP27520MSZ

------------------------------------------------------------
22. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    9x 0xAD00 HMCT04AGERA197N 128 GB 2 rank 5600, configured at 4800
    3x 0xAD00 HMCT04AGERA199N 128 GB 2 rank 5600, configured at 4800


------------------------------------------------------------
```

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Platform Notes (Continued)

```
 23. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       Cisco Systems, Inc.
    BIOS Version:      C245M8.4.3.4.295.0430240859
    BIOS Date:         04/30/2024
    BIOS Revision:     5.27
```

Compiler Version Notes

```
==============================================================================================================
C               | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
AMD clang version 16.0.3 (CLANG: AOCC_4.2.0-Build#89 2023_12_13)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/aocc-compiler-4.2.0/bin
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop

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Base Optimization Flags (Continued)

Fortran benchmarks (continued):
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Benchmarks using Fortran, C, and C++:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang

Base Other Flags

C benchmarks:
-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3
-fvector-transform -fscalar-transform -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang

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Peak Optimization Flags (Continued)

649.fotonik3d_s: `basepeak = yes`

654.roms_s: `basepeak = yes`

Benchmarks using both Fortran and C:

621.wrf_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=9 -mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining -mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt -O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang`

627.cam4_s: `basepeak = yes`

628.pop2_s: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=9 -mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining -mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt -Mrecursive -fvector-transform -fscalar-transform -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang`

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: `basepeak = yes`

Peak Other Flags

C benchmarks:
`-Wno-return-type -Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

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Peak Other Flags (Continued)

Benchmarks using both Fortran and C:
`-Wno-return-type -Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-return-type -Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.html`
`http://www.spec.org/cpu2017/flags/aocc400-flags.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.xml`
`http://www.spec.org/cpu2017/flags/aocc400-flags.xml`

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Tested with SPEC CPU®2017 v1.1.9 on 2024-05-05 23:27:07-0400.
Report generated on 2024-06-04 11:55:16 by CPU2017 PS formatter v6716.
Originally published on 2024-06-04.