# SPEC CPU®2017 Integer Rate Result

**Fujitsu**

PRIMERGY TX1330 M6, Intel Xeon E-2468, 2.6 GHz

<table>
<thead>
<tr>
<th>SPECrate®2017_int_base</th>
<th>82.4</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPECrate®2017_int_peak</td>
<td>Not Run</td>
</tr>
</tbody>
</table>

**CPU2017 License:** 19  
**Test Sponsor:** Fujitsu  
**Tested by:** Fujitsu  
**Test Date:** Apr-2024  
**Hardware Availability:** Apr-2024  
**Software Availability:** Dec-2023

<table>
<thead>
<tr>
<th>Copies</th>
<th>SPECrate®2017_int_base (82.4)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>16</td>
</tr>
<tr>
<td>10</td>
<td>16</td>
</tr>
<tr>
<td>20</td>
<td>16</td>
</tr>
<tr>
<td>30</td>
<td>16</td>
</tr>
<tr>
<td>40</td>
<td>16</td>
</tr>
<tr>
<td>50</td>
<td>16</td>
</tr>
<tr>
<td>60</td>
<td>16</td>
</tr>
<tr>
<td>70</td>
<td>16</td>
</tr>
<tr>
<td>80</td>
<td>16</td>
</tr>
<tr>
<td>90</td>
<td>16</td>
</tr>
<tr>
<td>100</td>
<td>16</td>
</tr>
<tr>
<td>110</td>
<td>16</td>
</tr>
<tr>
<td>120</td>
<td>16</td>
</tr>
<tr>
<td>130</td>
<td>16</td>
</tr>
<tr>
<td>140</td>
<td>16</td>
</tr>
<tr>
<td>150</td>
<td>16</td>
</tr>
<tr>
<td>160</td>
<td>16</td>
</tr>
<tr>
<td>170</td>
<td>16</td>
</tr>
<tr>
<td>180</td>
<td>16</td>
</tr>
<tr>
<td>190</td>
<td>16</td>
</tr>
<tr>
<td>200</td>
<td>16</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Hardware</th>
<th>Software</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>CPU Name:</strong> Intel Xeon E-2468</td>
<td><strong>OS:</strong> SUSE Linux Enterprise Server 15 SP5</td>
</tr>
<tr>
<td><strong>Max MHz:</strong> 5200</td>
<td><strong>5.14.21-150500.53-default</strong></td>
</tr>
<tr>
<td><strong>Nominal:</strong> 2600</td>
<td><strong>Compiler:</strong> C/C++: Version 2024.0.2 of Intel oneAPI DPC++/C++</td>
</tr>
<tr>
<td><strong>Enabled:</strong> 8 cores, 1 chip, 2 threads/core</td>
<td><strong>Compiler for Linux:</strong></td>
</tr>
<tr>
<td><strong>Orderable:</strong> 1 chip</td>
<td><strong>Fortran: Version 2024.0.2 of Intel Fortran</strong></td>
</tr>
<tr>
<td><strong>Cache L1:</strong> 32 KB I + 48 KB D on chip per core</td>
<td><strong>Compiler for Linux:</strong></td>
</tr>
<tr>
<td><strong>L2:</strong> 2 MB I+D on chip per core</td>
<td><strong>Firmware:</strong> Fujitsu BIOS Version V5.0.0.27 R1.5.0 for D4132-A1x. Released Jul-2024</td>
</tr>
<tr>
<td><strong>L3:</strong> 24 MB I+D on chip per chip</td>
<td>tested as V5.0.0.27 R1.0.0 for D4132-A1x Mar-2024</td>
</tr>
<tr>
<td><strong>Other:</strong> None</td>
<td><strong>File System:</strong> xfs</td>
</tr>
<tr>
<td><strong>Memory:</strong> 64 GB (2 x 32 GB 2Rx8 PC5-4800B-E, running at 4400)</td>
<td><strong>System State:</strong> Run level 3 (multi-user)</td>
</tr>
<tr>
<td><strong>Storage:</strong> 1 x SATA M.2 SSD, 960 GB</td>
<td><strong>Base Pointers:</strong> 64-bit</td>
</tr>
<tr>
<td><strong>Other:</strong> CPU Cooling: Air</td>
<td><strong>Peak Pointers:</strong> Not Applicable</td>
</tr>
<tr>
<td><strong>Power Management:</strong> BIOS set to prefer performance at the cost of additional power usage</td>
<td><strong>Other:</strong> None</td>
</tr>
</tbody>
</table>

---

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<td>Software Availability: Dec-2023</td>
</tr>
</tbody>
</table>

### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>16</td>
<td>406</td>
<td>62.8</td>
<td>415</td>
<td>61.4</td>
<td>414</td>
<td>61.6</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>16</td>
<td>313</td>
<td>72.3</td>
<td>314</td>
<td>72.1</td>
<td>313</td>
<td>72.4</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>16</td>
<td>186</td>
<td>139</td>
<td>185</td>
<td>140</td>
<td>185</td>
<td>140</td>
</tr>
<tr>
<td>520.omnetpp_r</td>
<td>16</td>
<td>386</td>
<td>54.4</td>
<td>386</td>
<td>54.4</td>
<td>387</td>
<td>54.2</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>16</td>
<td>143</td>
<td>118</td>
<td>144</td>
<td>118</td>
<td>143</td>
<td>119</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>16</td>
<td>173</td>
<td>162</td>
<td>176</td>
<td>160</td>
<td>176</td>
<td>159</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>16</td>
<td>303</td>
<td>60.5</td>
<td>300</td>
<td>61.0</td>
<td>300</td>
<td>61.0</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>16</td>
<td>459</td>
<td>57.7</td>
<td>459</td>
<td>57.7</td>
<td>459</td>
<td>57.7</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>16</td>
<td>270</td>
<td>155</td>
<td>270</td>
<td>155</td>
<td>270</td>
<td>155</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>16</td>
<td>415</td>
<td>41.7</td>
<td>416</td>
<td>41.6</td>
<td>420</td>
<td>41.1</td>
</tr>
</tbody>
</table>

### Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```bash
LD_LIBRARY_PATH = 
/home/Benchmark/speccpu.ic2024.0.2/lib/intel64:/home/Benchmark/speccpu.ic2024.0.2/lib/ia32:/home/Benchmark/speccpu.ic2024.0.2/je5.0.1-32/

MALLOC_CONF = "retain:true"
```

### General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Files system page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop_caches  
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
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SPECrate®2017_int_base = 82.4
SPECrate®2017_int_peak = Not Run

Platform Notes

BIOS configuration:
Fan Control = Full
Intel(R) Turbo Boost Max Technology 3.0 = Disabled
Total Memory Encryption = Disabled

Sysinfo program /home/Benchmark/speccpu.ic2024.0.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed May 15 18:28:08 2024

SUT (System Under Test) info as seen by some common utilities.

---------------------------------------------------------------------
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS
---------------------------------------------------------------------

1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux

2. w
18:28:08 up 5:25, 1 user, load average: 4.35, 12.25, 14.65
USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
root     tty1     -                13:03    5:22m  1.18s  0.12s -bash

3. Username
From environment variable $USER: root

4. ulimit -a
   core file size (blocks, -c) unlimited
   data seg size (kbytes, -d) unlimited

(Continued on next page)
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### Platform Notes (Continued)

- scheduling priority (-e) 0
- file size (blocks, -f) unlimited
- pending signals (-i) 254848
- max locked memory (kbytes, -l) 64
- max memory size (kbytes, -m) unlimited
- open files (-n) 1024
- pipe size (512 bytes, -p) 8
- POSIX message queues (bytes, -q) 819200
- real-(r) 0
- stack size (kbytes, -s) unlimited
- cpu time (seconds, -t) unlimited
- max user processes (-u) 254848
- virtual memory (kbytes, -v) unlimited
- file locks (-x) unlimited

---

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
  -bash
  -bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 -c
  ic2024.0.2-lin-core-avx2-rate-20231213.cfg --define smt-on --define cores=16 --define physicallogical
  --define no numa --tune base -- all --define drop_caches intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=16 --configfile
  ic2024.0.2-lin-core-avx2-rate-20231213.cfg --define smt-on --define cores=16 --define physicallogical
  --define no numa --tune base --output_format all --define drop_caches --nopower --runmode rate --tune base
  --size refere intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/Benchmark/speccpu.ic2024.0.2

---

6. /proc/cpuinfo

- model name : Intel(R) Xeon(R) E E-2468
- vendor_id : GenuineIntel
- cpu family : 6
- stepping : 1
- microcode : 0x121
- bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
- cpu cores : 8
- siblings : 16
- 1 physical ids (chips)
- 16 processors (hardware threads)
- physical id 0: core ids 0-7
- physical id 0: apicids 0-15

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

---

7. lscpu

From lscpu from util-linux 2.37.4:
- Architecture: x86_64
- CPU op-mode(s): 32-bit, 64-bit
- Address sizes: 46 bits physical, 48 bits virtual
- Byte Order: Little Endian
- CPU(s): 16
- On-Line CPU(s) list: 0-15

(Continued on next page)
Fujitsu
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CPU2017 License: 19
Test Sponsor: Fujitsu
Tested by: Fujitsu

Platform Notes (Continued)

Vendor ID: GenuineIntel
Model name: Intel(R) Xeon(R) E E-2468
CPU family: 6
Model: 183
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 1
Stepping: 1
CPU max MHz: 6700.0000
CPU min MHz: 800.0000
BogoMIPS: 5222.40

Flags:
  fpu vme de pse tsc msr pae mca cmov pat pse36
  clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtsscp
  lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
  nonstop_tsc cpuid aperf perfctr tsc_known_freq pni pclmulqdq dtes64 monitor
  ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1 sse4_2
  x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
  abm 3nowprefetch cpuid_fault ebp invpcid_single ssbd ibrs ibpb
  ibrs enhanced tpr_shadow vmmi flexpriority ept vpid ept_ad
  tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
  clwb intel_pt sha_ni xsaveopt xsaves split_lock_detect
  avx_vnni dtherm ida arat pln pts hwp hwp_notify hwp_act_window hwp_epp
  hwp_pkg_req hfi umip puo ospke waitpkg gfnl vaes vpclmulqdq tme rdpid
  movdiri movdir64b fshr md_clear serialize pconf config arch_lbr flush_l2d
  arch_capabilities

Virtualization:
  VT-x
L1d cache: 384 KiB (8 instances)
L1i cache: 256 KiB (8 instances)
L2 cache: 16 MiB (8 instances)
L3 cache: 24 MiB (1 instance)
NUMA node(s): 0-15
NUMA node0 CPU(s): 0-15
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mnio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapsgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

<table>
<thead>
<tr>
<th>NAME</th>
<th>ONE-SIZE</th>
<th>ALL-SIZE</th>
<th>WAYS</th>
<th>TYPE</th>
<th>LEVEL</th>
<th>SETS</th>
<th>PHY-LINE</th>
<th>COHERENCY-SIZE</th>
</tr>
</thead>
<tbody>
<tr>
<td>L1d</td>
<td>48K</td>
<td>384K</td>
<td>12</td>
<td>Data</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L1i</td>
<td>32K</td>
<td>256K</td>
<td>8</td>
<td>Instruction</td>
<td>1</td>
<td>64</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L2</td>
<td>2M</td>
<td>16M</td>
<td>16</td>
<td>Unified</td>
<td>2</td>
<td>2048</td>
<td>1</td>
<td>64</td>
</tr>
<tr>
<td>L3</td>
<td>24M</td>
<td>24M</td>
<td>12</td>
<td>Unified</td>
<td>3</td>
<td>32768</td>
<td>1</td>
<td>64</td>
</tr>
</tbody>
</table>

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 1 nodes (0)
  node 0 cpus: 0-15
  node 0 size: 63745 MB
  node 0 free: 63251 MB
  node distances: (Continued on next page)
Platform Notes (Continued)

9. /proc/meminfo
   MemTotal:  65275744 kB

10. who -r
    run-level 3 May 15 13:03

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
    Default Target Status
    multi-user degraded

12. Failed units, from systemctl list-units --state=failed
    UNIT LOAD ACTIVE SUB DESCRIPTION
    * sep5.service loaded failed failed systemd script to load sep5 driver at boot time

13. Services, from systemctl list-unit-files
    STATE UNIT FILES
    enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd chrony cron display-manager getty@
    enabled-runtime systemd-remount-fs
    disabled autofs autocrypt-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
    indirect pcscd wickedd

14. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
    root=UUID=7d6282e3-8e21-4b62-ab94-5941e54159d1
    splash=silent
    mitigations=auto quiet
    security=apparmor
    crashkernel=235M,high
    crashkernel=72M,low

15. cpupower frequency-info
    analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 6.40 GHz.
    The governor "powersave" may decide which speed to use within this range.
    boost state support:
    Supported: yes
    Active: yes

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Platform Notes (Continued)

16. tuned-adm active
   Current active profile: balanced

17. sysctl
   kernel.numa_balancing  0
   kernel.randomize_va_space  2
   vm.compaction_proactiveness  20
   vm.dirty_background_bytes  0
   vm.dirty_background_ratio  10
   vm.dirty_bytes  0
   vm.dirty_expire_centisecs  3000
   vm.dirty_ratio  20
   vm.dirty_writeback_centisecs  500
   vm.dirtytime_expire_seconds  43200
   vm.extfrag_threshold  500
   vm.min_unmapped_ratio  1
   vm.nr_hugepages  0
   vm.nr_hugepages_mempolicy  0
   vm.nr_overcommit_hugepages  0
   vm.swappiness  60
   vm.watermark_boost_factor  15000
   vm.watermark_scale_factor  10
   vm.zone_reclaim_mode  0

18. /sys/kernel/mm/transparent_hugepage
   defrag always defer defer+madvise [madvise] never
   enabled [always] madvise never
   hpage_pmd_size  2097152
   shmem_enabled always within_size advise [never] deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged
   alloc_sleep_millisecs  60000
   defrag  1
   max_ptes_none  511
   max_ptes_shared  256
   max_ptes_swap  64
   pages_to_scan  4096
   scan_alloc_millisecs  10000

20. OS release
   From /etc/*-release /etc/*-version
   os-release SUSE Linux Enterprise Server 15 SP5

21. Disk information
   SPEC is set to: /home/Benchmark/speccpu.ic2024.0.2
   Filesystem Type Size Used Avail Use% Mounted on
   /dev/sda2 xfs 223G 98G 126G 44% /

22. /sys/devices/virtual/dmi/id
   Vendor: FUJITSU
   Product: PRIMERGY TX1330 M6
   Product Family: SERVER
   Serial: xxxxxxxxxx

(Continued on next page)
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Platform Notes (Continued)

23. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
2x Samsung M324R4GA3BB0-CQKOD 32 GB 2 rank 4800, configured at 4400

24. BIOS
(BThis section combines info from /sys/devices and dmidecode.)
BIOS Vendor: FUJITSU // American Megatrends International, LLC.
BIOS Version: V5.0.0.27 R1.0.0 for D4132-A1x
BIOS Date: 03/13/2024
BIOS Revision: 1.0

Compiler Version Notes

C       | 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base) 525.x264_r(base) 557.xz_r(base)
-----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
-----------------------------------------------------------------------------------------------

C++     | 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base) 541.leela_r(base)
-----------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.0.2 Build 20231213
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Fortran | 548.exchange2_r(base)
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Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx
## SPEC CPU®2017 Integer Rate Result

**Fujitsu**

PRIMERGY TX1330 M6, Intel Xeon E-2468, 2.6 GHz

<table>
<thead>
<tr>
<th>CPU2017 License: 19</th>
<th>Test Date: Apr-2024</th>
</tr>
</thead>
<tbody>
<tr>
<td>Test Sponsor: Fujitsu</td>
<td>Hardware Availability: Apr-2024</td>
</tr>
<tr>
<td>Tested by: Fujitsu</td>
<td>Software Availability: Dec-2023</td>
</tr>
</tbody>
</table>

### SPECrate®2017_int_base = 82.4

### SPECrate®2017_int_peak = Not Run

---

**Base Portability Flags**

<table>
<thead>
<tr>
<th>Flag</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>500.perlbench_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX_X64</td>
</tr>
<tr>
<td>502.gcc_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>505.mcf_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>520.omnettp_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>523.xalancbmk_r</td>
<td>-DSPEC_LP64 -DSPEC_LINUX</td>
</tr>
<tr>
<td>525.x264_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>531.deepsjeng_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>541.leela_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>548.exchange2_r</td>
<td>-DSPEC_LP64</td>
</tr>
<tr>
<td>557.xz_r</td>
<td>-DSPEC_LP64</td>
</tr>
</tbody>
</table>

**Base Optimization Flags**

#### C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-_TMPDIR=/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

#### C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-_TMPDIR=/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

#### Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-_TMPDIR=/opt/intel/oneapi/compiler/2024.0/lib -lqkmalloc

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The flags files that were used to format this result can be browsed at


You can also download the XML flags sources by saving the following links:


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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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