



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

xFusion

FusionServer 1158H V7
(AMD EPYC 9454)

SPECrate®2017_int_base = 534

SPECrate®2017_int_peak = 554

CPU2017 License: 6488

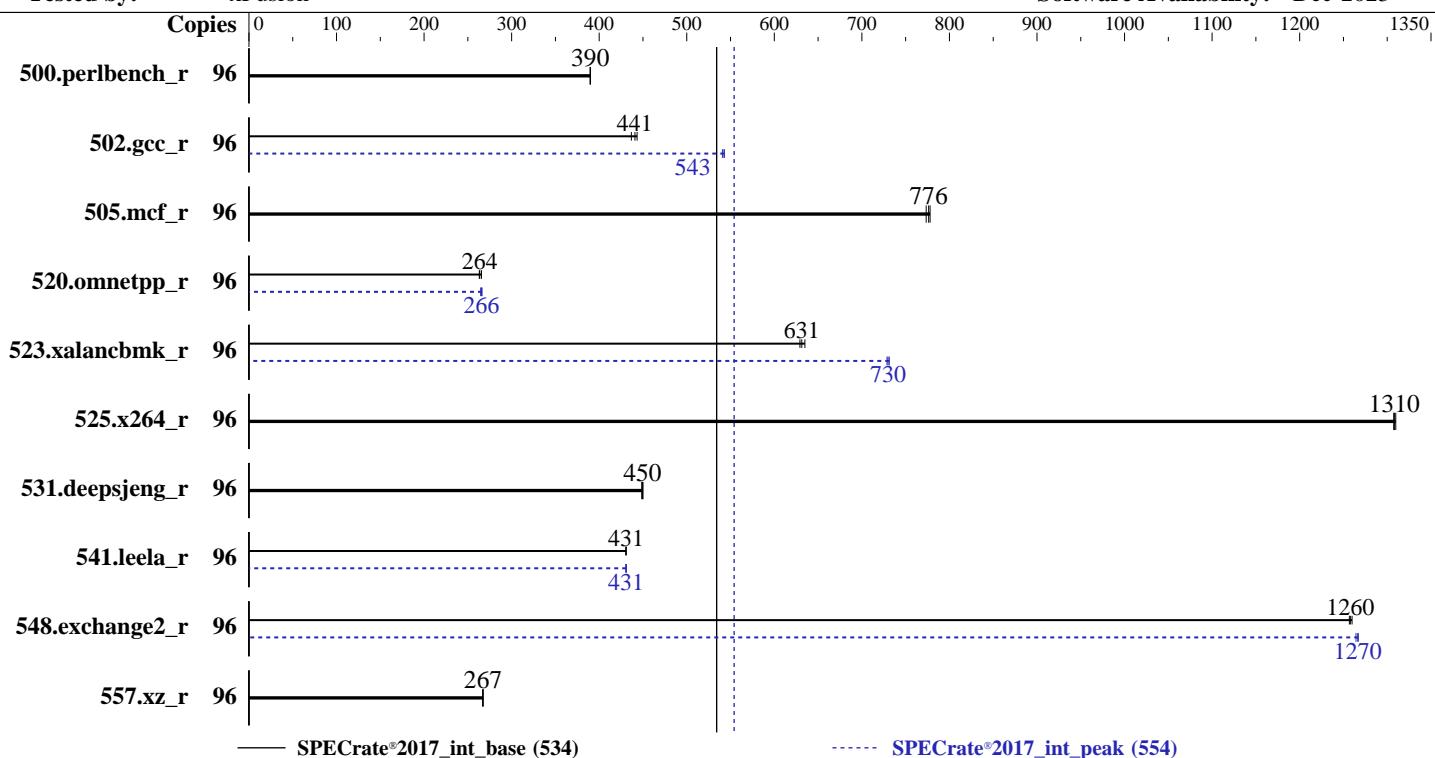
Test Date: Jul-2024

Test Sponsor: xFusion

Hardware Availability: May-2024

Tested by: xFusion

Software Availability: Dec-2023



Hardware

CPU Name: AMD EPYC 9454
Max MHz: 3800
Nominal: 2750
Enabled: 48 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 6 cores
Other: None
Memory: 384 GB (12 x 32 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux release 9.0 (Plow)
5.14.0-70.13.1.el9_0.x86_64
Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
Parallel: No
Firmware: Version 2.09.11 released Jun-2024
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	96	392	390	392	390	392	390	96	392	390	392	390	392	390		
502.gcc_r	96	307	443	311	437	308	441	96	251	543	250	543	251	541		
505.mcf_r	96	200	776	199	778	201	773	96	200	776	199	778	201	773		
520.omnetpp_r	96	479	263	478	264	474	266	96	476	265	474	266	473	266		
523.xalancbmk_r	96	161	631	161	629	160	635	96	139	730	139	731	139	729		
525.x264_r	96	128	1310	129	1310	128	1310	96	128	1310	129	1310	128	1310		
531.deepsjeng_r	96	245	450	244	450	245	449	96	245	450	244	450	245	449		
541.leela_r	96	369	431	369	431	369	431	96	369	431	369	431	369	430		
548.exchange2_r	96	200	1260	200	1260	200	1260	96	199	1260	199	1270	199	1270		
557.xz_r	96	388	267	388	267	388	267	96	388	267	388	267	388	267		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH = "/cpu2017/amd_rate_aocc400_znver4_A_lib/lib:/cpu2017/amd_rate_aocc400_znver4_A_lib/lib32:
MALLOC_CONF = "retain:true"

Environment variables set by runcpu during the 523.xalancbmk_r peak run:

MALLOC_CONF = "thp:never"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

Determinism Control = Manual
Determinism Enable = Power
TDP Control = Manual
TDP = 400
PPT Control = Manual
PPT = 400
NUMA Nodes Per Socket = NPS4

Sysinfo program /cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Tue Jul 9 20:01:16 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged

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Platform Notes (Continued)

```
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
```

```
-----  
1. uname -a  
Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64  
x86_64 x86_64 GNU/Linux
```

```
-----  
2. w  
20:01:16 up 1:24, 1 user, load average: 0.08, 0.04, 0.00  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT  
root pts/0 19:55 12.00s 0.94s 0.00s -bash
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 3094757  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 3094757  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize 30  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@pts/0  
-bash  
python3 ./run_amd_rate_aocc400_znver4_A1.py  
/bin/bash ./amd_rate_aocc400_znver4_A1.sh  
runcpu --config amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 intrate  
runcpu --configfile amd_rate_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower  
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.031/templogs/preenv.intrate.031.0.log --lognum 031.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /cpu2017
```

```
-----  
6. /proc/cpuinfo
```

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Platform Notes (Continued)

```
model name      : AMD EPYC 9454 48-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
stepping        : 1
microcode       : 0xa101148
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 48
siblings         : 96
1 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 96
On-line CPU(s) list:    0-95
Vendor ID:               AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9454 48-Core Processor
BIOS Model name:        AMD EPYC 9454 48-Core Processor
CPU family:              25
Model:                  17
Thread(s) per core:     2
Core(s) per socket:     48
Socket(s):              1
Stepping:                1
Frequency boost:        enabled
CPU max MHz:            3810.7910
CPU min MHz:             1500.0000
BogoMIPS:                5491.72
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                        constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmpfperf rapl
                        pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 x2apic movbe
                        popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy
                        abm sse4a misalignsse 3dnowprefetch osvw ibr skinit wdt tce topoext
                        perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13
                        invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1
                        avx2 smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbmb_total cqmq_mbmb_local
                        avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin arat npt lbrv
                        svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists
                        pausefilter pfthreshold avic v_vmsave_vmload vgif v_spec_ctrl avx512vbmi
                        umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnmi avx512_bitalg
                        avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
                        AMD-V
Virtualization:          1.5 MiB (48 instances)
L1d cache:                1.5 MiB (48 instances)
L1i cache:
```

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Platform Notes (Continued)

```
L2 cache: 48 MiB (48 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-11,48-59
NUMA node1 CPU(s): 12-23,60-71
NUMA node2 CPU(s): 24-35,72-83
NUMA node3 CPU(s): 36-47,84-95
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
filling
Vulnerability Srbds: Not affected
Vulnerability Tsx sync abort: Not affected
```

```
From lscpu --cache:
  NAME ONE-SIZE ALL-SIZE WAYS TYPE      LEVEL    SETS PHY-LINE COHERENCY-SIZE
  L1d     32K     1.5M   8 Data        1       64      1          64
  L1i     32K     1.5M   8 Instruction  1       64      1          64
  L2      1M      48M   8 Unified      2      2048     1          64
  L3     32M     256M  16 Unified     3     32768     1          64
```

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0-11,48-59
node 0 size: 95931 MB
node 0 free: 95201 MB
node 1 cpus: 12-23,60-71
node 1 size: 96762 MB
node 1 free: 96257 MB
node 2 cpus: 24-35,72-83
node 2 size: 96726 MB
node 2 free: 96261 MB
node 3 cpus: 36-47,84-95
node 3 size: 96714 MB
node 3 free: 96286 MB
node distances:
node  0  1  2  3
  0: 10 12 12 12
  1: 12 10 12 12
  2: 12 12 10 12
  3: 12 12 12 10
```

9. /proc/meminfo

```
MemTotal: 395402688 kB
```

10. who -r

```
run-level 3 Jul 9 18:36
```

11. Systemd service manager version: systemd 250 (250-6.el9_0)

```
Default Target  Status
multi-user      running
```

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Platform Notes (Continued)

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited crond dbus-broker firewalld getty@ irqbalance kdump mdmonitor microcode nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd systemd-network-generator tuned udisks2
enabled-runtime	systemd-remount-fs
disabled	console-getty cpupower debug-shell hwloc-dump-hwdata kvm_stat man-db-restart-cache-update nftables rdisc rhsm rhsm-facts rpmbuild serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect	sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=(hd0,msdos2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=UUID=10976966-ea39-4bac-9efc-bf882faaa099
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=UUID=a11e21b6-41a7-47ed-890c-9fb71934ef79
nohz_full=1-95

14. cpupower frequency-info

analyzing CPU 0:
current policy: frequency should be within 1.50 GHz and 2.75 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 2750MHz

15. tuned-adm active

Current active profile: throughput-performance
--

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_prolactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

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Platform Notes (Continued)

17. /sys/kernel/mm/transparent_hugepage
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hugepage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

19. OS release
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)

20. Disk information
SPEC is set to: /cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 150G 7.7G 143G 6% /

21. /sys/devices/virtual/dmi/id
Product: 1158H V7
Product Family: Genoa

22. dmidecode
Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
Memory:
12x Samsung M321R4GA3BB6-CQKDG 32 GB 2 rank 4800

23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor: INSYDE Corp.
BIOS Version: 2.09.11
BIOS Date: 06/14/2024
BIOS Revision: 2.9

Compiler Version Notes

=====

C | 502.gcc_r(peak)

=====

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Compiler Version Notes (Continued)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C | 502.gcc_r(peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++ | 523.xalancbmk_r(peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base,
peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++ | 523.xalancbmk_r(peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
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InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base) 531.deepsjeng_r(base, peak) 541.leela_r(base, peak)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====
Fortran | 548.exchange2_r(base, peak)
=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64



SPEC CPU®2017 Integer Rate Result

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xFusion

FusionServer 1158H V7
(AMD EPYC 9454)

CPU2017 License: 6488

Test Sponsor: xFusion

Tested by: xFusion

SPECrate®2017_int_base = 534

SPECrate®2017_int_peak = 554

Test Date: Jul-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

Base Optimization Flags

C benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-z muldefs -O3 -march=znver4 -fveclib=AMDLIBM -ffast-math
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdaloc
```

C++ benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -z muldefs -O3
-march=znver4 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden -lamdlibm -lflang
-lamdaloc-ext
```

Fortran benchmarks:

```
-m64 -fno -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdaloc
```

Base Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```



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SPECrate®2017_int_peak = 554

Test Date: Jul-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

502.gcc_r: -m32 -f1to -z muldefs -Ofast -march=znver4
-fveclib=AMDLIB -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: basepeak = yes

525.x264_r: basepeak = yes

557.xz_r: basepeak = yes

(Continued on next page)



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Tested by: xFusion

Test Date: Jul-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

Peak Optimization Flags (Continued)

C++ benchmarks:

```
520.omnetpp_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lamdalloc-ext
```

```
523.xalancbmk_r: -m32 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-fno-loop-reroll -Ofast -march=znver4 -fveclib=AMDLIBM
-ffast-math -finline-aggressive
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-lamdalloc-ext
```

```
531.deepsjeng_r: basepeak = yes
```

```
541.leela_r: -m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math
-finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt
-fvirtual-function-elimination -fvisibility=hidden
-lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -flto -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fepilog-vectorization-of-inductions
-mllvm -optimize-strided-mem-cost -floop-transform
-mllvm -unroll-aggressive -mllvm -unroll-threshold=500 -lamdlibm
-lflang -lamdalloc
```



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Test Date: Jul-2024

Hardware Availability: May-2024

Software Availability: Dec-2023

Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32

C++ benchmarks (except as noted below):

-Wno-unused-command-line-argument

523.xalancbmk_r: -L/usr/lib32 -Wno-unused-command-line-argument
-L/home/work/cpu2017/v119/aocc4/znver4/rate/amd_rate_aocc400_znver4_A_lib/lib32

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/xFusion-Platform-Settings-AMD-V1.1.xml>

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