



SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

New H3C Technologies Co., Ltd.

H3C UniServer R4900 G6 Ultra (Intel Xeon Silver 4510T)

SPECrate®2017_fp_base = 337

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9066

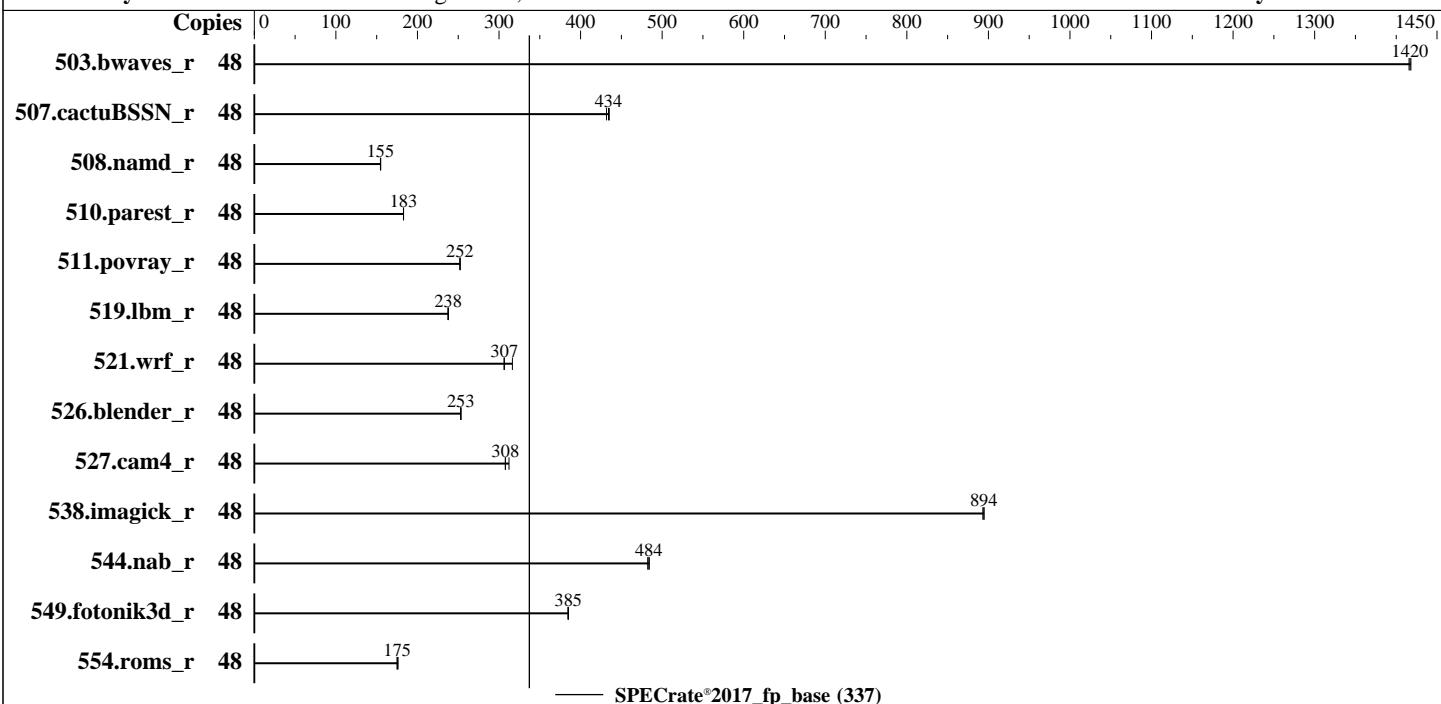
Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Oct-2023

Software Availability: Mar-2024



Hardware

CPU Name: Intel Xeon Silver 4510T
 Max MHz: 3700
 Nominal: 2000
 Enabled: 24 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 30 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4400)
 Storage: 1 x 1.92 TB NVME SSD
 Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.2 (Plow)
 Compiler: 5.14.0-284.11.1.el9_2.x86_64
 C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: Version 6.10.43 released Jul-2024 BIOS
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	340	1420	340	1420	339	1420							
507.cactusBSSN_r	48	141	432	140	435	140	434							
508.namd_r	48	295	155	295	155	294	155							
510.parest_r	48	686	183	687	183	687	183							
511.povray_r	48	444	252	445	252	445	252							
519.lbm_r	48	213	238	213	238	213	238							
521.wrf_r	48	351	306	351	307	340	316							
526.blender_r	48	289	253	289	253	289	253							
527.cam4_r	48	269	312	273	308	273	308							
538.imagick_r	48	133	894	133	895	134	893							
544.nab_r	48	168	482	167	484	167	484							
549.fotonik3d_r	48	486	384	486	385	486	385							
554.roms_r	48	436	175	433	176	435	175							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/speccpu/lib/intel64:/home/speccpu/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

(Continued on next page)



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General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Settings:

SNC = Enable SNC2 (2-clusters)

Power Performance Tuning = BIOS Controls EPPB

ENERGY_PERF_BIAS_CFG mode = Performance

Sysinfo program /home/speccpu/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Tue Aug 20 16:18:30 2024

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a
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 16. sysctl
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 19. OS release
 20. Disk information
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 22. dmidecode
 23. BIOS
-

1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT 2023 x86_64 x86_64 x86_64 GNU/Linux

2. w
16:18:30 up 3:16, 2 users, load average: 32.68, 44.18, 46.41
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root tty1 13:14 3:04m 0.88s 0.02s sh intrate.sh

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Platform Notes (Continued)

```
root      pts/0      13:13      3:04m  0.03s  0.03s -bash
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
real-time non-blocking time  (microseconds, -R) unlimited  
core file size              (blocks, -c) 0  
data seg size                (kbytes, -d) unlimited  
scheduling priority          (-e) 0  
file size                    (blocks, -f) unlimited  
pending signals               (-i) 4124547  
max locked memory            (kbytes, -l) 64  
max memory size              (kbytes, -m) unlimited  
open files                   (-n) 1024  
pipe size                    (512 bytes, -p) 8  
POSIX message queues         (bytes, -q) 819200  
real-time priority           (-r) 0  
stack size                   (kbytes, -s) unlimited  
cpu time                     (seconds, -t) unlimited  
max user processes            (-u) 4124547  
virtual memory                (kbytes, -v) unlimited  
file locks                   (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31  
login -- root  
-bash  
sh intrate.sh  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 -c  
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst  
--define invoke_with_interleave --define drop_caches --tune base -o all fprate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=48 --configfile  
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=24 --define physicalfirst  
--define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode  
rate --tune base --size refrate fprate --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/speccpu
```

```
-----  
6. /proc/cpuinfo  
model name      : INTEL(R) XEON(R) SILVER 4510T  
vendor_id       : GenuineIntel  
cpu family     : 6  
model          : 143  
stepping        : 8  
microcode       : 0x2b000571  
bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrss  
cpu cores       : 12  
siblings        : 24  
2 physical ids (chips)  
48 processors (hardware threads)  
physical id 0: core ids 0-11  
physical id 1: core ids 0-11  
physical id 0: apicids 0-23  
physical id 1: apicids 64-87
```

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Platform Notes (Continued)

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 46 bits physical, 57 bits virtual
Byte Order: LittleEndian
CPU(s): 48
On-line CPU(s) list: 0-47
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: INTEL(R) XEON(R) SILVER 4510T
BIOS Model name: INTEL(R) XEON(R) SILVER 4510T
CPU family: 6
Model: 143
Thread(s) per core: 2
Core(s) per socket: 12
Socket(s): 2
Stepping: 8
CPU max MHz: 3700.0000
CPU min MHz: 800.0000
BogoMIPS: 4000.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
       nonstop_tsc cpuid aperf fmpf perf tsc_known_freq pni pclmulqdq dtes64 monitor
       ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
       sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
       invpcid_single intel_ppin cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced
       tpr_shadow vnumi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2
       smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap
       avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
       xsaveopt xsavec xgetbv1 xsavec cqmq_llc cqmq_occup_llc cqmq_mbm_total
       cqmq_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
       arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req hfi avx512vbmi umip
       pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
       avx512_bitAlg tme avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote
       movdiri movdir64b enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr
       ibt amx_bf16 avx512_fp16 amx_tile amx_int8 flush_lld arch_capabilities
       VT-x
Virtualization: VT-x
L1d cache: 1.1 MiB (24 instances)
L1i cache: 768 KiB (24 instances)
L2 cache: 48 MiB (24 instances)
L3 cache: 60 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-5,24-29
NUMA node1 CPU(s): 6-11,30-35
NUMA node2 CPU(s): 12-17,36-41
NUMA node3 CPU(s): 18-23,42-47
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
```

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Platform Notes (Continued)

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	2M	48M	16	Unified	2	2048	1	64
L3	30M	60M	15	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)
node 0 cpus: 0-5,24-29
node 0 size: 257109 MB
node 0 free: 251731 MB
node 1 cpus: 6-11,30-35
node 1 size: 258045 MB
node 1 free: 254861 MB
node 2 cpus: 12-17,36-41
node 2 size: 258005 MB
node 2 free: 254930 MB
node 3 cpus: 18-23,42-47
node 3 size: 258040 MB
node 3 free: 254902 MB
node distances:
node 0 1 2 3
0: 10 12 21 21
1: 12 10 21 21
2: 21 21 10 12
3: 21 21 12 10

9. /proc/meminfo

MemTotal: 1055949248 kB

10. who -r
run-level 3 Aug 20 13:02

11. Systemd service manager version: systemd 252 (252-13.el9_2)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online
accounts-daemon atd auditd avahi-daemon bluetooth chronyd crond cups dbus-broker firewalld
gdm getty@ insights-client-boot irqbalance iscsi iscsi-onboot kdump libstoragemgmt
lm_sensors low-memory-monitor lvm2-monitor mcelog mdmonitor microcode multipathd
nis-domainname nvmefc-boot-connections ostree-remount pmcd pmie pmlogger
power-profiles-daemon qemu-guest-agent rhsmcertd rpcbind rsyslog rtkit-daemon
selinux-autorelabel-mark smartd sshd sssd switcheroo-control sysstat systemd-boot-update

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Platform Notes (Continued)

```

enabled-runtime      systemd-network-generator tuned udisks2 upower vgaauthd virtqemud vmtoolsd
disabled          systemd-remount-fs
                   arp-ethers autofs blk-availability brltty canberra-system-bootup canberra-system-shutdown
                   canberra-system-shutdown-reboot chrony-wait cni-dhcp console-getty cpupower cups-browsed
                   dbus-daemon debug-shell dnf-system-upgrade dnsmasq dovecot fancontrol fcoe grafana-server
                   gssproxy httpd httpd@ ibacm iprdump iprinit iprupdate ipsec iscsid iscsiuiuo kpatch
                   kvm_stat ledmon libvirt-guests libvирtd lldpad man-db-restart-cache-update named
                   named-chroot nfs-blkmap nfs-server nftables nmb numad nvvmf-autoconnect
                   ostree-readonly-sysroot-migration pesign pmfind pmie_farm pmlogger_farm pmproxy podman
                   podman-auto-update podman-clean-transient podman-kube@ podman-restart postfix powertop
                   psacct ras-mc-ctl rasdaemon rdisc rhcd rhsm rhsm-facts rpmdb-rebuild rrdcached saslauthd
                   selinux-check-proper-disable serial-getty@ smb snmpd snmptrapd spamassassin
                   speech-dispatcherd srp_daemon srp_daemon_port@ sshd-keygen@ systemd-boot-check-no-failures
                   systemd-nspawn@ systemd-pstore systemd-sysext target targetclid tog-pegasus trace-cmd
                   virtinterfaced virtnetworkd virtnodeved virtnwfilterd virtproxyd virtsecretd virtstoraged
                   vsftpd wpa_supplicant
indirect           pcsd spice-vdagentd sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
                   systemd-sysupdate systemd-sysupdate-reboot virtlockd virtlogd vsftpd@
```

13. Linux kernel boot-time arguments, from /proc/cmdline

```

BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=/dev/mapper/rhel-root
ro
crashkernel=1G-4G:192M,4G-64G:256M,64G-:512M
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet
console=tty0
```

14. cpupower frequency-info

```

analyzing CPU 0:
    current policy: frequency should be within 800 MHz and 3.70 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.

    boost state support:
        Supported: yes
        Active: yes
```

15. tuned-adm active

```
Current active profile: throughput-performance
```

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	40
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1

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Platform Notes (Continued)

```
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness            10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode     0

-----
17. /sys/kernel/mm/transparent_hugepage
    defrag      always defer defer+madvise [madvise] never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force

-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan           4096
    scan_sleep_millisecs   10000

-----
19. OS release
    From /etc/*-release /etc/*-version
    os-release      Red Hat Enterprise Linux 9.2 (Plow)
    redhat-release  Red Hat Enterprise Linux release 9.2 (Plow)
    system-release  Red Hat Enterprise Linux release 9.2 (Plow)

-----
20. Disk information
    SPEC is set to: /home/speccpu
    Filesystem      Type  Size  Used Avail Use% Mounted on
    /dev/mapper/rhel-home xfs   1.7T  96G  1.6T   6% /home

-----
21. /sys/devices/virtual/dmi/id
    Vendor:      New H3C Technologies Co., Ltd.
    Product:     H3C UniServer R4900 G6 Ultra
    Product Family: Rack
    Serial:      210235A4CHH244000015

-----
22. dmidecode
    Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
    "DMTF SMBIOS" standard.
    Memory:
        1x Hynix HMCG94AGBRA179N 64 GB 2 rank 5600, configured at 4400
        15x Hynix HMCG94AGBRA181N 64 GB 2 rank 5600, configured at 4400

-----
23. BIOS
    (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor: American Megatrends International, LLC.
```

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Platform Notes (Continued)

BIOS Version: 6.10.43
BIOS Date: 07/27/2024
BIOS Revision: 5.32

Compiler Version Notes

```
=====
C      | 519.lbm_r(base) 538.imagick_r(base) 544.nab_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
C++     | 508.namd_r(base) 510.parest_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
C++, C   | 511.povray_r(base) 526.blender_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
C++, C, Fortran | 507.cactuBSSN_r(base)
-----
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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-----

=====
Fortran   | 503.bwaves_r(base) 549.fotonik3d_r(base) 554.roms_r(base)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

=====
Fortran, C | 521.wrf_r(base) 527.cam4_r(base)
-----
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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```



SPEC CPU®2017 Floating Point Rate Result

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New H3C Technologies Co., Ltd.

H3C UniServer R4900 G6 Ultra (Intel Xeon Silver 4510T)

SPECrate®2017_fp_base = 337

SPECrate®2017_fp_peak = Not Run

CPU2017 License: 9066

Test Sponsor: New H3C Technologies Co., Ltd.

Tested by: New H3C Technologies Co., Ltd.

Test Date: Aug-2024

Hardware Availability: Oct-2023

Software Availability: Mar-2024

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)



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Base Optimization Flags (Continued)

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs  
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512  
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc  
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevE.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

http://www.spec.org/cpu2017/flags/New_H3C-Platform-Settings-V1.0-SPR-RevE.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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