SPEC CPU®2017 Floating Point Rate Result

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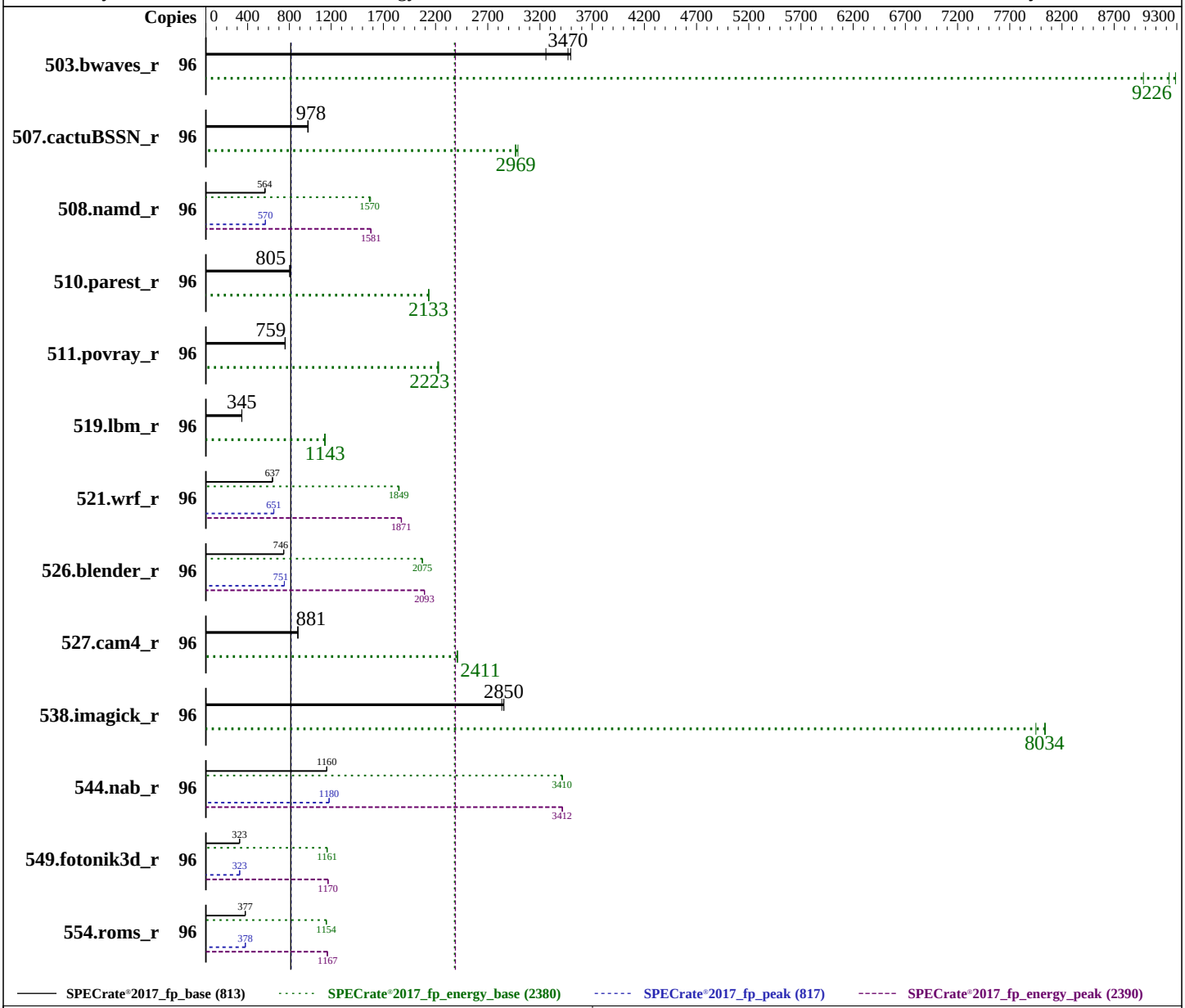
Lenovo Global Technology

ThinkSystem SR655 V3 (2.60 GHz, AMD EPYC 9655)

SPECrate®2017_fp_base =	813
SPECrate®2017_fp_energy_base =	2380
SPECrate®2017_fp_peak =	817
SPECrate®2017_fp_energy_peak =	2390

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Sep-2024
Hardware Availability: Nov-2024
Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9655
Max MHz: 4500
Nominal: 2600
Enabled: 96 cores, 1 chip
Orderable: 1 chip

(Continued on next page)

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
Kernel 5.14.0-427.13.1.el9_4.x86_64
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: Lenovo BIOS Version KAE125W 5.10 released Aug-2024

(Continued on next page)

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Hardware (Continued)

Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	384 MB I+D on chip per chip, 32 MB shared / 8 cores
Other:	None
Memory:	384 GB (12 x 32 GB 2Rx8 PC5-6400B-R, running at 5200)
Storage:	1 x 480 GB SATA SSD
Other:	CPU Cooling: Air

Software (Continued)

File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to balance power and performance

Power

Max. Power (W):	461.2
Idle Power (W):	89.63
Min. Temperature (C):	20.56
Elevation (m):	43
Line Standard:	220 V / 50 Hz / 1 phase / 3 wires
Provisioning:	Line-powered

Power Settings

Management FW:	Version 53.9 of KAX341H
Memory Mode:	Normal

Power-Relevant Hardware

Power Supply:	1 x 1100 W (non-redundant)
Details:	ThinkSystem 1100W 230V Titanium Power Supply 4P57A72666
Backplane:	8 x 2.5-inch HDD back plane
Other Storage:	None
Storage Model #s:	4XB7A17107
NICs Installed:	1 x ThinkSystem Ethernet 4-port Adaptor @ 1 Gb
NICs Enabled (FW/OS):	4 / 1
NICs Connected/Speed:	1 @ 1 Gb
Other HW Model #s:	6 x Performance fans

Power Analyzer

Power Analyzer:	WIN:9888
Hardware Vendor:	YOKOGAWA, Inc.
Model:	YokogawaWT310E
Serial Number:	C3UD17025E
Input Connection:	Default
Metrology Institute:	CNAS
Calibration By:	GRG METROLOGY & TEST (BEIJING) CO., LTD.
Calibration Label:	J202308266858A-0002
Calibration Date:	16-Oct-2023
PTDaemon® Version:	1.10.0 (82175bac; 2022-08-17)
Setup Description:	Connected to PSU1
Current Ranges Used:	5A
Voltage Range Used:	300V

Temperature Meter

Temperature Meter:	WIN:9889
Hardware Vendor:	Digi International, Inc.
Model:	DigiWATCHPORT_H
Serial Number:	W63390099
Input Connection:	USB
PTDaemon Version:	1.10.0 (82175bac; 2022-08-17)
Setup Description:	50 mm in front of SUT main intake

Base Results Table

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power

Table continues on next page. Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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ThinkSystem SR655 V3 (2.60 GHz, AMD EPYC 9655)

SPECrate®2017_fp_base = 813

SPECrate®2017_fp_energy_base = 2380

SPECrate®2017_fp_peak = 817

SPECrate®2017_fp_energy_peak = 2390

CPU2017 License: 9017
Test Sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test Date: Sep-2024
Hardware Availability: Nov-2024
Software Availability: Oct-2024

Base Results Table (Continued)

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
503.bwaves_r	96	275	3500	113	9290	410	420	**278**	**3470**	**114**	**9230**	**410**	**418**	296	3260	117	8980	395	402
507.cactuBSSN_r	96	**124**	**978**	**45.0**	**2970**	**362**	**367**	124	976	45.1	2960	362	366	124	981	44.7	2990	361	366
508.namd_r	96	**162**	**564**	**63.3**	**1570**	**392**	**406**	160	569	63.0	1580	393	406	162	563	63.5	1570	392	405
510.parest_r	96	313	803	128	2140	409	458	312	805	128	2130	410	458	**312**	**805**	**128**	**2130**	**411**	**458**
511.povray_r	96	296	758	109	2220	370	381	295	761	109	2230	370	382	**295**	**759**	**109**	**2220**	**370**	**382**
519.lbm_r	96	**294**	**345**	**101**	**1140**	**343**	**346**	294	345	101	1140	343	347	294	344	101	1140	344	348
521.wrf_r	96	**337**	**637**	**127**	**1850**	**377**	**393**	336	640	127	1850	377	396	337	637	127	1850	377	395
526.blender_r	96	**196**	**746**	**76.3**	**2070**	**389**	**410**	196	746	76.4	2070	390	411	196	745	76.3	2070	389	410
527.cam4_r	96	191	880	75.9	2410	398	412	190	882	75.9	2410	399	414	**191**	**881**	**75.8**	**2410**	**398**	**412**
538.imagick_r	96	**83.7**	**2850**	**32.2**	**8030**	**385**	**400**	84.2	2830	32.5	7950	386	400	83.7	2850	32.2	8040	384	399
544.nab_r	96	139	1160	51.3	3420	368	421	**140**	**1160**	**51.4**	**3410**	**368**	**421**	140	1160	51.3	3410	368	421
549.fotonik3d_r	96	1158	323	358	1160	309	315	1161	322	360	1160	310	365	**1158**	**323**	**359**	**1160**	**310**	**343**
554.roms_r	96	**404**	**377**	**146**	**1150**	**361**	**372**	404	378	146	1150	361	374	405	377	146	1150	360	372

SPECrate®2017_fp_base = 813

SPECrate®2017_fp_energy_base = 2380

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Peak Results Table

Benchmark	Copies	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power	Seconds	Ratio	Energy (kJ)	Energy Ratio	Average Power	Maximum Power
503.bwaves_r	96	275	3500	113	9290	410	420	**278**	**3470**	**114**	**9230**	**410**	**418**	296	3260	117	8980	395	402
507.cactuBSSN_r	96	**124**	**978**	**45.0**	**2970**	**362**	**367**	124	976	45.1	2960	362	366	124	981	44.7	2990	361	366
508.namd_r	96	160	571	62.9	1580	393	407	**160**	**570**	**62.9**	**1580**	**393**	**405**	160	569	62.9	1580	393	406
510.parest_r	96	313	803	128	2140	409	458	312	805	128	2130	410	458	**312**	**805**	**128**	**2130**	**411**	**458**
511.povray_r	96	296	758	109	2220	370	381	295	761	109	2230	370	382	**295**	**759**	**109**	**2220**	**370**	**382**
519.lbm_r	96	**294**	**345**	**101**	**1140**	**343**	**346**	294	345	101	1140	343	347	294	344	101	1140	344	348
521.wrf_r	96	**330**	**651**	**125**	**1870**	**380**	**394**	331	650	125	1870	379	396	330	651	125	1870	379	395
526.blender_r	96	195	751	75.6	2090	389	411	**195**	**751**	**75.7**	**2090**	**389**	**411**	194	752	75.6	2090	389	411
527.cam4_r	96	191	880	75.9	2410	398	412	190	882	75.9	2410	399	414	**191**	**881**	**75.8**	**2410**	**398**	**412**
538.imagick_r	96	**83.7**	**2850**	**32.2**	**8030**	**385**	**400**	84.2	2830	32.5	7950	386	400	83.7	2850	32.2	8040	384	399
544.nab_r	96	137	1180	51.3	3420	375	421	137	1180	51.3	3420	375	420	**137**	**1180**	**51.3**	**3410**	**375**	**422**
549.fotonik3d_r	96	1156	324	356	1170	308	336	**1157**	**323**	**356**	**1170**	**308**	**351**	1158	323	356	1170	308	373
554.roms_r	96	**404**	**378**	**144**	**1170**	**357**	**369**	404	377	145	1160	358	368	404	378	145	1160	358	370

SPECrate®2017_fp_peak = 817

SPECrate®2017_fp_energy_peak = 2390

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-a
     occ500_znver5_A1/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
BIOS configuration:
Choose Operating Mode set to Custom Mode
Core Performance Boost set to Disabled
NUMA Nodes per Socket set to NPS4
Memory Speed set to 5200MHz
SMT Mode set to Disabled

 Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Sun Sep 22 00:13:37 2024

 SUT (System Under Test) info as seen by some common utilities.
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT
  2024 x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   00:13:37 up 3 min,  1 user,  load average: 0.15, 0.16, 0.08
  USER     TTY        LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1      00:11   21.00s  1.49s  0.09s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  real-time non-blocking time  (microseconds, -R) unlimited
  core file size              (blocks, -c) 0
  data seg size               (kbytes, -d) unlimited
  scheduling priority                 (-e) 0
  file size                   (blocks, -f) unlimited
  pending signals                     (-i) 1545956
  max locked memory           (kbytes, -l) 2097152
  max memory size             (kbytes, -m) unlimited
  open files                          (-n) 1024
  pipe size                (512 bytes, -p) 8
  POSIX message queues         (bytes, -q) 819200
  real-time priority                  (-r) 0
```

(Continued on next page)

Platform Notes (Continued)

```
   stack size                  (kbytes, -s) unlimited
   cpu time                   (seconds, -t) unlimited
   max user processes                 (-u) 1545956
   virtual memory              (kbytes, -v) unlimited
   file locks                         (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize 31
 login -- root
 -bash
 /bin/bash ./Run026-compliant-amd-ratefp.sh
 python3 ./run_amd_rate_aocc500_znver5_A1.py
 /bin/bash ./amd_rate_aocc500_znver5_A1.sh
 runcpu --power --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
 runcpu --power --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --runmode
   rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : AMD EPYC 9655 96-Core Processor
    vendor_id       : AuthenticAMD
    cpu family      : 26
    model           : 2
    stepping        : 1
    microcode       : 0xb00210e
    bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
    TLB size        : 192 4K pages
    cpu cores       : 96
    siblings        : 96
    1 physical ids (chips)
    96 processors (hardware threads)
    physical id 0: core ids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
    physical id 0: apicids 0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.37.4:
  Architecture:                       x86_64
  CPU op-mode(s):                     32-bit, 64-bit
  Address sizes:                      52 bits physical, 57 bits virtual
  Byte Order:                         Little Endian
  CPU(s):                             96
  On-line CPU(s) list:                0-95
  Vendor ID:                          AuthenticAMD
  BIOS Vendor ID:                     Advanced Micro Devices, Inc.
  Model name:                         AMD EPYC 9655 96-Core Processor
  BIOS Model name:                    AMD EPYC 9655 96-Core Processor
  CPU family:                         26
  Model:                              2
  Thread(s) per core:                 1
```

(Continued on next page)

Platform Notes (Continued)

```
  Core(s) per socket:                  96
  Socket(s):                           1
  Stepping:                            1
  CPU(s) scaling MHz:                  58%
  CPU max MHz:                         4509.3750
  CPU min MHz:                         1500.0000
  BogoMIPS:                            5192.38
  Flags:                               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                                       clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                                       lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                                       aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                                       x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
                                       extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit
                                       wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cat_l3
                                       cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp ibrs_enhanced
                                       vmmcall fsgsbase tsc_adjust bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a
                                       avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd
                                       sha_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc
                                       cqm_occup_llc cqm_mbm_total cqm_mbm_local avx_vnni avx512_bf16 clzero
                                       irperf xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                       nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                       pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                                       avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni
                                       avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect movdiri
                                       movdir64b overflow_recov succor smca fsrm avx512_vp2intersect flush_l1d
                                       debug_swap
  Virtualization:                      AMD-V
  L1d cache:                           4.5 MiB (96 instances)
  L1i cache:                           3 MiB (96 instances)
  L2 cache:                            96 MiB (96 instances)
  L3 cache:                            384 MiB (12 instances)
  NUMA node(s):                        4
  NUMA node0 CPU(s):                   0-23
  NUMA node1 CPU(s):                   24-47
  NUMA node2 CPU(s):                   48-71
  NUMA node3 CPU(s):                   72-95
  Vulnerability Gather data sampling: Not affected
  Vulnerability Itlb multihit:         Not affected
  Vulnerability L1tf:                  Not affected
  Vulnerability Mds:                   Not affected
  Vulnerability Meltdown:              Not affected
  Vulnerability Mmio stale data:       Not affected
  Vulnerability Retbleed:              Not affected
  Vulnerability Spec rstack overflow: Not affected
  Vulnerability Spec store bypass:     Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:            Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:            Mitigation; Enhanced / Automatic IBRS, IBPB conditional, STIBP
                                       disabled, RSB filling, PBRSB-eIBRS Not affected
  Vulnerability Srbds:                 Not affected
  Vulnerability Tsx async abort:       Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K     4.5M   12 Data            1    64        1             64
      L1i       32K       3M    8 Instruction     1    64        1             64
      L2         1M      96M   16 Unified         2  1024        1             64
      L3        32M     384M   16 Unified         3 32768        1             64
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 4 nodes (0-3)
  node 0 cpus: 0-23
  node 0 size: 96344 MB
  node 0 free: 95707 MB
  node 1 cpus: 24-47
  node 1 size: 96761 MB
  node 1 free: 96298 MB
  node 2 cpus: 48-71
  node 2 size: 96761 MB
  node 2 free: 96262 MB
  node 3 cpus: 72-95
  node 3 size: 96660 MB
  node 3 free: 96194 MB
  node distances:
  node   0   1   2   3
    0:  10  12  12  12
    1:  12  10  12  12
    2:  12  12  10  12
    3:  12  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       395804796 kB

------------------------------------------------------------
10. who -r
  run-level 3 Sep 22 00:10

------------------------------------------------------------
11. Systemd service manager version: systemd 252 (252-32.el9_4)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
                   dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
                   mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark
                   sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
  enabled-runtime  systemd-remount-fs
  disabled         canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot
                   chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade
                   kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm rhsm-facts
                   rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
                   systemd-boot-check-no-failures systemd-pstore systemd-sysext
  indirect         sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                   systemd-sysupdate-reboot

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=(hd3,gpt2)/boot/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
  root=UUID=fb138839-ae87-46f8-b982-db1388d5161d
```

(Continued on next page)

Platform Notes (Continued)

```
  ro
  resume=UUID=e504a4b5-5b29-425c-bfb8-cd6371631035

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 49:
    current policy: frequency should be within 1.50 GHz and 2.60 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: no
      Active: no
      Boost States: 0
      Total States: 3
      Pstate-P0:  800MHz

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
18. OS release
```

(Continued on next page)

Platform Notes (Continued)

```
  From /etc/*-release /etc/*-version
  os-release     Red Hat Enterprise Linux 9.4 (Plow)
  redhat-release Red Hat Enterprise Linux release 9.4 (Plow)
  system-release Red Hat Enterprise Linux release 9.4 (Plow)

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/sda4      xfs   373G   81G  293G  22% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:         Lenovo
    Product:        ThinkSystem SR655V3
    Product Family: ThinkSystem
    Serial:         1234567890

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    6x SK Hynix HMCG88AHBRA477N 32 GB 2 rank 6400, configured at 5200
    6x SK Hynix HMCG88AHBRA478N 32 GB 2 rank 6400, configured at 5200


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       Lenovo
   BIOS Version:      KAE125W-5.10
   BIOS Date:         08/02/2024
   BIOS Revision:     5.10
   Firmware Revision: 53.9
```

Compiler Version Notes

```
==============================================================================================================
C              | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++            | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

(Continued on next page)

Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

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Base Optimization Flags (Continued)

C benchmarks (continued):
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

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Base Optimization Flags (Continued)

Benchmarks using both C and C++ (continued):
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl

Benchmarks using Fortran, C, and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

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Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd_r: `-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100 -mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc -ldl`

510.parest_r: `basepeak = yes`

Fortran benchmarks:

503.bwaves_r: `basepeak = yes`

549.fotonik3d_r: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto -Mrecursive -mllvm -reduce-array-computations=3 -fepilog-vectorization-of-inductions -fvector-transform -fscalar-transform -lamdlibm -lamdalloc -ldl -lflang`

554.roms_r: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto -Mrecursive -mllvm -reduce-array-computations=3 -fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc -ldl -lflang`

Benchmarks using both Fortran and C:

521.wrf_r: `-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6 -Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7 -mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining -mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions -lamdlibm -lamdalloc -ldl -lflang`

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Peak Optimization Flags (Continued)

527.cam4_r: `basepeak = yes`

Benchmarks using both C and C++:

511.povray_r: `basepeak = yes`

526.blender_r: `-m64 -std=c++14`
`-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6`
`-Wl,-mllvm -Wl,-reduce-array-computations=3`
`-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast`
`-march=znver5 -fveclib=AMDLIBM -ffast-math -flto`
`-fstruct-layout=7 -mllvm -unroll-threshold=50`
`-fremap-arrays -fstrip-mining`
`-mllvm -inline-threshold=1000`
`-mllvm -reduce-array-computations=3 -zopt`
`-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl`

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: `basepeak = yes`

Peak Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/aocc500-flags.html
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-A.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/aocc500-flags.xml
http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-A.xml

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