



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX1440 M2,
AMD EPYC 9845, 2.10 GHz

SPECspeed®2017_fp_base = 373

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 19

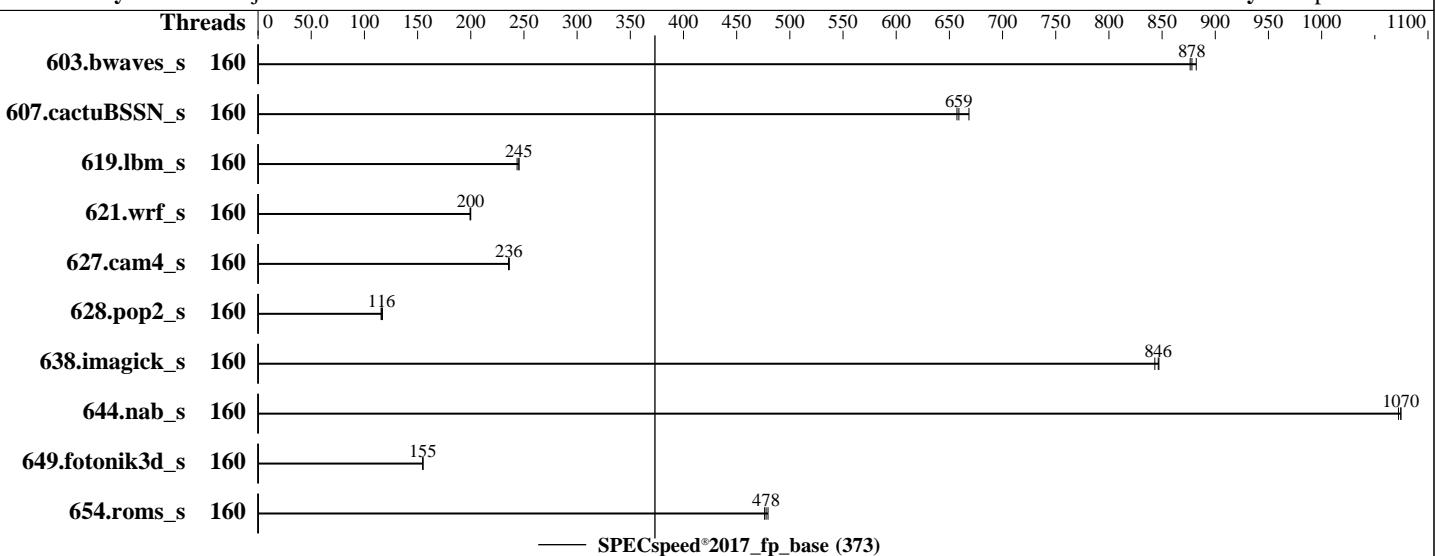
Test Date: Oct-2024

Test Sponsor: Fujitsu

Hardware Availability: Dec-2024

Tested by: Fujitsu

Software Availability: Sep-2024



Hardware

CPU Name: AMD EPYC 9845
 Max MHz: 3700
 Nominal: 2100
 Enabled: 160 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 320 MB I+D on chip per chip, 32 MB shared / 16 cores
 Other: None
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-5600B-R, running at 4800)
 Storage: 1 x SATA SSD, 960 GB
 Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6 kernel version 6.4.0-150600.21-default
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
 Parallel: Yes
 Firmware: Fujitsu BIOS Version V5.0.0.35 R2.2.0 for D4130-A1x. Released Jan-2025 tested as V5.0.0.35 R9.90.0 for D4130-A1x Sep-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: None
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	160	67.3	876	<u>67.2</u>	<u>878</u>	66.9	882									
607.cactuBSSN_s	160	25.4	657	<u>25.3</u>	<u>659</u>	24.9	668									
619.lbm_s	160	21.5	244	21.4	245	<u>21.4</u>	<u>245</u>									
621.wrf_s	160	<u>66.2</u>	<u>200</u>	66.2	200	66.4	199									
627.cam4_s	160	<u>37.6</u>	<u>236</u>	37.6	236	37.5	236									
628.pop2_s	160	103	116	<u>102</u>	<u>116</u>	102	117									
638.imagick_s	160	<u>17.0</u>	<u>846</u>	17.0	847	17.1	843									
644.nab_s	160	<u>16.3</u>	<u>1070</u>	16.3	1070	16.3	1070									
649.fotonik3d_s	160	58.8	155	58.9	155	<u>58.8</u>	<u>155</u>									
654.roms_s	160	<u>33.0</u>	<u>478</u>	32.8	479	33.1	476									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-159"
LD_LIBRARY_PATH =
    "/home/Benchmark/speccpu2017s-Turin/amd_speed_aocc500_znver5_A_lib/lib:/home/Benchmark/speccpu2017s-Tu
    rin/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "160"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:
SMT control = Disabled
L1 Stride Prefetcher = Disabled
Determinism Slider = Power
TDP Control = Manual
TDP Limit = 400
Package Power Limit Control = Manual
Package Power Limit = 400
Power Profile Selection = High Performance
FAN Control = Full

```
Sysinfo program /home/Benchmark/speccpu2017s-Turin/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Oct 14 12:14:28 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r

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Platform Notes (Continued)

```
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
-----
-----
1. uname -a
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux
-----
2. w
12:14:28 up 1:01, 2 users, load average: 120.26, 144.63, 140.55
USER   TTY      FROM             LOGIN@    IDLE     JCPU    PCPU WHAT
root   tty1          -           11:13    48:48   0.92s  0.05s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
root   pts/0        10.37.58.202  11:22    49:00   0.05s  0.05s -bash
-----
3. Username
From environment variable $USER: root
-----
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals          (-i) 1541950
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority       (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes        (-u) 1541950
virtual memory            (kbytes, -v) unlimited
file locks               (-x) unlimited
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
python3 ./run_amd_fpspeed_aocc500_znver5_A1_31.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower
--runmode speed --tune base --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0 --from_runcpu 2
```

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Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo
$SPEC = /home/Benchmark/speccpu2017s-Turin
```

```
-----  
6. /proc/cpuinfo  
    model name      : AMD EPYC 9845 160-Core Processor  
    vendor_id       : AuthenticAMD  
    cpu family     : 26  
    model          : 17  
    stepping       : 0  
    microcode      : 0xb101021  
    bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass  
    TLB size       : 192 4K pages  
    cpu cores      : 160  
    siblings       : 160  
    1 physical ids (chips)  
    160 processors (hardware threads)  
    physical id 0: core ids 0-159  
    physical id 0: apicids 0-159
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

```
From lscpu from util-linux 2.39.3:  
Architecture:                           x86_64  
CPU op-mode(s):                         32-bit, 64-bit  
Address sizes:                          52 bits physical, 57 bits virtual  
Byte Order:                            Little Endian  
CPU(s):                                 160  
On-line CPU(s) list:                   0-159  
Vendor ID:                             AuthenticAMD  
BIOS Vendor ID:                        Advanced Micro Devices, Inc.  
Model name:                            AMD EPYC 9845 160-Core Processor  
BIOS Model name:                       AMD EPYC 9845 160-Core Processor  
BIOS CPU family:                      Unknown CPU @ 2.1GHz  
CPU family:                            26  
Model:                                 17  
Thread(s) per core:                   1  
Core(s) per socket:                  160  
Socket(s):                            1  
Stepping:                             0  
Frequency boost:                     enabled  
CPU(s) scaling MHz:                 59%  
CPU max MHz:                          3718.0659  
CPU min MHz:                          1500.0000  
BogoMIPS:                            4193.86  
Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mttr pge mca cmov pat  
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb  
rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid  
extd_apicid aperfmpfperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid  
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm  
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch  
osvw ibs skinit wdt tce topoext perfctr_nb bpext  
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2  
ibrs ibpb stibp ibrs_enhanced vmmcall fsqsbbase tsc_adjust bmi1 avx2  
smep bmi2 erms invpcid cqmq rdt_a avx512f avx512dq rdseed adx smap  
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt  
xsavec xgetbv1 xsaves cqmq_llc cqmq_occu_llc cqmq_mb_m_total
```

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Platform Notes (Continued)

```
cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero iperf
xsaverptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_lld debug_swap
AMD-V
Virtualization:
L1d cache: 7.5 MiB (160 instances)
L1i cache: 5 MiB (160 instances)
L2 cache: 160 MiB (160 instances)
L3 cache: 320 MiB (10 instances)
NUMA node(s): 1
NUMA node0 CPU(s): 0-159
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	7.5M	12	Data	1	64	1	64
L1i	32K	5M	8	Instruction	1	64	1	64
L2	1M	160M	16	Unified	2	1024	1	64
L3	32M	320M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-159
node 0 size: 385513 MB
node 0 free: 383316 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 394766188 kB
```

10. who -r
run-level 3 Oct 14 11:13

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status

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Platform Notes (Continued)

multi-user running

```
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ irqbalance  
                iscsi issue-generator kbdsettings kdump kdump-early kdump-notify klog lvm2-monitor nsqd  
                postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore virtqemud wicked  
                wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
                chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info  
                firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievrd iscsi-init iscsid  
                issue-add-ssh-keys kexec-load ksm kvm kvm_stat libvirt-guests lunmask man-db-create multipathd  
                nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd serial-getty@  
                smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnserve  
                systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@  
                systemd-sysext systemd-time-wait-sync systemd-timesyncd tcsd udisks2 virtinterfaced  
                virtlockd virtlogd virtnetworkd virtnodedevd virtnwfilterd virtsecretd virtstoraged  
                vncserver@  
indirect       pcscd systemd-userdbd tftp wickedd
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default  
root=UUID=803d1916-887f-4ele-bc36-a1ab2542d352  
splash=silent  
resume=/dev/disk/by-uuid/ff08e126-00b4-4583-943a-09584dbe7c67  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=369M,high  
crashkernel=72M,low
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 14:  
    current policy: frequency should be within 1.50 GHz and 2.10 GHz.  
                  The governor "performance" may decide which speed to use  
                  within this range.  
    boost state support:  
      Supported: yes  
      Active: yes
```

```
-----  
15. sysctl  
kernel.numa_balancing          0  
kernel.randomize_va_space      0  
vm.compaction_proactiveness   20  
vm.dirty_background_bytes     0  
vm.dirty_background_ratio     10  
vm.dirty_bytes                 0  
vm.dirty_expire_centisecs    3000  
vm.dirty_ratio                 8  
vm.dirty_writeback_centisecs  500  
vm.dirtytime_expire_seconds   43200  
vm.extfrag_threshold          500  
vm.min_unmapped_ratio         1  
vm.nr_hugepages                0  
vm.nr_hugepages_mempolicy     0
```

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Platform Notes (Continued)

```
vm.nr_overcommit_hugepages      0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode           1
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage  
    defrag          [always] defer defer+madvise madvise never  
    enabled         [always] madvise never  
    hpage_pmd_size 2097152  
    shmem_enabled   always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
    alloc_sleep_millisecs 60000  
    defrag                1  
    max_ptes_none        511  
    max_ptes_shared       256  
    max_ptes_swap         64  
    pages_to_scan        4096  
    scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
From /etc/*-release /etc/*-version  
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----  
19. Disk information  
SPEC is set to: /home/Benchmark/speccpu2017s-Turin  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sda3        xfs   476G  41G  436G  9%  /home
```

```
-----  
20. /sys/devices/virtual/dmi/id  
Vendor:          FUJITSU  
Product:         PRIMERGY RX1440 M2  
Product Family: SERVER  
Serial:          xxxxxxxxxxxx
```

```
-----  
21. dmidecode  
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.  
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the  
"DMTF SMBIOS" standard.  
Memory:  
 11x Samsung M321R4GA3PB0-CWMKH 32 GB 2 rank 5600, configured at 4800  
 1x Samsung M321R4GA3PB0-CWMXH 32 GB 2 rank 5600, configured at 4800
```

```
-----  
22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor:          FUJITSU // American Megatrends Inc.  
BIOS Version:         V5.0.0.35 R9.90.0 for D4130-A1x  
BIOS Date:            09/13/2024  
BIOS Revision:        9.90  
Firmware Revision:   2.45
```



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Compiler Version Notes

```
=====
C           | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----


=====
C++, C, Fortran | 607.cactuBSSN_s(base)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----


=====
Fortran      | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----


=====
Fortran, C    | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)
-----
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
-----
```

Base Compiler Invocation

C benchmarks:
clang

Fortran benchmarks:
flang

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

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Fujitsu

PRIMERGY RX1440 M2,
AMD EPYC 9845, 2.10 GHz

SPECspeed®2017_fp_base = 373

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 19

Test Sponsor: Fujitsu

Tested by: Fujitsu

Test Date: Oct-2024

Hardware Availability: Dec-2024

Software Availability: Sep-2024

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdallic
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)



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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt  
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100  
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none  
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-Turin-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.00.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-Turin-RevA.xml>



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