



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

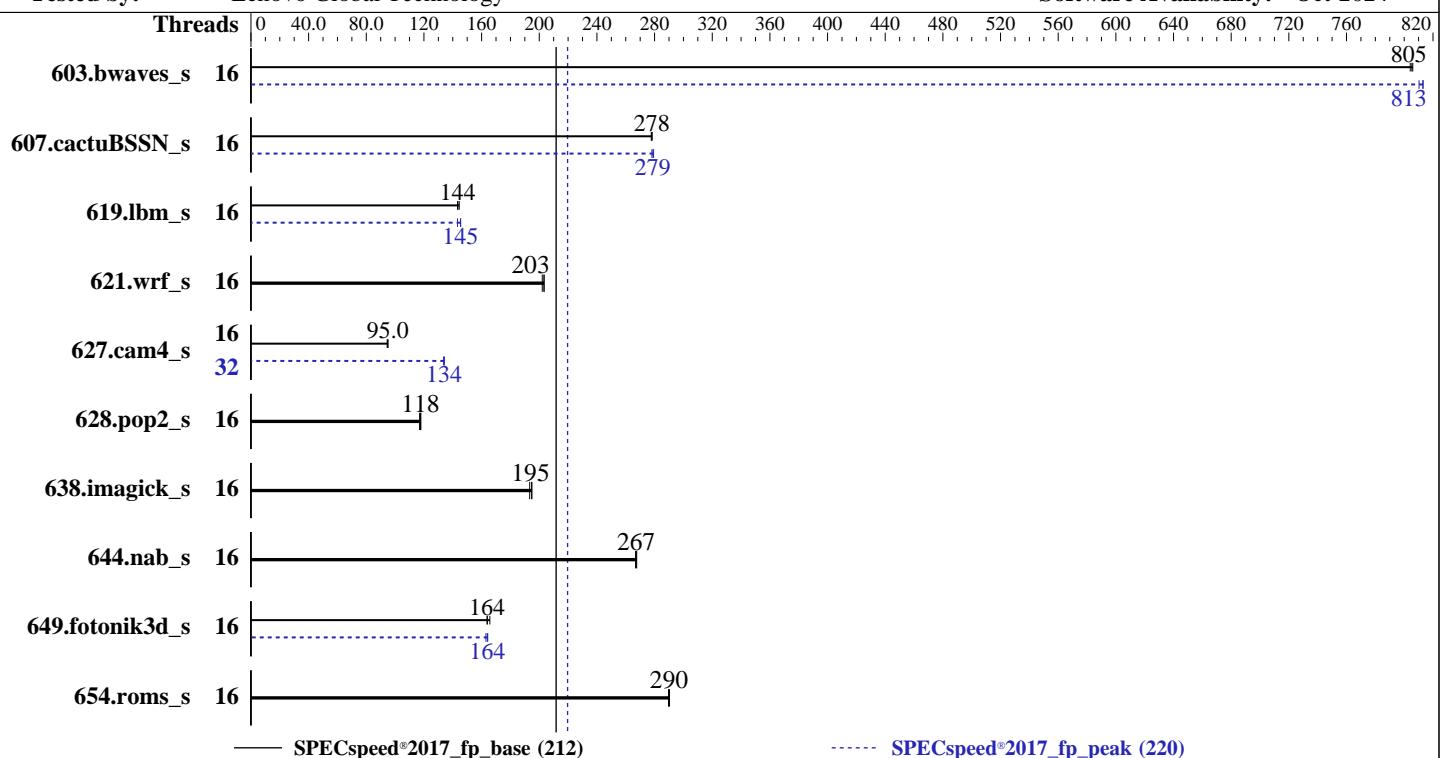
Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9135
Max MHz: 4300
Nominal: 3650
Enabled: 16 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 64 MB I+D on chip per chip,
16 MB shared / 4 cores
Other: None
Memory: 384 GB (12 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
Compiler: Kernel 5.14.0-427.13.1.el9_4.x86_64
C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: Yes
Firmware: Lenovo BIOS Version KAE127C 5.10 released Sep-2024
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None
Power Management: BIOS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECspeed®2017_fp_base = 212

SPECspeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	73.2	806	73.4	804	73.3	805	16	72.6	813	72.6	813	72.8	810
607.cactuBSSN_s	16	59.9	278	59.9	278	60.0	278	16	59.8	279	59.7	279	59.9	278
619.lbm_s	16	36.6	143	36.3	144	36.5	144	16	36.6	143	36.0	145	36.0	145
621.wrf_s	16	65.1	203	65.1	203	65.4	202	16	65.1	203	65.1	203	65.4	202
627.cam4_s	16	93.3	95.0	93.3	95.0	93.8	94.5	32	66.3	134	66.2	134	66.1	134
628.pop2_s	16	101	118	101	117	101	118	16	101	118	101	117	101	118
638.imagick_s	16	74.1	195	74.0	195	74.6	193	16	74.1	195	74.0	195	74.6	193
644.nab_s	16	65.5	267	65.4	267	65.3	268	16	65.5	267	65.4	267	65.3	268
649.fotonik3d_s	16	55.6	164	55.7	164	55.0	166	16	55.5	164	55.9	163	55.6	164
654.roms_s	16	54.3	290	54.4	290	54.2	290	16	54.3	290	54.4	290	54.2	290
SPECspeed®2017_fp_base = 212							SPECspeed®2017_fp_peak = 220							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-31"
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-amd-aocc500_znver5_A1/amd_speed_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd-
    aocc500_znver5_A1/amd_speed_aocc500_znver5_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "32"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

Environment variables set by runcpu during the 627.cam4_s peak run:

```
GOMP_CPU_AFFINITY = "0-31"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-15"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Sun Nov 10 10:25:00 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2024

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Platform Notes (Continued)

```
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
1. uname -a
Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT
2024 x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
10:25:00 up 3 min, 0 users, load average: 0.11, 0.13, 0.06
USER      TTY      LOGIN@    IDLE      JCPU      PCPU WHAT
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (i) 1545299
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 1545299
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run036-compliant-amd-speedfp.sh
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.205/templogs/preenv.fpspeed.205.0.log --lognum 205.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9135 16-Core Processor
vendor_id        : AuthenticAMD
cpu family       : 26
model            : 2
stepping          : 1
microcode        : 0xb002116
bugs              : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size         : 192 4K pages
cpu cores        : 16
siblings          : 32
1 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-3,8-11,16-19,24-27
physical id 0: apicids 0-7,16-23,32-39,48-55
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
```

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 32
On-line CPU(s) list:    0-31
Vendor ID:               AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9135 16-Core Processor
BIOS Model name:         AMD EPYC 9135 16-Core Processor
CPU family:              26
Model:                  2
Thread(s) per core:     2
Core(s) per socket:      16
Socket(s):              1
Stepping:                1
BogoMIPS:                7288.35
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
                        lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                        aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                        x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
                        extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinfit
                        wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
                        cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
                        ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2 smep bmi2 erms
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECspeed®2017_fp_base = 212

SPECspeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavc
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin
cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassist pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic
v_spec_ctrl vnmi avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
bus_lock_detect movdiri movdir64b overflow_recov succor smca fsrm
avx512_vp2intersect flush_lld debug_swap
```

Virtualization:

AMD-V

L1d cache:

768 KiB (16 instances)

L1i cache:

512 KiB (16 instances)

L2 cache:

16 MiB (16 instances)

L3 cache:

64 MiB (4 instances)

NUMA node(s):

1

NUMA node0 CPU(s):

0-31

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability Llft: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation: usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation: Enhanced / Automatic IBRS, IBPB conditional, STIBP

always-on, RSB filling, PBRSB-eIBRS Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	1M	16M	16	Unified	2	1024	1	64
L3	16M	64M	16	Unified	3	16384	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 1 nodes (0)

node 0 cpus: 0-31

node 0 size: 386363 MB

node 0 free: 385465 MB

node distances:

node 0

0: 10

9. /proc/meminfo

MemTotal: 395636668 kB

10. who -r

run-level 3 Nov 10 10:22

11. Systemd service manager version: systemd 252 (252-32.el9_4)

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
Default Target Status
multi-user      running
```

```
-----  
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled        NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond
                    dbus-broker firewalld getty@ insights-client-boot irqbalance kdump low-memory-monitor
                    mdmonitor microcode nis-domainname rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark
                    sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
    enabled-runtime systemd-remount-fs
    disabled       canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot
                    chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade
                    kvm_stat man-db-restart-cache-update nftables pesign rdisc rhcd rhsm rhsm-facts
                    rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
                    systemd-boot-check-no-failures systemd-pstore systemd-sysext
    indirect       sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                    sssd-sysupdate-reboot
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=(hd3,gpt2)/boot/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
    root=UUID=21c97f14-98a0-48c7-9f90-a3745db4e9e3
    ro
    resume=UUID=633cd70d-9ebf-45a2-9a4b-295a1359f23d
```

```
-----  
14. cpupower frequency-info
analyzing CPU 9:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 21800MHz
```

```
-----  
15. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy       0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            1
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Platform Notes (Continued)

```
16. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag          1
    max_ptes_none   511
    max_ptes_shared 256
    max_ptes_swap   64
    pages_to_scan   4096
    scan_sleep_millisecs 10000
```

```
-----  
18. OS release
    From /etc/*-release /etc/*-version
    os-release      Red Hat Enterprise Linux 9.4 (Plow)
    redhat-release  Red Hat Enterprise Linux release 9.4 (Plow)
    system-release Red Hat Enterprise Linux release 9.4 (Plow)
```

```
-----  
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1
Filesystem  Type  Size  Used  Avail Use% Mounted on
/dev/sda4    xfs  373G  42G  332G  12% /home
```

```
-----  
20. /sys/devices/virtual/dmi/id
Vendor:        Lenovo
Product:       ThinkSystem SR655 V3
Product Family: ThinkSystem
Serial:        1234567890
```

```
-----  
21. dmidecode
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  12x Samsung M321R4GA3PB1-CCPPC 32 GB 2 rank 6400, configured at 6000
```

```
-----  
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:        Lenovo
BIOS Version:       KAE127C-5.10
BIOS Date:          09/18/2024
BIOS Revision:      5.10
Firmware Revision: 53.9
```

Compiler Version Notes

=====

C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2024

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Compiler Version Notes (Continued)

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

```
=====
```

```
C++, C, Fortran | 607.cactubSSN_s(base, peak)
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

```
=====
```

```
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

```
=====
```

```
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
```

```
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
```

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdallic
-lflang

Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECspeed®2017_fp_base = 212

SPECspeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2024

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-freemap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-freemap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

Fortran benchmarks:

```
flang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECspeed®2017_fp_base = 212

SPECspeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2024

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdaloc -lflang
```

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

```
649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Nov-2024

Hardware Availability: Feb-2025

Software Availability: Oct-2024

Peak Optimization Flags (Continued)

649.fotonik3d_s (continued):

```
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -mllvm -reduce-array-computations=3
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm
-lamdaloc -lflang
```

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

```
627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdaloc
-lflang
```

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -DSPEC_OPENMP
-fremap-arrays -fstrip-mining -fstruct-layout=9
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR655 V3
(3.65 GHz, AMD EPYC 9135)

SPECSpeed®2017_fp_base = 212

SPECSpeed®2017_fp_peak = 220

CPU2017 License: 9017

Test Date: Nov-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2025

Tested by: Lenovo Global Technology

Software Availability: Oct-2024

Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-B.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-B.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

SPEC CPU and SPECSpeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-11-09 21:24:59-0500.

Report generated on 2024-12-05 10:07:41 by CPU2017 PDF formatter v6716.

Originally published on 2024-12-03.