



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C225 M8 (AMD EPYC 9254 24-Core Processor)

SPECspeed®2017_int_base = 16.1

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9019

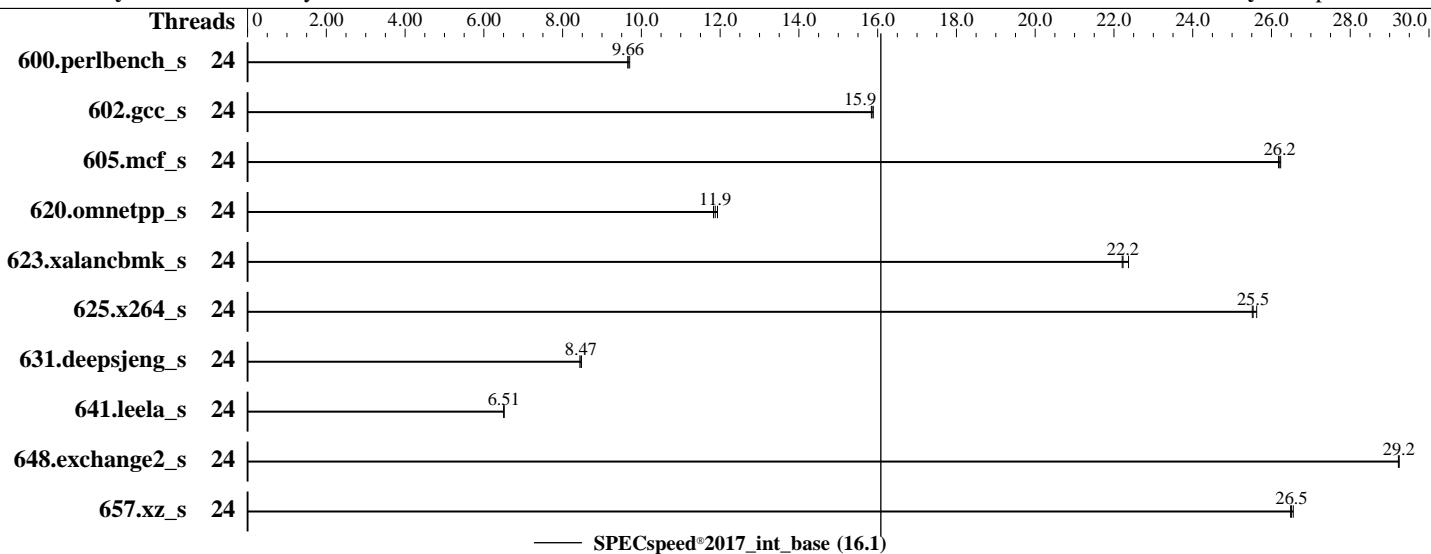
Test Date: Jan-2025

Test Sponsor: Cisco Systems

Hardware Availability: Nov-2024

Tested by: Cisco Systems

Software Availability: Sep-2024



Hardware

CPU Name: AMD EPYC 9254
Max MHz: 4150
Nominal: 2900
Enabled: 24 cores, 1 chip, 2 threads/core
Orderable: 1 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 128 MB I+D on chip per chip, 32 MB shared / 6 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)
Storage: 1 x 240 GB M.2 SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6 kernel version
Compiler: 6.4.0-150600.21-default
Parallel: C/C++/Fortran: Version 5.0.0 of AOCC
Firmware: Yes
File System: Version 4.3.5 released Aug-2024
System State: btrfs
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: Not Applicable
Power Management: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	24	184	9.66	183	9.70	184	9.66							
602.gcc_s	24	251	15.8	251	15.9	251	15.9							
605.mcf_s	24	180	26.2	180	26.2	180	26.2							
620.omnetpp_s	24	138	11.8	137	11.9	137	11.9							
623.xalancbmk_s	24	63.7	22.2	63.8	22.2	63.3	22.4							
625.x264_s	24	69.1	25.5	68.8	25.6	69.1	25.5							
631.deepsjeng_s	24	169	8.48	169	8.47	170	8.44							
641.leela_s	24	262	6.51	262	6.51	262	6.51							
648.exchange2_s	24	101	29.2	101	29.2	101	29.2							
657.xz_s	24	233	26.5	233	26.6	233	26.5							

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
 numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
 To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
 To free node-local memory and avoid remote memory usage,
 'sysctl -w vm.zone_reclaim_mode=1' run as root.
 To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
 To disable address space layout randomization (ASLR) to reduce run-to-run
 variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
 'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
 'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-47"
LD_LIBRARY_PATH =
    "/home/cpu2017/amd_speed_aocc500_znver5_A/lib:/home/cpu2017/amd_speed_aocc500_znver5_A/lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "48"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS settings:

NUMA nodes per socket set to NPS2
Determinism Slider set to Power
DF C-States set to Disabled

```
Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Fri Jan 24 13:33:58 2025
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage

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Platform Notes (Continued)

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
18. OS release  
19. Disk information  
20. /sys/devices/virtual/dmi/id  
21. dmidecode  
22. BIOS
```

```
-----  
1. uname -a  
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)  
x86_64 x86_64 x86_64 GNU/Linux
```

```
-----  
2. w  
13:33:58 up 3 min, 2 users, load average: 0.10, 0.12, 0.06  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttys1 - 13:33 21.00s 1.02s 0.08s /bin/bash ./amd_speed_aocc500_znver5_A1.sh
```

```
-----  
3. Username  
From environment variable $USER: root
```

```
-----  
4. ulimit -a  
core file size          (blocks, -c) unlimited  
data seg size           (kbytes, -d) unlimited  
scheduling priority     (-e) 0  
file size               (blocks, -f) unlimited  
pending signals          (-i) 3094862  
max locked memory       (kbytes, -l) 2097152  
max memory size         (kbytes, -m) unlimited  
open files              (-n) 1024  
pipe size               (512 bytes, -p) 8  
POSIX message queues    (bytes, -q) 819200  
real-time priority      (-r) 0  
stack size              (kbytes, -s) unlimited  
cpu time                (seconds, -t) unlimited  
max user processes       (-u) 3094862  
virtual memory           (kbytes, -v) unlimited  
file locks              (-x) unlimited
```

```
-----  
5. sysinfo process ancestry  
/usr/lib/systemd/systemd --switched-root --system --deserialize=42  
login -- root  
-bash  
python3 ./run_amd_speed_aocc500_znver5_A1.py -b intspeed  
/bin/bash ./amd_speed_aocc500_znver5_A1.sh  
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 intspeed  
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune base --reportable --iterations 3 --nopower  
--runmode speed --tune base --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile  
$SPEC/tmp/CPU2017.001/templogs/preenv.intspeed.001.0.log --lognum 001.0 --from_runcpu 2  
specperl $SPEC/bin/sysinfo  
$SPEC = /home/cpu2017
```

```
-----  
6. /proc/cpuinfo  
model name      : AMD EPYC 9254 24-Core Processor  
vendor_id       : AuthenticAMD
```

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Platform Notes (Continued)

```

cpu family      : 25
model          : 17
stepping        : 1
microcode       : 0xa101148
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass srso
TLB size        : 3584 4K pages
cpu cores       : 24
siblings         : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

```

From lscpu from util-linux 2.39.3:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 48
On-line CPU(s) list:    0-47
Vendor ID:               AuthenticAMD
BIOS Vendor ID:         Advanced Micro Devices, Inc.
Model name:              AMD EPYC 9254 24-Core Processor
BIOS Model name:         AMD EPYC 9254 24-Core Processor
BIOS CPU family:        Unknown CPU @ 2.9GHz
CPU family:              107
Model:                  25
Thread(s) per core:     17
Core(s) per socket:      24
Socket(s):              1
Stepping:                1
Frequency boost:        enabled
CPU(s) scaling MHz:     75%
CPU max MHz:             4151.7568
CPU min MHz:             1500.0000
BogoMIPS:                5790.88
Flags:                   fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpfperf rapl pnpi pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        oswi ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                        perfctr_llc mwaitx cpb cat_13 cdp_13 hw_pstate ssbd mba perfmon_v2
                        ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bmi1 avx2 smep bmi2
                        erms invpcid cqmp rdta avx512f avx512dq rdseed adx smap avx512fma
                        clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavenc
                        xgetbv1 xsaves cqmp_llc cqmp_occur_llc cqmp_mbm_total cqmp_mbm_local
                        user_shstck avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
                        amd_ppin cppc arat npt lbrv svm_lock nrrip_save tsc_scale vmcb_clean
                        flushbyasid decodeassists pausefilter pfthreshold avic
                        v_vmsave_vmlload vgif x2avic v_spec_ctrl vnmi avx512vbmi umip pkru
                        ospe avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
                        avx512_vpocntdq la57 rdpid overflow_recov succor smca fsrm flush_lld
                        debug_swap

```

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Platform Notes (Continued)

Virtualization:	AMD-V
L1d cache:	768 KiB (24 instances)
L1i cache:	768 KiB (24 instances)
L2 cache:	24 MiB (24 instances)
L3 cache:	128 MiB (4 instances)
NUMA node(s):	2
NUMA node0 CPU(s):	0-11,24-35
NUMA node1 CPU(s):	12-23,36-47
Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llft:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Mitigation; Safe RET
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	768K	8	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	8	Unified	2	2048	1	64
L3	32M	128M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 2 nodes (0-1)
node 0 cpus: 0-11,24-35
node 0 size: 386764 MB
node 0 free: 385869 MB
node 1 cpus: 12-23,36-47
node 1 size: 386978 MB
node 1 free: 386063 MB
node distances:
node    0    1
  0:   10   12
  1:   12   10
```

9. /proc/meminfo

```
MemTotal:      792312828 kB
```

10. who -r

```
run-level 3 Jan 24 13:30
```

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```
Default Target  Status
multi-user      running
```

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Platform Notes (Continued)

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance iscsi issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore virtqemud wickedd wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievfd iscsi-init iscsid issue-add-ssh-keys kexec-load ksm kvm_stat libvirt-guests lunmask man-db-create multipathd munger nfs nfs-blkmap nfs-server nfsserver rpcbind rpmconfigcheck rsyncd rtkit-daemon salt-minion serial-getty@ slurmd smartd_generate_opts snmpd snmptrapd strongswan strongswan-starter svnserve systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@ systemd-sysext systemd-time-wait-sync systemd-timesyncd tcsd udisks2 virtinterfaced virtlockd virtlogd virtnetworkd virtnodeudev virtnwfilterd virtsecretd virtstoraged yplibd
indirect	pcscd systemd-userdbd tftp wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=076ee295-de6f-47c7-95ad-f8ccc04c1b41
splash=silent
mitigations=auto
quiet
security=apparmor

14. cpupower frequency-info

analyzing CPU 38:
current policy: frequency should be within 1.50 GHz and 2.90 GHz.
The governor "performance" may decide which speed to use
within this range.
boost state support:
Supported: yes
Active: yes

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	1
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	1

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Platform Notes (Continued)

```
16. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag      1
    max_ptes_none 511
    max_ptes_shared 256
    max_ptes_swap 64
    pages_to_scan 4096
    scan_sleep_millisecs 10000
```

```
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
19. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type   Size  Used Avail Use% Mounted on
/dev/sdb2        btrfs  224G  8.1G  212G  4%  /home
```

```
20. /sys/devices/virtual/dmi/id
Vendor:          Cisco Systems Inc
Product:         UCSC-C225-M8S
Serial:          WZP28199HWP
```

```
21. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
9x 0xCE00 M321R8GA0PB0-CWMCH 64 GB 2 rank 5600, configured at 4800
3x 0xCE00 M321R8GA0PB0-CWMKJ 64 GB 2 rank 5600, configured at 4800
```

```
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Cisco Systems, Inc.
BIOS Version:     C225M8.4.3.5.166.0819241228
BIOS Date:        08/19/2024
BIOS Revision:    5.27
```

Compiler Version Notes

```
=====C | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)=====
```

```
=====AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)=====
```

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Compiler Version Notes (Continued)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====

Fortran | 648.exchange2_s(base)

=====

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64

602.gcc_s: -DSPEC_LP64

605.mcf_s: -DSPEC_LP64

620.omnetpp_s: -DSPEC_LP64

623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64

625.x264_s: -DSPEC_LP64

631.deepsjeng_s: -DSPEC_LP64

641.leela_s: -DSPEC_LP64

648.exchange2_s: -DSPEC_LP64

657.xz_s: -DSPEC_LP64



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Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-allow-multiple-definition -Wl,-mllvm -Wl,-extra-inliner -O3  
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP  
-flto -fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp -lamdlibm  
-lflang -lamdalloc
```

C++ benchmarks:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto  
-mllvm -loop-unswitch-threshold=200000  
-mllvm -reduce-array-computations=3 -mllvm -unroll-threshold=100 -zopt  
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-iv-split -Wl,-mllvm -Wl,-inline-recursion=4  
-Wl,-mllvm -Wl,-lsr-in-nested-loop -O3 -march=znver5 -fveclib=AMDLIBM  
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp  
-lomp -lamdlibm -lflang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.html>



SPEC CPU®2017 Integer Speed Result

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Cisco Systems

Cisco UCS C225 M8 (AMD EPYC 9254 24-Core Processor)

SPECspeed®2017_int_base = 16.1

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9019

Test Sponsor: Cisco Systems

Tested by: Cisco Systems

Test Date: Jan-2025

Hardware Availability: Nov-2024

Software Availability: Sep-2024

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Cisco-Platform-Settings-AMD-v3-revA.xml>

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Tested with SPEC CPU®2017 v1.1.9 on 2025-01-24 13:33:58-0500.

Report generated on 2025-02-25 19:05:15 by CPU2017 PDF formatter v6716.

Originally published on 2025-02-25.