



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

SuperServer SYS-122HA-TN-LCC
(X14DBM-APL, Intel Xeon 6960P)

SPECrate®2017_int_base = 1160

SPECrate®2017_int_peak = 1200

CPU2017 License: 001176

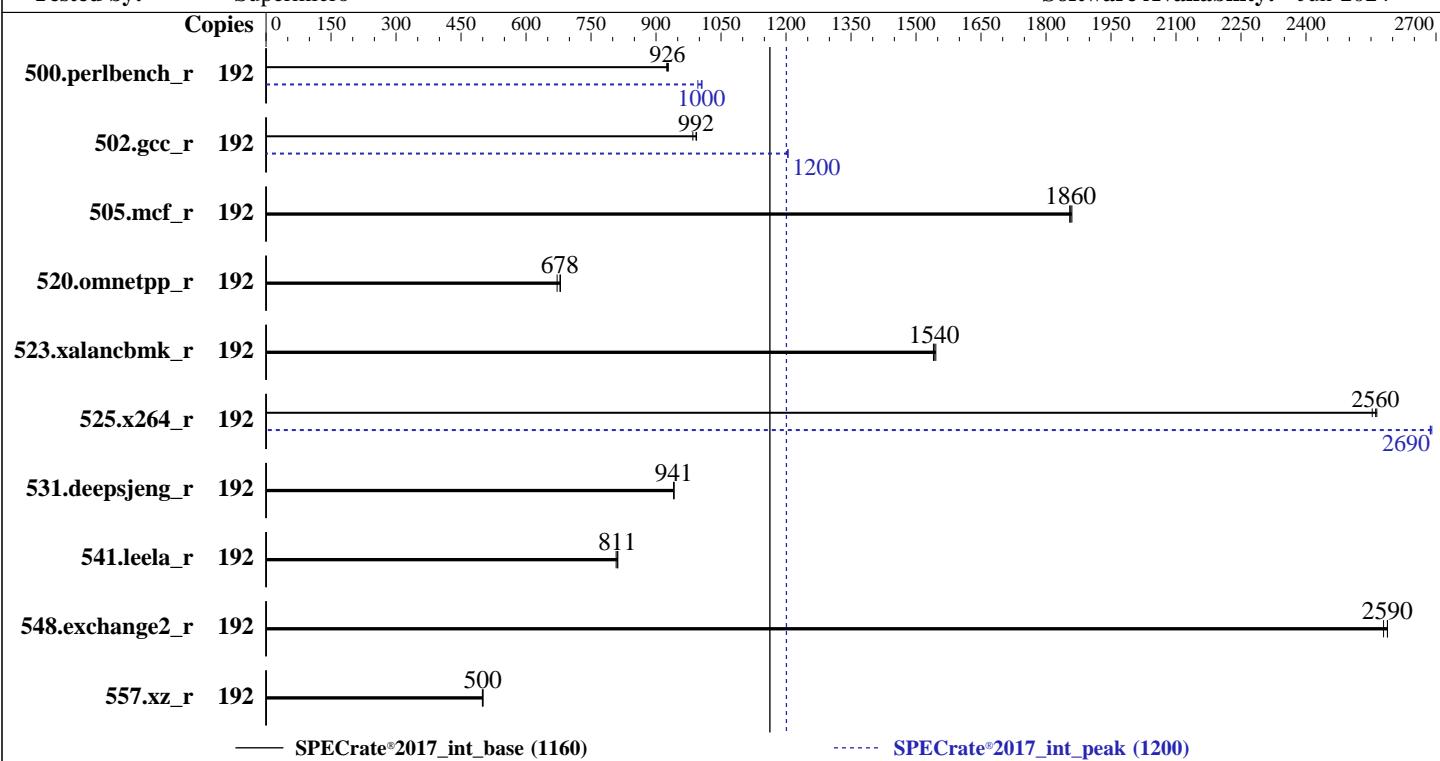
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Feb-2025

Hardware Availability: Feb-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6960P
Max MHz: 3900
Nominal: 2700
Enabled: 96 cores, 2 chips, 2 threads/core
Orderable: 1, 2 chips
Cache L1: 64 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 432 MB I+D on chip per chip
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-88/56B-M)
Storage: 1 x 1.6TB NVMe SSD
Other: CPU Cooling: DLC

Software

OS: SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default
Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel: No
Firmware: Version 1.2 released Jan-2025
File System: btrfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS set to prefer performance at the cost of additional power usage.



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	192	329	928	331	925	330	926	192	307	996	304	1000	304	1010		
502.gcc_r	192	276	985	274	992	274	994	192	226	1200	226	1200	226	1200		
505.mcf_r	192	167	1860	167	1850	167	1860	192	167	1860	167	1850	167	1860		
520.omnetpp_r	192	371	678	371	679	375	672	192	371	678	371	679	375	672		
523.xalancbmk_r	192	132	1540	131	1540	131	1550	192	132	1540	131	1540	131	1550		
525.x264_r	192	131	2560	132	2550	131	2560	192	125	2690	125	2690	125	2690		
531.deepsjeng_r	192	234	941	234	941	234	941	192	234	941	234	941	234	941		
541.leela_r	192	392	811	392	812	394	808	192	392	811	392	812	394	808		
548.exchange2_r	192	194	2590	195	2580	194	2590	192	194	2590	195	2580	194	2590		
557.xz_r	192	414	501	415	499	414	500	192	414	501	415	499	414	500		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

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General Notes (Continued)

is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Submitted_by: Mingyuan Ge <MingyuanG@supermicro.com>
Submitted: Mon Feb 10 20:38:02 EST 2025
Submission: cpu2017-20250210-46328.sub

Platform Notes

BIOS Settings:

Power Performance Tuning = BIOS Controls EPB
ENERGY_PERF_BIAS_CFG mode = Extreme Performance
KTI Prefetch = Enable
DCU Streamer Prefetcher = Disable
LLC Dead Line Alloc = Disable
Stale AtoS = Disable
Intel SST-PP=Level 3

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on 181-170 Sat Mar 1 11:10:57 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
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 9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Failed units, from systemctl list-units --state=failed
 13. Services, from systemctl list-unit-files
 14. Linux kernel boot-time arguments, from /proc/cmdline
 15. cpupower frequency-info
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
-

1. uname -a
Linux 181-170 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36cle09) x86_64
x86_64 x86_64 GNU/Linux

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Platform Notes (Continued)

2. w
11:10:57 up 2 min, 1 user, load average: 0.55, 0.30, 0.12
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty2 - 11:10 1.00s 0.86s 0.01s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 6189032
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 6189032
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 -c
ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 --configfile
ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.006/templogs/preenv.intrate.006.0.log --lognum 006.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

6. /proc/cpuinfo
model name : Intel(R) Xeon(R) 6960P
vendor_id : GenuineIntel
cpu family : 6
model : 173
stepping : 1
microcode : 0x1000380
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
cpu cores : 48
siblings : 96
2 physical ids (chips)
192 processors (hardware threads)

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Platform Notes (Continued)

```
physical id 0: core ids 0-15,64-79,128-143
physical id 1: core ids 0-15,64-79,128-143
physical id 0: apicids 0-31,128-159,256-287
physical id 1: apicids 512-543,640-671,768-799
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture: x86_64
CPU op-mode(s): 32-bit, 64-bit
Address sizes: 52 bits physical, 57 bits virtual
Byte Order: Little Endian
CPU(s): 192
On-line CPU(s) list: 0-191
Vendor ID: GenuineIntel
BIOS Vendor ID: Intel(R) Corporation
Model name: Intel(R) Xeon(R) 6960P
BIOS Model name: Intel(R) Xeon(R) 6960P CPU @ 3.1GHz
BIOS CPU family: 179
CPU family: 6
Model: 173
Thread(s) per core: 2
Core(s) per socket: 48
Socket(s): 2
Stepping: 1
BogoMIPS: 6200.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtTopology nonstop_tsc cpuid aperfmpf tsc_known_freq pn
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 intel_ppin cdp_12
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid
rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocntd
la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm
md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
amx_tile amx_int8 flush_lld arch_capabilities
Virtualization: VT-x
L1d cache: 4.5 MiB (96 instances)
L1i cache: 6 MiB (96 instances)
L2 cache: 192 MiB (96 instances)
L3 cache: 864 MiB (2 instances)
NUMA node(s): 6
NUMA node0 CPU(s): 0-15,96-111
NUMA node1 CPU(s): 16-31,112-127
NUMA node2 CPU(s): 32-47,128-143
NUMA node3 CPU(s): 48-63,144-159
NUMA node4 CPU(s): 64-79,160-175
NUMA node5 CPU(s): 80-95,176-191
```

(Continued on next page)



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Platform Notes (Continued)

Vulnerability Gather data sampling:	Not affected
Vulnerability Itlb multihit:	Not affected
Vulnerability Llrf:	Not affected
Vulnerability Mds:	Not affected
Vulnerability Meltdown:	Not affected
Vulnerability Mmio stale data:	Not affected
Vulnerability Reg file data sampling:	Not affected
Vulnerability Retbleed:	Not affected
Vulnerability Spec rstack overflow:	Not affected
Vulnerability Spec store bypass:	Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:	Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2:	Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling; PBRSB-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:	Not affected
Vulnerability Tsx async abort:	Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	64K	6M	16	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	432M	864M	16	Unified	3	442368	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 6 nodes (0-5)

node 0 cpus: 0-15,96-111

node 0 size: 257499 MB

node 0 free: 256727 MB

node 1 cpus: 16-31,112-127

node 1 size: 258039 MB

node 1 free: 257193 MB

node 2 cpus: 32-47,128-143

node 2 size: 258039 MB

node 2 free: 256884 MB

node 3 cpus: 48-63,144-159

node 3 size: 258039 MB

node 3 free: 257422 MB

node 4 cpus: 64-79,160-175

node 4 size: 258039 MB

node 4 free: 257355 MB

node 5 cpus: 80-95,176-191

node 5 size: 257630 MB

node 5 free: 256881 MB

node distances:

node	0	1	2	3	4	5
0:	10	12	12	21	21	21
1:	12	10	12	21	21	21
2:	12	12	10	21	21	21
3:	21	21	21	10	12	12
4:	21	21	21	12	10	12
5:	21	21	21	12	12	10

9. /proc/meminfo

MemTotal: 1584422276 kB

10. who -r

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Platform Notes (Continued)

```
run-level 3 Mar 1 11:10 last=5
```

```
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
Default Target Status
graphical degraded
```

```
12. Failed units, from systemctl list-units --state=failed
UNIT LOAD ACTIVE SUB DESCRIPTION
* udisks2.service loaded failed failed Disk Manager
```

```
13. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
display-manager firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early
kdump-notify klog lvm2-monitor nsqd nvmefc-boot-connections nvmf-autoconnect postfix
purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wicd
wicd-dhcp4 wicd-dhcp6 wicd-nanny
enabled-runtime systemd-remount-fs
disabled accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebttables
exchange-bmc-os-info fsidd gpm grub2-once haveged ipmi ipmievda issue-add-ssh-keys
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb ostree-remount rpcbind
rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd
speech-dispatcherd systemd-boot-check-no-failures systemd-confext
systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
update-system-flatpaks upower vncserver@
indirect systemd-userdbd wicd
```

```
14. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=d1a687ce-0a78-499c-9181-4e82ae083016
splash=silent
mitigations=auto
quiet
security=apparmor
crashkernel=365M,high
crashkernel=72M,low
```

```
15. cpupower frequency-info
analyzing CPU 16:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes
```

```
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 20
```

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Platform Notes (Continued)

```
vm.dirty_writeback_centisecs      500
vm.dirtytime_expire_seconds     43200
vm.extfrag_threshold            500
vm.min_unmapped_ratio           1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     60
vm.watermark_boost_factor       15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage
    defrag          always defer defer+madvise [madvise] never
    enabled         [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled   always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs    60000
    defrag                  1
    max_ptes_none           511
    max_ptes_shared          256
    max_ptes_swap            64
    pages_to_scan            4096
    scan_sleep_millisecs     10000
```

```
-----  
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----  
20. Disk information
  SPEC is set to: /home/cpu2017
  Filesystem      Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p3  btrfs  1.5T  156G  1.3T  11% /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
  Vendor:        Supermicro
  Product:       Super Server
  Product Family: Family
  Serial:        0123456789
```

```
-----  
22. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x Micron Technology MTC40F2046S1HC88XD1 WCCCC 64 GB 2 rank 8800
```

```
-----  
23. BIOS
  (This section combines info from /sys/devices and dmidecode.)
```

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Platform Notes (Continued)

BIOS Vendor: American Megatrends International, LLC.
BIOS Version: 1.2
BIOS Date: 01/24/2025
BIOS Revision: 5.35

Compiler Version Notes

=====

C | 502.gcc_r(peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C | 502.gcc_r(peak)

=====

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=====

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

C++ | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
| 541.leela_r(base, peak)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

=====

Fortran | 548.exchange2_r(base, peak)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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=====

Base Compiler Invocation

C benchmarks:

icx

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc



SPEC CPU®2017 Integer Rate Result

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Supermicro

SuperServer SYS-122HA-TN-LCC
(X14DBM-APL, Intel Xeon 6960P)

SPECrate®2017_int_base = 1160

SPECrate®2017_int_peak = 1200

CPU2017 License: 001176

Test Date: Feb-2025

Test Sponsor: Supermicro

Hardware Availability: Feb-2025

Tested by: Supermicro

Software Availability: Jun-2024

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Peak Portability Flags

500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/opt/intel/oneapi/compiler/2024.1/lib -lqkmalloc

502.gcc_r: -m32 -L/opt/intel/oneapi/compiler/2024.1/lib32 -std=gnu89
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc

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Hardware Availability: Feb-2025

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Peak Optimization Flags (Continued)

505.mcf_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -futto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fno-alias
-L/opt/intel/oneapi/compiler/2024.1/lib -lgkmalloc
```

557.xz_r: basepeak = yes

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.2025-05-09.html>
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.2025-05-09.xml>
<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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