SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

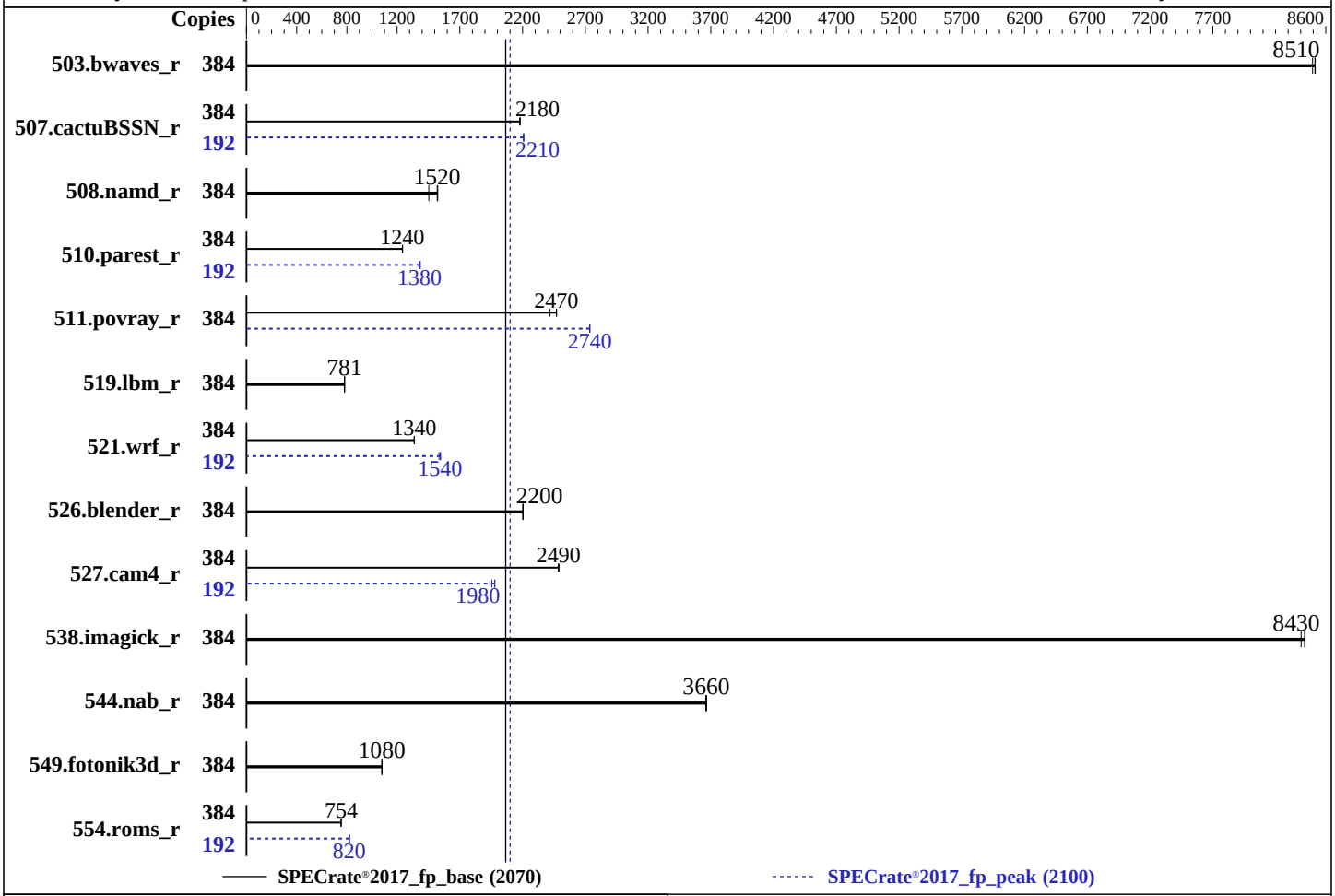
SuperServer SYS-222FT-HEA-LCC
(X14DBT-FAP, Intel Xeon 6972P)

SPECrate®2017_fp_base = 2070

SPECrate®2017_fp_peak = 2100

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024



Hardware

CPU Name:	Intel Xeon 6972P
Max MHz:	3900
Nominal:	2400
Enabled:	192 cores, 2 chips, 2 threads/core
Orderable:	1,2 chips
Cache L1:	64 KB I + 48 KB D on chip per core
L2:	2 MB I+D on chip per core
L3:	480 MB I+D on chip per chip
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	1 x 1.8 TB NVMe SSD
Other:	CPU Cooling: DLC

Software

OS:	SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default
Compiler:	C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
Parallel:	No
Firmware:	Version 1.0 released Feb-2025
File System:	btrfs
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	jemalloc memory allocator V5.0.1
Power Management:	BIOS set to prefer performance at the cost of additional power usage.

SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Supermicro

SuperServer SYS-222FT-HEA-LCC
(X14DBT-FAP, Intel Xeon 6972P)

SPECrate®2017_fp_base = 2070

SPECrate®2017_fp_peak = 2100

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Mar-2025
Hardware Availability: Feb-2025
Software Availability: Jun-2024

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	384	452	8520	**452**	**8510**	453	8490	384	452	8520	**452**	**8510**	453	8490
507.cactuBSSN_r	384	223	2180	223	2180	**223**	**2180**	192	110	2210	**110**	**2210**	110	2210
508.namd_r	384	**240**	**1520**	239	1520	251	1450	384	**240**	**1520**	239	1520	251	1450
510.parest_r	384	807	1240	809	1240	**809**	**1240**	192	**364**	**1380**	364	1380	363	1390
511.povray_r	384	363	2470	371	2420	**363**	**2470**	384	328	2730	**328**	**2740**	328	2740
519.lbm_r	384	**518**	**781**	518	781	518	781	384	**518**	**781**	518	781	518	781
521.wrf_r	384	**643**	**1340**	644	1340	643	1340	192	**278**	**1540**	278	1550	280	1530
526.blender_r	384	**266**	**2200**	265	2210	266	2200	384	**266**	**2200**	265	2210	266	2200
527.cam4_r	384	270	2490	**270**	**2490**	270	2490	192	170	1980	**170**	**1980**	172	1960
538.imagick_r	384	113	8440	**113**	**8430**	114	8400	384	113	8440	**113**	**8430**	114	8400
544.nab_r	384	177	3660	176	3670	**176**	**3660**	384	177	3660	176	3670	**176**	**3660**
549.fotonik3d_r	384	**1385**	**1080**	1388	1080	1385	1080	384	**1385**	**1080**	1388	1080	1385	1080
554.roms_r	384	809	754	**809**	**754**	809	754	192	**372**	**820**	372	821	372	819

SPECrate®2017_fp_base = 2070

SPECrate®2017_fp_peak = 2100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
```

(Continued on next page)

General Notes (Continued)

```
 is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
 is mitigated in the system as tested and documented.
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
 is mitigated in the system as tested and documented.

 jemalloc, a general purpose malloc implementation
 built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
 sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

Platform Notes

```
BIOS Settings:
 Power Performance Tuning = BIOS Controls EPB
 ENERGY_PERF_BIAS_CFG mode = Extreme Performance
 KTI Prefetch = Enable
 DCU Streamer Prefetcher = Disable
 LLC Dead Line Alloc = Disable
 Stale AtoS = Disable

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on 180-113 Fri Mar  7 18:13:41 2025

 SUT (System Under Test) info as seen by some common utilities.

 ------------------------------------------------------------
 Table of contents
 ------------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Failed units, from systemctl list-units --state=failed
 13. Services, from systemctl list-unit-files
 14. Linux kernel boot-time arguments, from /proc/cmdline
 15. cpupower frequency-info
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
 ------------------------------------------------------------

 ------------------------------------------------------------
 1. uname -a
   Linux 180-113 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09) x86_64
   x86_64 x86_64 GNU/Linux
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
2. w
   18:13:42 up 2 min,  1 user,  load average: 0.49, 0.31, 0.12
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty2     -                18:12    3.00s  1.09s  0.01s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 6188404
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 6188404
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=42
 login -- root
 -bash
 -bash
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=384 -c
   ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=192 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=384 --configfile
   ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=192 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
   --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) 6972P
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 173
    stepping        : 1
    microcode       : 0x1000380
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
    cpu cores       : 96
    siblings        : 192
    2 physical ids (chips)
    384 processors (hardware threads)
    physical id 0: core ids 0-31,64-95,128-159
```

(Continued on next page)

Platform Notes (Continued)

```
    physical id 1: core ids 0-31,64-95,128-159
    physical id 0: apicids 0-63,128-191,256-319
    physical id 1: apicids 512-575,640-703,768-831
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               384
  On-line CPU(s) list:                  0-383
  Vendor ID:                            GenuineIntel
  BIOS Vendor ID:                       Intel(R) Corporation
  Model name:                           Intel(R) Xeon(R) 6972P
  BIOS Model name:                      Intel(R) Xeon(R) 6972P  CPU @ 2.4GHz
  BIOS CPU family:                      179
  CPU family:                           6
  Model:                                173
  Thread(s) per core:                   2
  Core(s) per socket:                   96
  Socket(s):                            2
  Stepping:                             1
  BogoMIPS:                             4800.00
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                        nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                        pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
                                        xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
                                        tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
                                        3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
                                        ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
                                        vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
                                        rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
                                        clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                                        split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
                                        arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2
                                        gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
                                        la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm
                                        md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
                                        amx_tile amx_int8 flush_l1d arch_capabilities
  Virtualization:                       VT-x
  L1d cache:                            9 MiB (192 instances)
  L1i cache:                            12 MiB (192 instances)
  L2 cache:                             384 MiB (192 instances)
  L3 cache:                             960 MiB (2 instances)
  NUMA node(s):                         6
  NUMA node0 CPU(s):                    0-31,192-223
  NUMA node1 CPU(s):                    32-63,224-255
  NUMA node2 CPU(s):                    64-95,256-287
  NUMA node3 CPU(s):                    96-127,288-319
  NUMA node4 CPU(s):                    128-159,320-351
  NUMA node5 CPU(s):                    160-191,352-383
  Vulnerability Gather data sampling:   Not affected
```

(Continued on next page)

Platform Notes (Continued)

```
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
  Vulnerability Retbleed:               Not affected
  Vulnerability Spec rstack overflow:   Not affected
  Vulnerability Spec store bypass:      Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:             Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:             Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                        PBRSB-eIBRS Not affected; BHI BHI_DIS_S
  Vulnerability Srbds:                  Not affected
  Vulnerability Tsx async abort:        Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K       9M   12 Data            1     64        1             64
     L1i       64K      12M   16 Instruction     1     64        1             64
     L2         2M     384M   16 Unified         2   2048        1             64
     L3       480M     960M   16 Unified         3 491520        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 6 nodes (0-5)
  node 0 cpus: 0-31,192-223
  node 0 size: 257460 MB
  node 0 free: 256016 MB
  node 1 cpus: 32-63,224-255
  node 1 size: 258031 MB
  node 1 free: 257256 MB
  node 2 cpus: 64-95,256-287
  node 2 size: 257992 MB
  node 2 free: 257243 MB
  node 3 cpus: 96-127,288-319
  node 3 size: 258031 MB
  node 3 free: 257142 MB
  node 4 cpus: 128-159,320-351
  node 4 size: 258031 MB
  node 4 free: 257175 MB
  node 5 cpus: 160-191,352-383
  node 5 size: 257583 MB
  node 5 free: 256862 MB
  node distances:
  node   0   1   2   3   4   5
    0:  10  12  12  21  21  21
    1:  12  10  12  21  21  21
    2:  12  12  10  21  21  21
    3:  21  21  21  10  12  12
    4:  21  21  21  12  10  12
    5:  21  21  21  12  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584261324 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 7 18:12 last=5
```

(Continued on next page)

Platform Notes (Continued)

```
------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  graphical       degraded

------------------------------------------------------------
12. Failed units, from systemctl list-units --state=failed
    UNIT            LOAD   ACTIVE SUB    DESCRIPTION
  * udisks2.service loaded failed failed Disk Manager

------------------------------------------------------------
13. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron
                   display-manager firewalld getty@ irqbalance issue-generator kbdsettings kdump kdump-early
                   kdump-notify klog lvm2-monitor nscd nvmefc-boot-connections nvmf-autoconnect postfix
                   purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4
                   wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         accounts-daemon autofs autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl
                   ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell ebtables
                   exchange-bmc-os-info fsidd gpm grub2-once haveged ipmi ipmievd issue-add-ssh-keys
                   kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb ostree-remount rpcbind
                   rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                   speech-dispatcherd systemd-boot-check-no-failures systemd-confext
                   systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd udisks2
                   update-system-flatpaks upower vncserver@
  indirect         systemd-userdbd wickedd

------------------------------------------------------------
14. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=844840e7-ded5-4714-8032-7e92a13da2bd
  splash=silent
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=365M,high
  crashkernel=72M,low

------------------------------------------------------------
15. cpupower frequency-info
  analyzing CPU 152:
    Unable to determine current policy
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           2
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                     20
  vm.dirty_writeback_centisecs      500
```

(Continued on next page)

Platform Notes (Continued)

```
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
20. Disk information
SPEC is set to: /home/cpu2017
  Filesystem     Type   Size  Used Avail Use% Mounted on
  /dev/nvme0n1p2 btrfs  1.8T  9.7G  1.8T   1% /home

------------------------------------------------------------
21. /sys/devices/virtual/dmi/id
    Vendor:         Supermicro
    Product:        Super Server
    Product Family: Family
    Serial:         0123456789

------------------------------------------------------------
22. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    24x Samsung M321R8GA0PB2-CCPKC 64 GB 2 rank 6400


------------------------------------------------------------
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       American Megatrends International, LLC.
```

(Continued on next page)

Platform Notes (Continued)

```
    BIOS Version:      1.0
    BIOS Date:         02/15/2025
    BIOS Revision:     5.35
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)

Base Optimization Flags (Continued)

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Peak Compiler Invocation

C benchmarks:
icx

C++ benchmarks:
icpx

Fortran benchmarks:
ifx

Benchmarks using both Fortran and C:
ifx icx

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:
icpx icx

Benchmarks using Fortran, C, and C++:
icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

(Continued on next page)

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-SPR-revG.xml

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2025-03-07 21:13:41-0500.
Report generated on 2025-03-26 10:36:06 by CPU2017 PS formatter v6716.
Originally published on 2025-03-25.