# SPEC CPU®2017 Floating Point Rate Result

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## IEIT Systems Co., Ltd.

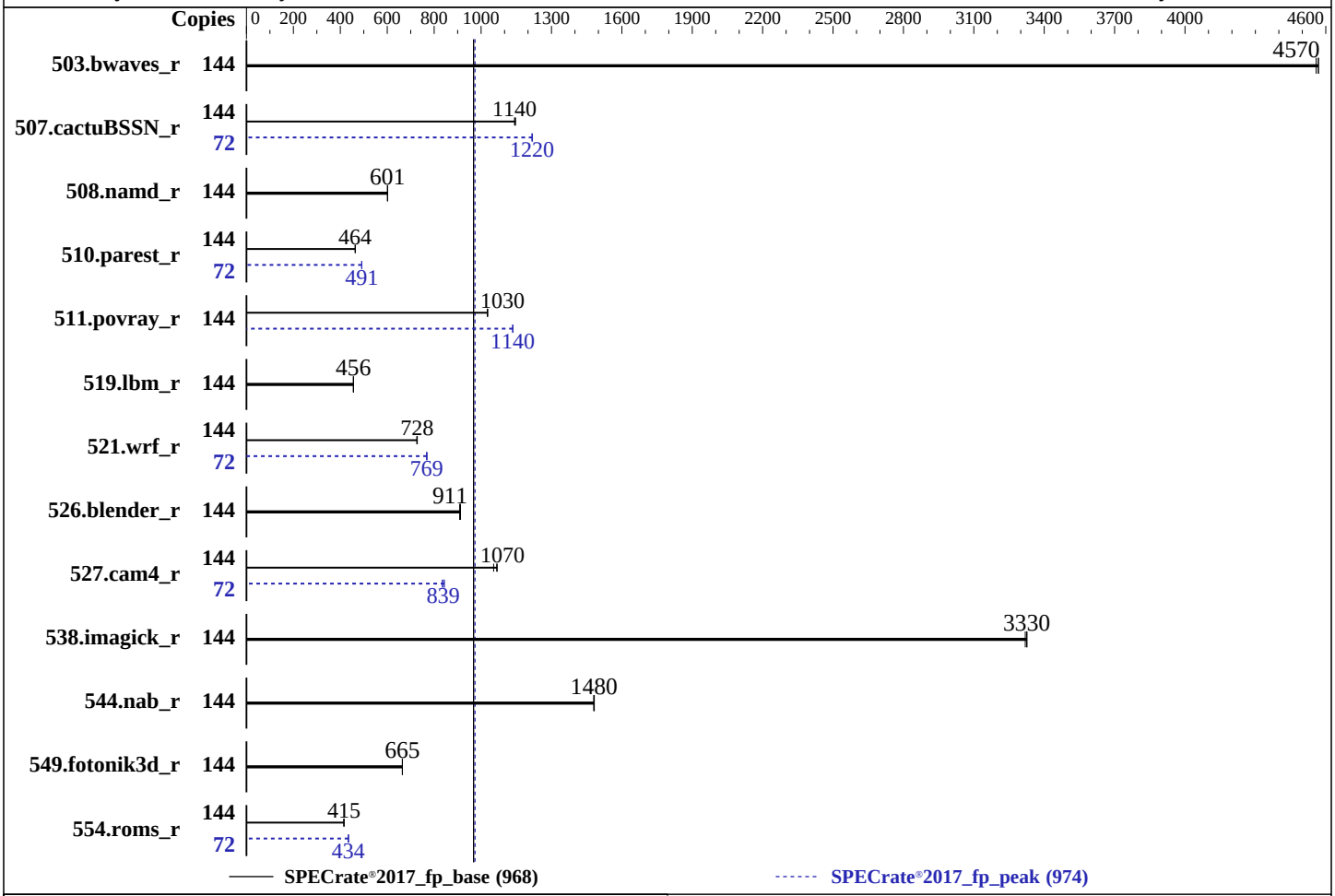
## meta brain NF5180G8 (Intel Xeon 6736P)

**SPECrate®2017_fp_base = 968**

**SPECrate®2017_fp_peak = 974**

**CPU2017 License:** 3358
**Test Sponsor:** IEIT Systems Co., Ltd.
**Tested by:** IEIT Systems Co., Ltd.

**Test Date:** Mar-2025
**Hardware Availability:** Apr-2025
**Software Availability:** Jun-2024




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon 6736P |
| Max MHz: | 4100 |
| Nominal: | 2000 |
| Enabled: | 72 cores, 2 chips, 2 threads/core |
| Orderable: | 1,2 chips |
| Cache L1: | 64 KB I + 48 KB D on chip per core |
| L2: | 2 MB I+D on chip per core |
| L3: | 144 MB I+D on chip per chip |
| Other: | None |
| Memory: | 1 TB (16 x 64 GB 2Rx4 PC5-6400B-R) |
| Storage: | 1 x 1.92 TB NVME SSD |
| Other: | CPU Cooling: Air |

## Software

| | |
|---|---|
| OS: | SUSE Linux Enterprise Server 15 SP6 6.4.0-150600.21-default |
| Compiler: | C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux; Fortran: Version 2024.1 of Intel Fortran Compiler for Linux; |
| Parallel: | No |
| Firmware: | Version 02.03.00 released Feb-2025 |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |
| Peak Pointers: | 64-bit |
| Other: | jemalloc memory allocator V5.0.1 |
| Power Management: | BIOS set to prefer performance at the cost of additional power usage. |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 503.bwaves_r | 144 | 317 | 4560 | **316** | **4570** | 316 | 4570 | 144 | 317 | 4560 | **316** | **4570** | 316 | 4570 |
| 507.cactuBSSN_r | 144 | 159 | 1140 | **159** | **1140** | 159 | 1150 | 72 | 74.8 | 1220 | **74.9** | **1220** | 74.9 | 1220 |
| 508.namd_r | 144 | 227 | 602 | **227** | **601** | 228 | 601 | 144 | 227 | 602 | **227** | **601** | 228 | 601 |
| 510.parest_r | 144 | 813 | 463 | 812 | 464 | **812** | **464** | 72 | **383** | **491** | 383 | 491 | 383 | 492 |
| 511.povray_r | 144 | **328** | **1030** | 328 | 1030 | 327 | 1030 | 144 | 296 | 1140 | **296** | **1140** | 296 | 1130 |
| 519.lbm_r | 144 | 333 | 456 | 333 | 456 | **333** | **456** | 144 | 333 | 456 | 333 | 456 | **333** | **456** |
| 521.wrf_r | 144 | **443** | **728** | 443 | 728 | 444 | 726 | 72 | **210** | **769** | 210 | 769 | 210 | 768 |
| 526.blender_r | 144 | **241** | **911** | 240 | 912 | 241 | 909 | 144 | **241** | **911** | 240 | 912 | 241 | 909 |
| 527.cam4_r | 144 | **236** | **1070** | 236 | 1070 | 239 | 1050 | 72 | 149 | 844 | 151 | 834 | **150** | **839** |
| 538.imagick_r | 144 | 108 | 3320 | **108** | **3330** | 108 | 3330 | 144 | 108 | 3320 | **108** | **3330** | 108 | 3330 |
| 544.nab_r | 144 | 164 | 1480 | **164** | **1480** | 163 | 1480 | 144 | 164 | 1480 | **164** | **1480** | 163 | 1480 |
| 549.fotonik3d_r | 144 | 844 | 665 | 845 | 664 | **844** | **665** | 144 | 844 | 665 | 845 | 664 | **844** | **665** |
| 554.roms_r | 144 | **551** | **415** | 550 | 416 | 551 | 415 | 72 | 263 | 435 | 263 | 434 | **263** | **434** |

**SPECrate®2017_fp_base = 968**

**SPECrate®2017_fp_peak = 974**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
```

## General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Red Hat Enterprise Linux 8.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>       /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
```

**(Continued on next page)**

## General Notes (Continued)

```
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

## Platform Notes

```
BIOS configuration:
ENERGY_PERF_BIAS_CFG mode set to Performance
Hardware Prefetch set to Disable
VT Support set to Disable
SNC set to Enable

Sysinfo program /home/CPU2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Wed Mar 19 10:00:45 2025

SUT (System Under Test) info as seen by some common utilities.

------------------------------------------------------------
Table of contents
------------------------------------------------------------
 1. uname -a
 2. w
 3. Username
 4. ulimit -a
 5. sysinfo process ancestry
 6. /proc/cpuinfo
 7. lscpu
 8. numactl --hardware
 9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   10:00:45 up  5:20,  2 users,  load average: 76.80, 126.37, 136.78
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root     tty1     -                04:42    5:17m  1.52s  0.04s -bash
```

(Continued on next page)

## Platform Notes (Continued)

```
  root     tty2     -                06:41    3:19m  0.02s  0.02s -bash

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 4124510
  max locked memory       (kbytes, -l) 8192
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 4124510
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
5. sysinfo process ancestry
 /usr/lib/systemd/systemd --switched-root --system --deserialize=31
 login -- root
 -bash
 -bash
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 -c
   ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=72 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak -o all fprate
 runcpu --nobuild --action validate --define default-platform-flags --define numcopies=144 --configfile
   ic2024.1-lin-core-avx512-rate-20240308.cfg --define smt-on --define cores=72 --define physicalfirst
   --define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
   --runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
   $SPEC/tmp/CPU2017.027/templogs/preenv.fprate.027.0.log --lognum 027.0 --from_runcpu 2
 specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017

------------------------------------------------------------
6. /proc/cpuinfo
    model name      : Intel(R) Xeon(R) 6736P
    vendor_id       : GenuineIntel
    cpu family      : 6
    model           : 173
    stepping        : 1
    microcode       : 0x1000341
    bugs            : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi
    cpu cores       : 36
    siblings        : 72
    2 physical ids (chips)
    144 processors (hardware threads)
    physical id 0: core ids 0-35
    physical id 1: core ids 0-35
    physical id 0: apicids 0-71
    physical id 1: apicids 128-199
  Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
```

(Continued on next page)

## Platform Notes (Continued)

```
  virtualized systems.  Use the above data carefully.

------------------------------------------------------------
7. lscpu

From lscpu from util-linux 2.39.3:
  Architecture:                         x86_64
  CPU op-mode(s):                       32-bit, 64-bit
  Address sizes:                        52 bits physical, 57 bits virtual
  Byte Order:                           Little Endian
  CPU(s):                               144
  On-line CPU(s) list:                  0-143
  Vendor ID:                            GenuineIntel
  BIOS Vendor ID:                       Intel(R) Corporation
  Model name:                           Intel(R) Xeon(R) 6736P
  BIOS Model name:                      Intel(R) Xeon(R) 6736P  CPU @ 2.0GHz
  BIOS CPU family:                      179
  CPU family:                           6
  Model:                                173
  Thread(s) per core:                   2
  Core(s) per socket:                   36
  Socket(s):                            2
  Stepping:                             1
  CPU(s) scaling MHz:                   20%
  CPU max MHz:                          4100.0000
  CPU min MHz:                          800.0000
  BogoMIPS:                             4000.00
  Flags:                                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
                                        pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
                                        nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
                                        pclmulqdq dtes64 ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                                        pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
                                        xsave avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb
                                        cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp
                                        ibrs_enhanced fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms
                                        invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                                        clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt
                                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                        cqm_mbm_local split_lock_detect user_shstk avx_vnni avx512_bf16
                                        wbnoinvd dtherm ida arat pln pts hwp hwp_act_window hwp_epp
                                        hwp_pkg_req hfi avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni
                                        vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57
                                        rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear
                                        serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16 amx_tile
                                        amx_int8 flush_l1d arch_capabilities
  L1d cache:                            3.4 MiB (72 instances)
  L1i cache:                            4.5 MiB (72 instances)
  L2 cache:                             144 MiB (72 instances)
  L3 cache:                             288 MiB (2 instances)
  NUMA node(s):                         2
  NUMA node0 CPU(s):                    0-35,72-107
  NUMA node1 CPU(s):                    36-71,108-143
  Vulnerability Gather data sampling:   Not affected
  Vulnerability Itlb multihit:          Not affected
  Vulnerability L1tf:                   Not affected
  Vulnerability Mds:                    Not affected
  Vulnerability Meltdown:               Not affected
  Vulnerability Mmio stale data:        Not affected
  Vulnerability Reg file data sampling: Not affected
```

**(Continued on next page)**

## Platform Notes (Continued)

```
  Vulnerability Retbleed:              Not affected
  Vulnerability Spec rstack overflow:  Not affected
  Vulnerability Spec store bypass:     Mitigation; Speculative Store Bypass disabled via prctl
  Vulnerability Spectre v1:            Mitigation; usercopy/swapgs barriers and __user pointer sanitization
  Vulnerability Spectre v2:            Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                                       PBRSB-eIBRS Not affected; BHI BHI_DIS_S
  Vulnerability Srbds:                 Not affected
  Vulnerability Tsx async abort:       Not affected

From lscpu --cache:
     NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL   SETS PHY-LINE COHERENCY-SIZE
     L1d       48K     3.4M   12 Data            1     64        1             64
     L1i       64K     4.5M   16 Instruction     1     64        1             64
     L2         2M     144M   16 Unified         2   2048        1             64
     L3       144M     288M   16 Unified         3 147456        1             64

------------------------------------------------------------
8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
  available: 2 nodes (0-1)
  node 0 cpus: 0-35,72-107
  node 0 size: 515488 MB
  node 0 free: 483974 MB
  node 1 cpus: 36-71,108-143
  node 1 size: 515664 MB
  node 1 free: 485988 MB
  node distances:
  node   0   1
    0:  10  21
    1:  21  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1055900676 kB

------------------------------------------------------------
10. who -r
  run-level 3 Mar 19 04:41

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  multi-user      running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          apparmor auditd cron firewalld getty@ irqbalance issue-generator kbdsettings kdump
                   kdump-early kdump-notify nvmefc-boot-connections nvmf-autoconnect postfix purge-kernels
                   rollback sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                   wickedd-nanny
  enabled-runtime  systemd-remount-fs
  disabled         boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebtables
                   exchange-bmc-os-info fsidd grub2-once haveged hwloc-dump-hwdata ipmievd issue-add-ssh-keys
                   kexec-load lunmask nfs nfs-blkmap rpcbind rpmconfigcheck serial-getty@
                   systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysext
                   systemd-time-wait-sync systemd-timesyncd
  generated        jexec
  indirect         systemd-userdbd wickedd
```

(Continued on next page)

## Platform Notes (Continued)

```
------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=97ebf558-7124-402b-b21a-aea761c5c8d0
  splash=silent
  resume=/dev/disk/by-uuid/d3b5c251-8c9f-43f0-9e42-f93d8f2e68b2
  mitigations=auto
  quiet
  security=apparmor
  crashkernel=370M,high
  crashkernel=72M,low

------------------------------------------------------------
14. cpupower frequency-info
  analyzing CPU 71:
    current policy: frequency should be within 800 MHz and 4.10 GHz.
                    The governor "powersave" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. sysctl
  kernel.numa_balancing              1
  kernel.randomize_va_space          2
  vm.compaction_proactiveness       20
  vm.dirty_background_bytes          0
  vm.dirty_background_ratio         10
  vm.dirty_bytes                     0
  vm.dirty_expire_centisecs       3000
  vm.dirty_ratio                    20
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                      60
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                0

------------------------------------------------------------
16. /sys/kernel/mm/transparent_hugepage
  defrag          always defer defer+madvise [madvise] never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000
```

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## Platform Notes (Continued)

```
------------------------------------------------------------
18. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
19. Disk information
SPEC is set to: /home/CPU2017
  Filesystem     Type  Size  Used Avail Use% Mounted on
  /dev/nvme0n1p3 xfs   1.3T  306G  939G  25% /home

------------------------------------------------------------
20. /sys/devices/virtual/dmi/id
    Vendor:          IEIT SYSTEMS
    Product:         NF5180-M8-A0-R0-00
    Product Family: Not specified
    Serial:          00000000

------------------------------------------------------------
21. dmidecode
  Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
  The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
  determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
  "DMTF SMBIOS" standard.
  Memory:
    16x Samsung M321R8GA0PB2-CCPEC 64 GB 2 rank 6400


------------------------------------------------------------
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
   BIOS Vendor:       American Megatrends International, LLC.
   BIOS Version:      02.03.00
   BIOS Date:         02/14/2025
```

## Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
```

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## Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------------------------------------------------------
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------------------------------------------------
```

## Base Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

## Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

## Base Optimization Flags

C benchmarks:
-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:
-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

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## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
`-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib`

## Peak Compiler Invocation

C benchmarks:
`icx`

C++ benchmarks:
`icpx`

Fortran benchmarks:
`ifx`

Benchmarks using both Fortran and C:
`ifx icx`

Benchmarks using both C and C++:
`icpx icx`

Benchmarks using Fortran, C, and C++:
`icpx icx ifx`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

519.lbm_r: `basepeak = yes`

538.imagick_r: `basepeak = yes`

544.nab_r: `basepeak = yes`

**(Continued on next page)**

# Peak Optimization Flags (Continued)

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html
http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.2.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml
http://www.spec.org/cpu2017/flags/IEIT-Platform-Settings-intel-V1.2.xml


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