



SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL360 Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_fp_base = 1310

SPECrate®2017_fp_peak = 1340

CPU2017 License: 3

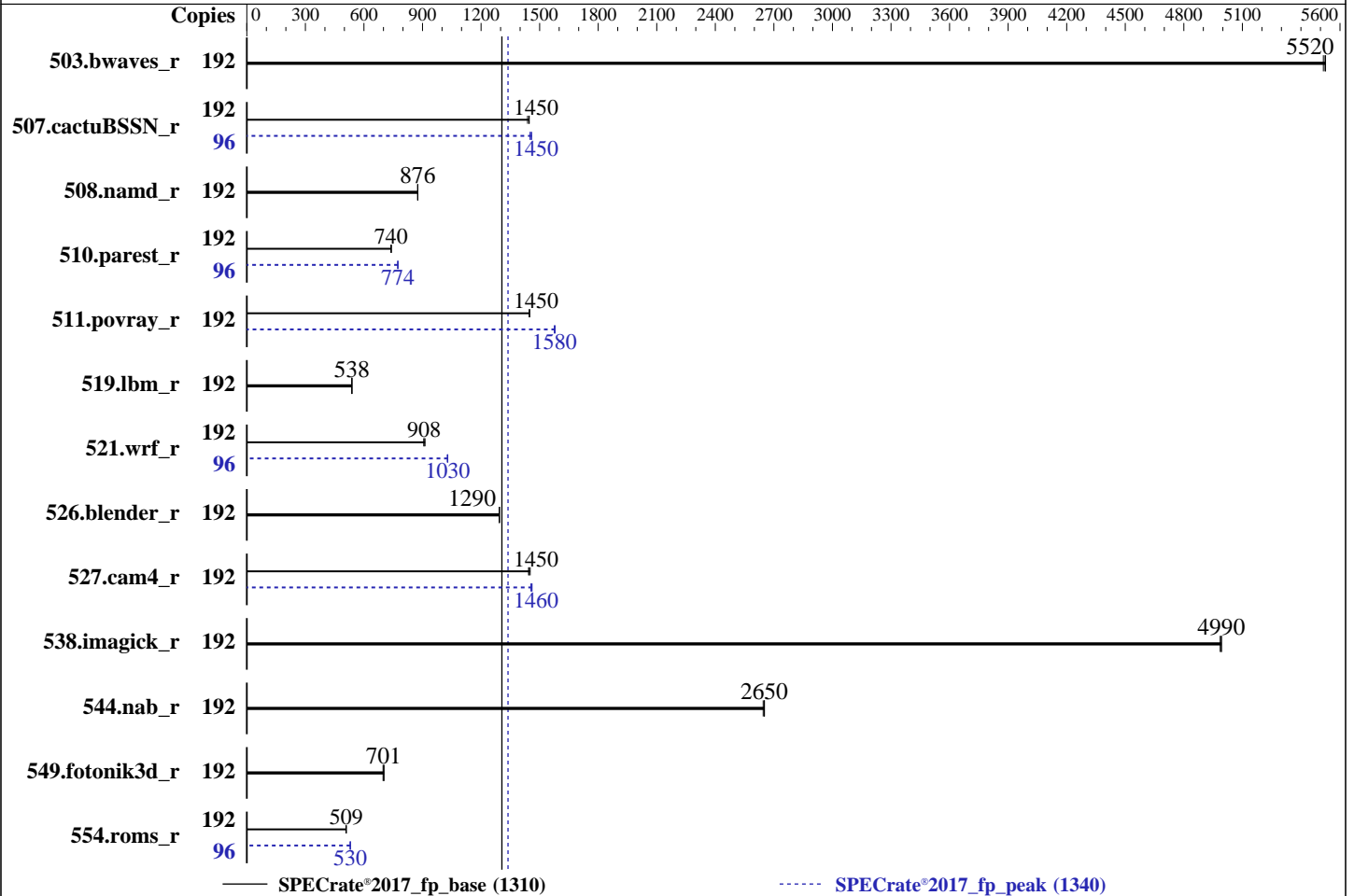
Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024



Hardware

CPU Name: Intel Xeon 6747P
 Max MHz: 3900
 Nominal: 2700
 Enabled: 96 cores, 2 chips, 2 threads/core
 Orderable: 1,2 Chips
 Cache L1: 64 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 288 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (16 x 32 GB 2Rx8 PC5-6400B-R)
 Storage: 1 x 3.2 TB NVMe SSD
 Other: CPU Cooling: CLC

Software

OS: SUSE Linux Enterprise Server 15 SP6
 Kernel 6.4.0-150600.21-default
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: HPE BIOS Version v1.20 02/14/2025 released Feb-2025
 File System: xfs
 System State: Run level 5 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	192	348	5530	349	5520	349	5510	192	348	5530	349	5520	349	5510
507.cactuBSSN_r	192	169	1440	168	1450	168	1450	96	83.6	1450	83.3	1460	83.8	1450
508.namd_r	192	208	876	208	875	208	876	192	208	876	208	875	208	876
510.parest_r	192	679	740	680	739	679	740	96	325	774	325	774	325	773
511.povray_r	192	309	1450	310	1450	310	1450	192	285	1580	284	1580	284	1580
519.lbm_r	192	376	538	376	538	376	538	192	376	538	376	538	376	538
521.wrf_r	192	474	908	471	913	474	907	96	209	1030	209	1030	209	1030
526.blender_r	192	226	1290	226	1290	226	1290	192	226	1290	226	1290	226	1290
527.cam4_r	192	232	1450	233	1440	231	1450	192	230	1460	231	1460	230	1460
538.imagick_r	192	95.6	4990	95.6	4990	95.8	4990	192	95.6	4990	95.6	4990	95.8	4990
544.nab_r	192	122	2650	122	2650	122	2650	192	122	2650	122	2650	122	2650
549.fotonik3d_r	192	1067	701	1072	698	1065	703	192	1067	701	1072	698	1065	703
554.roms_r	192	601	508	599	509	599	509	96	288	530	288	530	288	530

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
tuned-adm profile was stopped using "systemctl stop tuned"
```

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)

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General Notes (Continued)

is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

Last Level Cache (LLC) Prefetch set to Enabled

XPT Prefetch set to Disabled

Intel UPI Prefetch set to Disabled

Workload Profile set to Custom

DCU Stream Prefetcher set to Disabled

Adjacent Sector Prefetch set to Disabled

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Wed Mar 26 08:42:54 2025

SUT (System Under Test) info as seen by some common utilities.

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1. uname -a

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Platform Notes (Continued)

Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
x86_64 x86_64 x86_64 GNU/Linux

2. w
08:42:54 up 6:04, 3 users, load average: 0.00, 0.00, 2.28
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/0 172.17.1.96 02:39 6.00s 0.84s 0.01s -bash

3. Username
From environment variable \$USER: root

4. ulimit -a
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 2062644
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 2062644
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 -c
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --o all fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=192 --configfile
ic2024.1-lin-sapphirerapids-rate-20240308.cfg --define smt-on --define cores=96 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --nopower
--runmode rate --tune base:peak --size refrate fprate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.002/templogs/preenv.fprate.002.0.log --lognum 002.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

6. /proc/cpuinfo
model name : Intel(R) Xeon(R) 6747P
vendor_id : GenuineIntel
cpu family : 6
model : 173
stepping : 1
microcode : 0x1000380
bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs bhi

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Platform Notes (Continued)

```
cpu cores      : 48
siblings       : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-23,64-87
physical id 1: core ids 0-23,64-87
physical id 0: apicids 0-47,128-175
physical id 1: apicids 256-303,384-431
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                192
On-line CPU(s) list:   0-191
Vendor ID:             GenuineIntel
BIOS Vendor ID:       Intel(R) Corporation
Model name:            Intel(R) Xeon(R) 6747P
BIOS Model name:       Intel(R) Xeon(R) 6747P  CPU @ 2.7GHz
BIOS CPU family:       179
CPU family:            6
Model:                 173
Thread(s) per core:    2
Core(s) per socket:    48
Socket(s):             2
Stepping:              1
BogoMIPS:              5400.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx
pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good
nopl xtopology nonstop_tsc cpuid aperfmperf tsc_known_freq pni
pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 sse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt
tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm
3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 intel_ppin cdp_l2
ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow flexpriority ept
vpid ept_ad fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid
rtm cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt
clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
split_lock_detect user_shstk avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts hfi vnmi avx512vbmi umip pku ospke waitpkg avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq
la57 rdpid bus_lock_detect cldemote movdiri movdir64b enqcmd frsm
md_clear serialize tsxldtrk pconfig arch_lbr ibt amx_bf16 avx512_fp16
amx_tile amx_int8 flush_lld arch_capabilities

Virtualization:        VT-x
L1d cache:             4.5 MiB (96 instances)
L1i cache:             6 MiB (96 instances)
L2 cache:              192 MiB (96 instances)
L3 cache:              576 MiB (2 instances)
NUMA node(s):          4
NUMA node0 CPU(s):    0-23,96-119
NUMA node1 CPU(s):    24-47,120-143
```

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Platform Notes (Continued)

```

NUMA node2 CPU(s):          48-71,144-167
NUMA node3 CPU(s):          72-95,168-191
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:   Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:   Mitigation; Enhanced / Automatic IBRS; IBPB conditional; RSB filling;
                             PBRSE-eIBRS Not affected; BHI BHI_DIS_S
Vulnerability Srbds:        Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	64K	6M	16	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	288M	576M	16	Unified	3	294912	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0-23,96-119
node 0 size: 128699 MB
node 0 free: 106376 MB
node 1 cpus: 24-47,120-143
node 1 size: 129011 MB
node 1 free: 109656 MB
node 2 cpus: 48-71,144-167
node 2 size: 129011 MB
node 2 free: 109418 MB
node 3 cpus: 72-95,168-191
node 3 size: 128969 MB
node 3 free: 108890 MB
node distances:
node  0  1  2  3
0:  10  12  21  21
1:  12  10  21  21
2:  21  21  10  12
3:  21  21  12  10

```

9. /proc/meminfo

MemTotal: 528068592 kB

10. who -r

run-level 5 Mar 26 02:38

11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)

```

Default Target Status
graphical      running

```

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Platform Notes (Continued)

12. Services, from `systemctl list-unit-files`

STATE	UNIT FILES
enabled	ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog lvm2-monitor nscd nvme-fc-boot-connections nvme-autoconnect postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
enabled-runtime	systemd-remount-fs
disabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofsd autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebttables exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmiev d issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@ ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@ smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-sysextd systemd-time-wait-sync systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@
indirect	pcscd saned@ systemd-userdbd wickedd

13. Linux kernel boot-time arguments, from `/proc/cmdline`

```

BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=ad77ff4c-3b83-44e0-a924-423ebd972f45
splash=silent
resume=/dev/disk/by-uuid/flb43a48-2334-43e8-bfc6-98384ea036ac
mitigations=auto
quiet
security=apparmor

```

14. `cpupower frequency-info`

```

analyzing CPU 115:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

```

15. `tuned-adm active`

```

It seems that tuned daemon is not running, preset profile is not activated.
Preset profile: balanced

```

16. `sysctl`

```

kernel.numa_balancing          1
kernel.randomize_va_space      2
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                  20
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                 0
vm.nr_hugepages_mempolicy      0

```

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Platform Notes (Continued)

```
vm.nr_overcommit_hugepages      0
vm.swappiness                    60
vm.watermark_boost_factor       15000
vm.watermark_scale_factor       10
vm.zone_reclaim_mode            0
```

```
-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force
```

```
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

```
-----
19. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6
```

```
-----
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p3 xfs  2.0T 502G 1.5T 26% /home
```

```
-----
21. /sys/devices/virtual/dmi/id
Vendor:      HPE
Product:     HPE ProLiant Compute DL360 Gen12
Product Family: ProLiant
Serial:      D249RP0108
```

```
-----
22. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  8x Hynix HMC88AHBRA471N 32 GB 2 rank 6400
  8x Hynix HMC88AHBRA472N 32 GB 2 rank 6400
```

```
-----
23. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      HPE
BIOS Version:     1.20
BIOS Date:        02/14/2025
BIOS Revision:    1.20
Firmware Revision: 1.10
```




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Compiler Version Notes

=====
C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
=====

=====
Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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=====

Base Compiler Invocation

C benchmarks:
icx

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SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL360 Gen12

(2.70 GHz, Intel Xeon 6747P)

SPECrate®2017_fp_base = 1310

SPECrate®2017_fp_peak = 1340

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Jun-2024

Base Compiler Invocation (Continued)

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops

(Continued on next page)



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Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-qopt-mem-layout-trans=4 -mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapfirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

(Continued on next page)



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Tested by: HPE

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Hardware Availability: Mar-2025

Software Availability: Jun-2024

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: -w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids

-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -mprefer-vector-width=512

-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

503.bwaves_r: basepeak = yes

549.fotonik3d_r: basepeak = yes

554.roms_r: -w -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -nostandard-realloc-lhs

-align array32byte -auto -ljemalloc

-L/usr/local/jemalloc64-5.0.1/lib

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Test Date: Mar-2025

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -mprefer-vector-width=512 -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib
```

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4 -Wno-implicit-int
-mprefer-vector-width=512 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

```
-w -m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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