SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

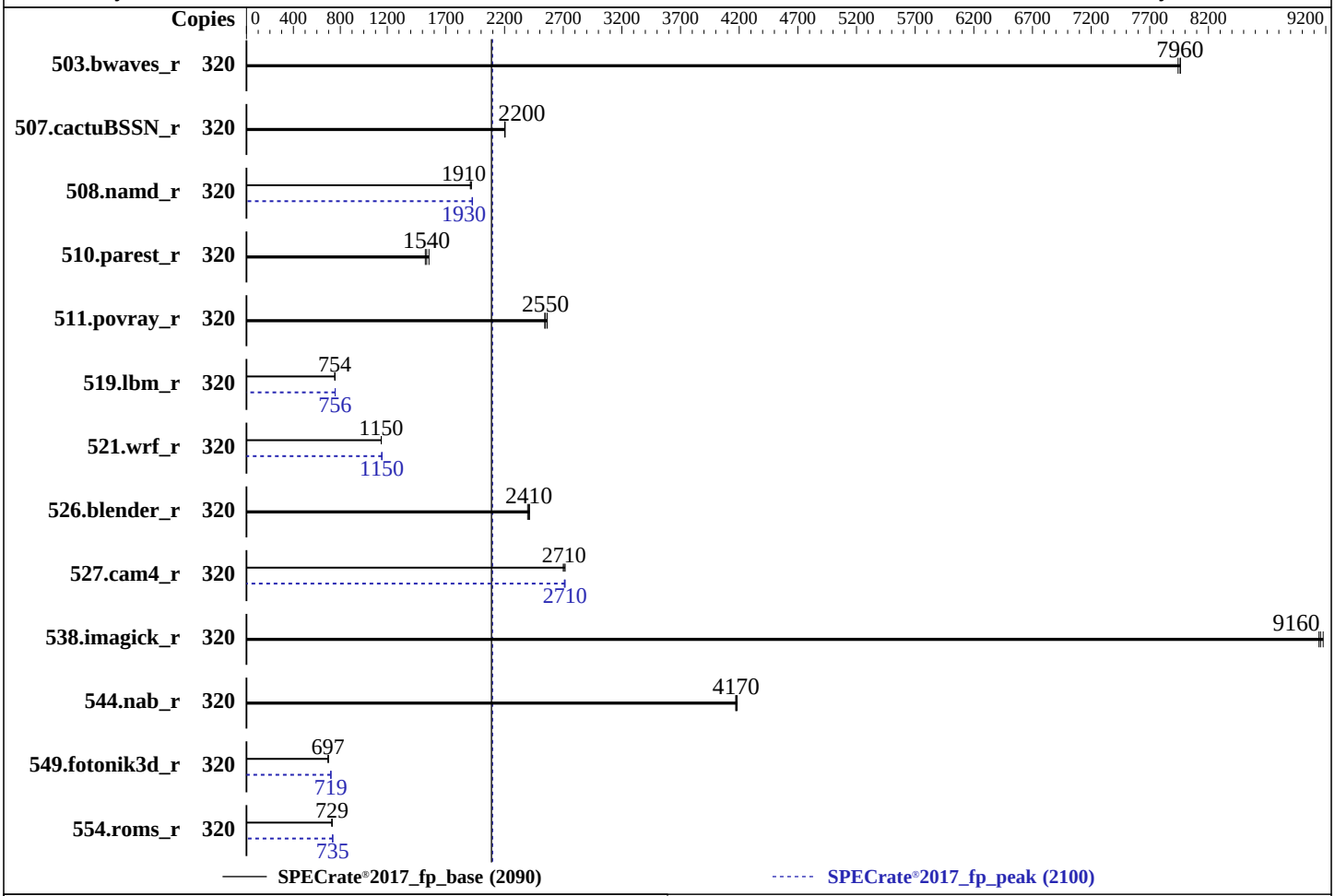
ProLiant DL385 Gen11
(2.10 GHz, AMD EPYC 9845)

SPECrate®2017_fp_base = 2090

SPECrate®2017_fp_peak = 2100

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2025
Hardware Availability: Mar-2025
Software Availability: Oct-2024



Hardware

CPU Name:	AMD EPYC 9845
Max MHz:	3700
Nominal:	2100
Enabled:	320 cores, 2 chips
Orderable:	1,2 chips
Cache L1:	32 KB I + 48 KB D on chip per core
L2:	1 MB I+D on chip per core
L3:	320 MB I+D on chip per chip, 32 MB shared / 16 cores
Other:	None
Memory:	1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)
Storage:	1 x 480 GB SATA SSD
Other:	CPU Cooling: DLC

Software

OS:	SUSE Linux Enterprise Server 15 SP6 kernel 6.4.0-150600.21-default
Compiler:	C/C++/Fortran: Version 5.0.0 of AOCC
Parallel:	No
Firmware:	HPE BIOS Version v2.30 01/17/2025 released Jan-2025
File System:	btrfs
System State:	Run level 5 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other:	None
Power Management:	BIOS and OS set to prefer performance at the cost of additional power usage

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	320	403	7960	**403**	**7960**	404	7940	320	403	7960	**403**	**7960**	404	7940
507.cactuBSSN_r	320	184	2200	184	2200	**184**	**2200**	320	184	2200	184	2200	**184**	**2200**
508.namd_r	320	159	1920	159	1910	**159**	**1910**	320	158	1920	**158**	**1930**	158	1930
510.parest_r	320	**545**	**1540**	538	1560	549	1520	320	**545**	**1540**	538	1560	549	1520
511.povray_r	320	291	2560	294	2540	**293**	**2550**	320	291	2560	294	2540	**293**	**2550**
519.lbm_r	320	448	754	**448**	**754**	447	754	320	446	756	446	756	**446**	**756**
521.wrf_r	320	624	1150	624	1150	**624**	**1150**	320	**621**	**1150**	621	1150	621	1150
526.blender_r	320	202	2410	203	2400	**202**	**2410**	320	202	2410	203	2400	**202**	**2410**
527.cam4_r	320	207	2700	**207**	**2710**	206	2720	320	206	2720	207	2710	**206**	**2710**
538.imagick_r	320	87.0	9140	86.7	9180	**86.9**	**9160**	320	87.0	9140	86.7	9180	**86.9**	**9160**
544.nab_r	320	129	4170	129	4180	**129**	**4170**	320	129	4170	129	4180	**129**	**4170**
549.fotonik3d_r	320	**1790**	**697**	1790	697	1789	697	320	1734	719	1733	720	**1733**	**719**
554.roms_r	320	698	729	697	730	**698**	**729**	320	**692**	**735**	693	734	691	735

SPECrate®2017_fp_base = 2090

SPECrate®2017_fp_peak = 2100

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.
tuned service was stopped using "systemctl stop tuned"
```

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Operating System Notes (Continued)

```
To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
     "/home/cpu2017_new/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017_new/amd_rate_aocc500_znver5_A_lib/l
     ib32:"
MALLOC_CONF = "retain:true"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
```

Platform Notes

```
 BIOS Configuration
 Workload Profile set to General Throughput Compute
 Determinism Control set to Manual
 Performance Determinism set to Power Deterministic
 Memory Patrol Scrubbing set to Disabled
 ACPI CST C2 Latency set to 18 microseconds
 NUMA memory domains per socket set to Two memory domains per socket
 Thermal Configuration set to Maximum Cooling
 AMD SMT Option set to Disabled
 Workload Profile set to Custom
  Power Regulator set to OS Control Mode
  L2 HW Prefetcher set to Disabled
 The reference code/AGESA version used in this ROM is version Turin-PI 1.0.0.3

 Sysinfo program /home/cpu2017_new/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost Sun Jan 19 09:49:15 2025

 SUT (System Under Test) info as seen by some common utilities.

 ----------------------------------------------------------
 Table of contents
 ----------------------------------------------------------
  1. uname -a
  2. w
  3. Username
  4. ulimit -a
  5. sysinfo process ancestry
```

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Platform Notes (Continued)

```
  6. /proc/cpuinfo
  7. lscpu
  8. numactl --hardware
  9. /proc/meminfo
 10. who -r
 11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
 12. Services, from systemctl list-unit-files
 13. Linux kernel boot-time arguments, from /proc/cmdline
 14. cpupower frequency-info
 15. tuned-adm active
 16. sysctl
 17. /sys/kernel/mm/transparent_hugepage
 18. /sys/kernel/mm/transparent_hugepage/khugepaged
 19. OS release
 20. Disk information
 21. /sys/devices/virtual/dmi/id
 22. dmidecode
 23. BIOS
------------------------------------------------------------

------------------------------------------------------------
1. uname -a
  Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09)
  x86_64 x86_64 x86_64 GNU/Linux

------------------------------------------------------------
2. w
   09:49:15 up 13:38,  2 users,  load average: 0.07, 0.02, 0.00
  USER     TTY      FROM             LOGIN@   IDLE   JCPU   PCPU WHAT
  root             :                22Apr24 ?xdm?  11:16   0.02s gdm-session-worker [pam/gdm-password]
  root     seat0    login-           22Apr24  0.00s  0.00s  0.01s /usr/lib/gdm/gdm-x-session
  --register-session --run-script gnome
  root             :1               22Apr24 ?xdm?  11:16   0.01s /usr/lib/gdm/gdm-x-session
  --register-session --run-script gnome
  root     pts/1    10.30.195.96     22Apr24 19.00s  5.51s  4.26s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

------------------------------------------------------------
3. Username
  From environment variable $USER:  root

------------------------------------------------------------
4. ulimit -a
  core file size          (blocks, -c) unlimited
  data seg size           (kbytes, -d) unlimited
  scheduling priority             (-e) 0
  file size               (blocks, -f) unlimited
  pending signals                 (-i) 6190370
  max locked memory       (kbytes, -l) 2097152
  max memory size         (kbytes, -m) unlimited
  open files                      (-n) 1024
  pipe size            (512 bytes, -p) 8
  POSIX message queues     (bytes, -q) 819200
  real-time priority              (-r) 0
  stack size              (kbytes, -s) unlimited
  cpu time               (seconds, -t) unlimited
  max user processes              (-u) 6190370
  virtual memory          (kbytes, -v) unlimited
  file locks                      (-x) unlimited

------------------------------------------------------------
```

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Platform Notes (Continued)

```
 5. sysinfo process ancestry
  /usr/lib/systemd/systemd --switched-root --system --deserialize=42
  sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
  sshd: root [priv]
  sshd: root@pts/1
  -bash
  python3 ./run_fprate.py
  /bin/bash ./amd_rate_aocc500_znver5_A1.sh
  runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
  runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
    --runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
    $SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
  specperl $SPEC/bin/sysinfo
 $SPEC = /home/cpu2017_new

 ------------------------------------------------------------
 6. /proc/cpuinfo
     model name      : AMD EPYC 9845 160-Core Processor
     vendor_id       : AuthenticAMD
     cpu family      : 26
     model           : 17
     stepping        : 0
     microcode       : 0xb101028
     bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
     TLB size        : 192 4K pages
     cpu cores       : 160
     siblings        : 160
     2 physical ids (chips)
     320 processors (hardware threads)
     physical id 0: core ids 0-159
     physical id 1: core ids 0-159
     physical id 0: apicids 0-159
     physical id 1: apicids 256-415
   Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
   virtualized systems.  Use the above data carefully.

 ------------------------------------------------------------
 7. lscpu

 From lscpu from util-linux 2.39.3:
   Architecture:                         x86_64
   CPU op-mode(s):                       32-bit, 64-bit
   Address sizes:                        52 bits physical, 57 bits virtual
   Byte Order:                           Little Endian
   CPU(s):                               320
   On-line CPU(s) list:                  0-319
   Vendor ID:                            AuthenticAMD
   BIOS Vendor ID:                       Advanced Micro Devices, Inc.
   Model name:                           AMD EPYC 9845 160-Core Processor
   BIOS Model name:                      AMD EPYC 9845 160-Core Processor                CPU @ 2.1GHz
   BIOS CPU family:                      107
   CPU family:                           26
   Model:                                17
   Thread(s) per core:                   1
   Core(s) per socket:                   160
   Socket(s):                            2
   Stepping:                             0
   Frequency boost:                      enabled
   CPU(s) scaling MHz:                   101%
   CPU max MHz:                          2100.0000
```

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Platform Notes (Continued)

```
   CPU min MHz:                         1500.0000
   BogoMIPS:                            4193.44
   Flags:                               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                                        extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                                        cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                                        osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                                        ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                                        smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                                        cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                                        xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                                        nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                                        pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                                        avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                                        avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
                                        movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
                                        flush_l1d debug_swap
   Virtualization:                      AMD-V
   L1d cache:                           15 MiB (320 instances)
   L1i cache:                           10 MiB (320 instances)
   L2 cache:                            320 MiB (320 instances)
   L3 cache:                            640 MiB (20 instances)
   NUMA node(s):                        4
   NUMA node0 CPU(s):                   0-79
   NUMA node1 CPU(s):                   80-159
   NUMA node2 CPU(s):                   160-239
   NUMA node3 CPU(s):                   240-319
   Vulnerability Gather data sampling:  Not affected
   Vulnerability Itlb multihit:         Not affected
   Vulnerability L1tf:                  Not affected
   Vulnerability Mds:                   Not affected
   Vulnerability Meltdown:              Not affected
   Vulnerability Mmio stale data:       Not affected
   Vulnerability Reg file data sampling: Not affected
   Vulnerability Retbleed:              Not affected
   Vulnerability Spec rstack overflow:  Not affected
   Vulnerability Spec store bypass:     Mitigation; Speculative Store Bypass disabled via prctl
   Vulnerability Spectre v1:            Mitigation; usercopy/swapgs barriers and __user pointer sanitization
   Vulnerability Spectre v2:            Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                                        disabled; RSB filling; PBRSB-eIBRS Not affected; BHI Not affected
   Vulnerability Srbds:                 Not affected
   Vulnerability Tsx async abort:       Not affected

 From lscpu --cache:
      NAME ONE-SIZE ALL-SIZE WAYS TYPE        LEVEL  SETS PHY-LINE COHERENCY-SIZE
      L1d       48K      15M   12 Data            1    64        1             64
      L1i       32K      10M    8 Instruction     1    64        1             64
      L2         1M     320M   16 Unified         2  1024        1             64
      L3        32M     640M   16 Unified         3 32768        1             64

 ------------------------------------------------------------
 8. numactl --hardware
 NOTE: a numactl 'node' might or might not correspond to a physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0-79
```

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Platform Notes (Continued)

```
  node 0 size: 386671 MB
  node 0 free: 384849 MB
  node 1 cpus: 80-159
  node 1 size: 387050 MB
  node 1 free: 385629 MB
  node 2 cpus: 160-239
  node 2 size: 387050 MB
  node 2 free: 385963 MB
  node 3 cpus: 240-319
  node 3 size: 386849 MB
  node 3 free: 385368 MB
  node distances:
  node   0   1   2   3
    0:  10  12  32  32
    1:  12  10  32  32
    2:  32  32  10  12
    3:  32  32  12  10

------------------------------------------------------------
9. /proc/meminfo
   MemTotal:       1584765892 kB

------------------------------------------------------------
10. who -r
  run-level 5 Apr 22 17:30

------------------------------------------------------------
11. Systemd service manager version: systemd 254 (254.10+suse.84.ge8d77af424)
  Default Target  Status
  graphical       running

------------------------------------------------------------
12. Services, from systemctl list-unit-files
  STATE            UNIT FILES
  enabled          ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
                   bluetooth cron display-manager getty@ irqbalance issue-generator kbdsettings klog
                   lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd systemd-pstore wicked
                   wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant
  enabled-runtime  systemd-remount-fs
  disabled         NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon autofs
                   autoyast-initscripts blk-availability bluetooth-mesh boot-sysctl ca-certificates
                   chrony-wait chronyd console-getty cups cups-browsed debug-shell dnsmasq ebtables
                   exchange-bmc-os-info firewalld fsidd gpm grub2-once haveged hwloc-dump-hwdata ipmi ipmievd
                   issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb openvpn@
                   ostree-remount rpcbind rpmconfigcheck rsyncd rtkit-daemon serial-getty@
                   smartd_generate_opts smb snmpd snmptrapd speech-dispatcherd systemd-boot-check-no-failures
                   systemd-confext systemd-network-generator systemd-sysext systemd-time-wait-sync
                   systemd-timesyncd tuned udisks2 update-system-flatpaks upower vncserver@ wpa_supplicant@
  indirect         pcscd saned@ systemd-userdbd wickedd

------------------------------------------------------------
13. Linux kernel boot-time arguments, from /proc/cmdline
  BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
  root=UUID=c70d6b0a-e893-482b-b564-48624143e39e
  splash=silent
  mitigations=auto
  quiet
  security=apparmor

------------------------------------------------------------
```

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Platform Notes (Continued)

```
14. cpupower frequency-info
  analyzing CPU 109:
    current policy: frequency should be within 1.50 GHz and 2.10 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.
    boost state support:
      Supported: yes
      Active: yes

------------------------------------------------------------
15. tuned-adm active
  No current active profile.

------------------------------------------------------------
16. sysctl
  kernel.numa_balancing               1
  kernel.randomize_va_space           0
  vm.compaction_proactiveness        20
  vm.dirty_background_bytes           0
  vm.dirty_background_ratio          10
  vm.dirty_bytes                      0
  vm.dirty_expire_centisecs        3000
  vm.dirty_ratio                      8
  vm.dirty_writeback_centisecs      500
  vm.dirtytime_expire_seconds     43200
  vm.extfrag_threshold              500
  vm.min_unmapped_ratio               1
  vm.nr_hugepages                     0
  vm.nr_hugepages_mempolicy           0
  vm.nr_overcommit_hugepages          0
  vm.swappiness                       1
  vm.watermark_boost_factor       15000
  vm.watermark_scale_factor          10
  vm.zone_reclaim_mode                1

------------------------------------------------------------
17. /sys/kernel/mm/transparent_hugepage
  defrag          [always] defer defer+madvise madvise never
  enabled         [always] madvise never
  hpage_pmd_size  2097152
  shmem_enabled   always within_size advise [never] deny force

------------------------------------------------------------
18. /sys/kernel/mm/transparent_hugepage/khugepaged
  alloc_sleep_millisecs   60000
  defrag                      1
  max_ptes_none             511
  max_ptes_shared           256
  max_ptes_swap              64
  pages_to_scan            4096
  scan_sleep_millisecs    10000

------------------------------------------------------------
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP6

------------------------------------------------------------
20. Disk information
SPEC is set to: /home/cpu2017_new
```

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Platform Notes (Continued)

```
  Filesystem     Type   Size  Used Avail Use% Mounted on
  /dev/sda3      btrfs  407G   74G  332G  19% /home

 ------------------------------------------------------------
 21. /sys/devices/virtual/dmi/id
     Vendor:         HPE
     Product:        ProLiant DL385 Gen11
     Product Family: ProLiant
     Serial:         DL385G11-006

 ------------------------------------------------------------
 22. dmidecode
   Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
   The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
   "DMTF SMBIOS" standard.
   Memory:
     10x Micron MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400
     14x Micron MTC40F2046S1RC64BD2 QSFF 64 GB 2 rank 6400


 ------------------------------------------------------------
 23. BIOS
 (This section combines info from /sys/devices and dmidecode.)
    BIOS Vendor:       HPE
    BIOS Version:      2.30
    BIOS Date:         01/17/2025
    BIOS Revision:     2.30
    Firmware Revision: 1.63
```

Compiler Version Notes

```
==============================================================================================================
C               | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++             | 508.namd_r(base, peak) 510.parest_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
--------------------------------------------------------------------------------------------------------------

==============================================================================================================
C++, C          | 511.povray_r(base, peak) 526.blender_r(base, peak)
--------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
```

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Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
C++, C, Fortran | 507.cactuBSSN_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran         | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------

==============================================================================================================================
Fortran, C      | 521.wrf_r(base, peak) 527.cam4_r(base, peak)
------------------------------------------------------------------------------------------------------------------------
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin
------------------------------------------------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`clang`

C++ benchmarks:
`clang++`

Fortran benchmarks:
`flang`

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
flang clang

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

C++ benchmarks:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto

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Base Optimization Flags (Continued)

C++ benchmarks (continued):
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

Fortran benchmarks:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

Benchmarks using both Fortran and C:
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Benchmarks using both C and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlibm -lamdalloc -lflang
-ldl

Benchmarks using Fortran, C, and C++:
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50

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Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlibm -lamdalloc -lflang -ldl

Base Other Flags

C benchmarks:
-Wno-unused-command-line-argument

C++ benchmarks:
-Wno-unused-command-line-argument

Fortran benchmarks:
-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:
-Wno-unused-command-line-argument

Benchmarks using both C and C++:
-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:
-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:
clang

C++ benchmarks:
clang++

Fortran benchmarks:
flang

Benchmarks using both Fortran and C:
flang clang

(Continued on next page)

Peak Compiler Invocation (Continued)

Benchmarks using both C and C++:
clang++ clang

Benchmarks using Fortran, C, and C++:
clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl

510.parest_r: basepeak = yes

Fortran benchmarks:

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Peak Optimization Flags (Continued)

503.bwaves_r: basepeak = yes

549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang

554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm
-lamdalloc -ldl -lflang

Benchmarks using both Fortran and C:

521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -reduce-array-computations=3 -zopt -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc
-ldl -lflang

Benchmarks using both C and C++:

511.povray_r: basepeak = yes

526.blender_r: basepeak = yes

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Peak Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: `basepeak = yes`

Peak Other Flags

C benchmarks:
`-Wno-unused-command-line-argument`

C++ benchmarks:
`-Wno-unused-command-line-argument`

Fortran benchmarks:
`-Wno-unused-command-line-argument`

Benchmarks using both Fortran and C:
`-Wno-unused-command-line-argument`

Benchmarks using both C and C++:
`-Wno-unused-command-line-argument`

Benchmarks using Fortran, C, and C++:
`-Wno-unused-command-line-argument`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html`
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.5.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml`
`http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.5.xml`

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