



SPEC CPU®2017 Integer Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(3.20 GHz, AMD EPYC 9555P)

SPECrate®2017_int_base = 728

SPECrate®2017_int_peak = 744

CPU2017 License: 3

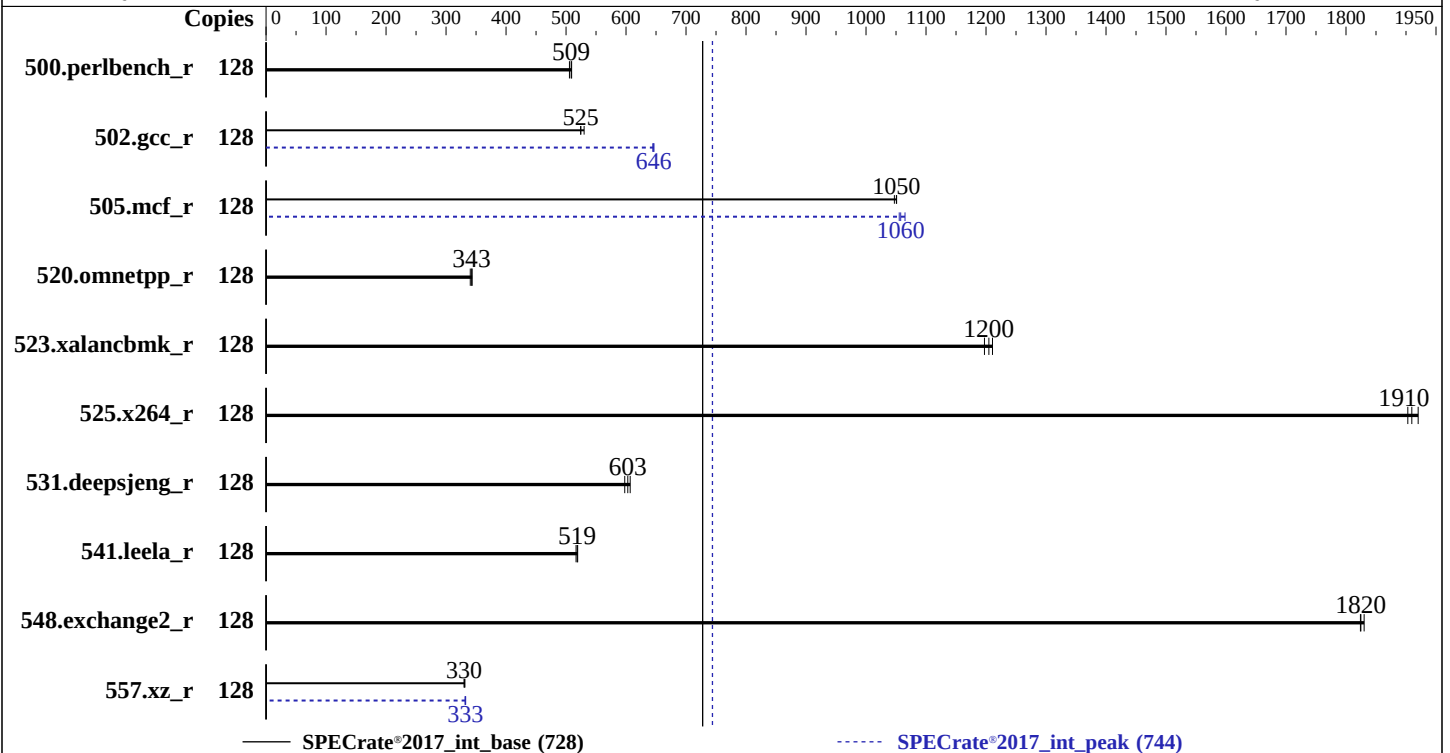
Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024



Hardware

CPU Name: AMD EPYC 9555P
Max MHz: 4400
Nominal: 3200
Enabled: 64 cores, 1 chip, 2 threads/core
Orderable: 1 chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip, 32 MB shared / 8 cores
Other: None
Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)
Storage: 1 x 3 TB NVMe SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP6
kernel 6.4.0-150600.21-default
Compiler: C/C++/Fortran: Version 5.0.0 of AOCC
Parallel: No
Firmware: HPE BIOS Version v1.20 07/11/2025 released Jul-2025
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other: None
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	403	506	<u>401</u>	<u>509</u>	400	509	128	403	506	<u>401</u>	<u>509</u>	400	509
502.gcc_r	128	<u>345</u>	<u>525</u>	346	524	342	530	128	<u>281</u>	<u>646</u>	280	646	281	645
505.mcf_r	128	198	1050	197	1050	<u>197</u>	<u>1050</u>	128	196	1060	<u>196</u>	<u>1060</u>	194	1060
520.omnetpp_r	128	489	343	493	340	<u>490</u>	<u>343</u>	128	489	343	493	340	<u>490</u>	<u>343</u>
523.xalancbmk_r	128	<u>112</u>	<u>1200</u>	113	1200	112	1210	128	<u>112</u>	<u>1200</u>	113	1200	112	1210
525.x264_r	128	118	1900	117	1920	<u>117</u>	<u>1910</u>	128	118	1900	117	1920	<u>117</u>	<u>1910</u>
531.deepsjeng_r	128	<u>243</u>	<u>603</u>	242	607	245	598	128	<u>243</u>	<u>603</u>	242	607	245	598
541.leela_r	128	408	519	<u>409</u>	<u>519</u>	410	517	128	408	519	<u>409</u>	<u>519</u>	410	517
548.exchange2_r	128	<u>184</u>	<u>1820</u>	184	1820	183	1830	128	<u>184</u>	<u>1820</u>	184	1820	183	1830
557.xz_r	128	419	330	<u>418</u>	<u>330</u>	417	331	128	417	332	416	333	<u>416</u>	<u>333</u>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



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Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD_LIBRARY_PATH =

"/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017/amd_rate_aocc500_znver5_A_lib/lib32:"

MALLOC_CONF = "retain:true"

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Configurations : Parameters are selected in the order shown below

Workload Profile set to High Performance Compute (HPC)

Determinism Control set to Manual

Performance Determinism set to Power Deterministic

Memory Patrol Scrubbing set to Disabled

ACPI CST C2 Latency set to 18 microseconds

Last-Level Cache (LLC) as NUMA Node set to Enabled

NUMA memory domains per socket set to Four memory domains per socket

Thermal Configuration set to Enhanced CPU Cooling

AMD Periodic Directory Rinse set to Periodic

Workload Profile set to Custom

Power Regulator set to OS Control Mode

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on localhost Sun Sep 7 08:34:34 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
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16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged

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Platform Notes (Continued)

18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. `uname -a`
Linux localhost 6.4.0-150600.21-default #1 SMP PREEMPT_DYNAMIC Thu May 16 11:09:22 UTC 2024 (36c1e09/lp)
x86_64 x86_64 x86_64 GNU/Linux

2. `w`
08:34:34 up 28 min, 3 users, load average: 0.01, 0.00, 0.00
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root pts/1 10.79.30.176:S.0 08:33 9.00s 0.78s 0.03s /bin/bash ./amd_rate_aocc500_znver5_A1.sh

3. Username
From environment variable \$USER: root

4. `ulimit -a`
core file size (blocks, -c) unlimited
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 3094537
max locked memory (kbytes, -l) 2097152
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 3094537
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

5. `sysinfo process ancestry`
/usr/lib/systemd/systemd --switched-root --system --deserialize=42
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
screen -S cpu
SCREEN -S cpu
/bin/bash
python3 ./run_intrate.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 intrate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate intrate --nopreenv --note-preenv --logfile
\$SPEC/tmp/CPU2017.030/temlogs/preenv.intrate.030.0.log --lognum 030.0 --from_runcpu 2
specperl \$SPEC/bin/sysinfo
\$SPEC = /home/cpu2017

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Platform Notes (Continued)

6. /proc/cpuinfo

```
model name      : AMD EPYC 9555P 64-Core Processor
vendor_id       : AuthenticAMD
cpu family      : 26
model           : 2
stepping        : 1
microcode       : 0xb002151
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 192 4K pages
cpu cores       : 64
siblings        : 128
1 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-63
physical id 0: apicids 0-127
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.39.3:

```
Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:      52 bits physical, 57 bits virtual
Byte Order:         Little Endian
CPU(s):             128
On-line CPU(s) list: 0-127
Vendor ID:          AuthenticAMD
Model name:         AMD EPYC 9555P 64-Core Processor
CPU family:         26
Model:              2
Thread(s) per core: 2
Core(s) per socket: 64
Socket(s):          1
Stepping:           1
Frequency boost:    enabled
CPU(s) scaling MHz: 102%
CPU max MHz:        3200.0000
CPU min MHz:        1500.0000
BogoMIPS:           6399.75
Flags:              fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                   pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                   rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                   extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                   sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm
                   cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                   osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                   perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                   ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                   smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                   avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                   xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                   cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                   xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                   nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                   pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnmi
                   avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                   avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid bus_lock_detect
```

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Platform Notes (Continued)

```
movdiri movdir64b overflow_recov succor smca fsrm avx512_vp2intersect
flush_l1d debug_swap
L1d cache: 3 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 64 MiB (64 instances)
L3 cache: 256 MiB (8 instances)
NUMA node(s): 8
NUMA node0 CPU(s): 0-7,64-71
NUMA node1 CPU(s): 8-15,72-79
NUMA node2 CPU(s): 16-23,80-87
NUMA node3 CPU(s): 24-31,88-95
NUMA node4 CPU(s): 32-39,96-103
NUMA node5 CPU(s): 40-47,104-111
NUMA node6 CPU(s): 48-55,112-119
NUMA node7 CPU(s): 56-63,120-127
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	1M	64M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 8 nodes (0-7)
node 0 cpus: 0-7,64-71
node 0 size: 96448 MB
node 0 free: 90048 MB
node 1 cpus: 8-15,72-79
node 1 size: 96763 MB
node 1 free: 96458 MB
node 2 cpus: 16-23,80-87
node 2 size: 96763 MB
node 2 free: 96396 MB
node 3 cpus: 24-31,88-95
node 3 size: 96763 MB
node 3 free: 96461 MB
node 4 cpus: 32-39,96-103
node 4 size: 96763 MB
node 4 free: 96270 MB
node 5 cpus: 40-47,104-111
node 5 size: 96725 MB
node 5 free: 94263 MB
```

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Platform Notes (Continued)

```
node 6 cpus: 48-55,112-119
node 6 size: 96763 MB
node 6 free: 96336 MB
node 7 cpus: 56-63,120-127
node 7 size: 96673 MB
node 7 free: 96310 MB
node distances:
node  0  1  2  3  4  5  6  7
0: 10 11 12 12 12 12 12 12
1: 11 10 12 12 12 12 12 12
2: 12 12 10 11 12 12 12 12
3: 12 12 11 10 12 12 12 12
4: 12 12 12 12 10 11 12 12
5: 12 12 12 12 11 10 12 12
6: 12 12 12 12 12 12 10 11
7: 12 12 12 12 12 12 11 10
```

```
-----
9. /proc/meminfo
MemTotal:      792233100 kB
```

```
-----
10. who -r
run-level 5 Sep 7 08:06
```

```
-----
11. Systemd service manager version: systemd 254 (254.27+suse.167.g130293e510)
Default Target Status
graphical          running
```

```
-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager YaST2-Firstboot YaST2-Second-Stage apparmor appstream-sync-cache auditd
bluetooth cron display-manager getty@ irqbalance iscsi issue-generator kbdsettings klog
lvm2-monitor nsd nvme-fc-boot-connections nvme-fc-autoconnect oracle postfix purge-kernels
rollback rsyslog sapconf smartd sshd sysctl-logger systemd-pstore virtqemu virtxend
wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny wpa_supplicant xencommons
enabled-runtime systemd-fsck-root systemd-remount-fs
disabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon amavis
apache2 apache2@ autofs autoyast-initscripts bgpd blk-availability bluetooth-mesh
boot-sysctl booth-arbitrator booth@ ca-certificates certmonger chrony-wait chronyd
clamav-milter clamd clamondacc console-getty corosync corosync-notifyd crm_mon ctddb cups
cups-browsed ddclient debug-shell dhcpd dhcpd6 dhcrelay dhcrelay6 dirsrv@ dlm dnsmasq drbd
drbd-lvchange@ drbd-wait-promotable@ ebttables exchange-bmc-os-info fetchmail firewall
fsidd gpm grub2-once haveged hawk hwloc-dump-hwdata ipmi ipmievdp ipvsadm iscsi-init iscsid
isisd issue-add-ssh-keys kexec-load ksm kvm_stat ldirectord libvirt-guests logd lunmask
lvmlockd lvmlocks man-db-create mariadb mariadb@ multipathd named nfs nfs-blkmap
nfs-server nfsserver nmb openvpn@ ospf6d ospfd ostree-remount pacemaker racoon
racoon-setkey radvd rarpd@ ripd ripngd rpcbind rpmconfigcheck rsyncd rtkit-daemon sanlock
sbd sbd_remote serial-getty@ smartd_generate_opts smb snmpd snmptrapd spamd spamd
speech-dispatcherd squid srp_daemon srp_daemon_port@ strongswan strongswan-starter sysstat
systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-nspawn@
systemd-sysext systemd-time-wait-sync systemd-timesyncd tcsh udisks2
update-system-flatpaks upower virtinterfaced virtlockd virtlogd virtnetworkd virtnodeudev
virtnwfilterd virtsecret virtstoraged vncserver@ vsftpd wdm winbind wpa_supplicant@
xen-dom0-modules xen-init-dom0 xen-qemu-dom0-disk-backend xen-watchdog xenconsoled
xendomains xenstored zebra
indirect pcsd saned@ systemd-userdbd tftp uidd wickedd
```

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Platform Notes (Continued)

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-6.4.0-150600.21-default
root=UUID=31eb4e3c-f432-4a30-b618-dd595ca988cb
splash=silent
mitigations=auto
quiet
security=apparmor
```

14. cpupower frequency-info

```
analyzing CPU 41:
  current policy: frequency should be within 1.50 GHz and 3.20 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
  boost state support:
    Supported: yes
    Active: yes
```

15. sysctl

```
kernel.numa_balancing          0
kernel.randomize_va_space      0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      314572800
vm.dirty_background_ratio      0
vm.dirty_bytes                 0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag      [always] defer defer+madvice madvice never
enabled     [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000
```

18. OS release

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Platform Notes (Continued)

From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP6

19. Disk information

SPEC is set to: /home/cpu2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/nvme6n1p3	ext4	2.9T	19G	2.7T	1%	/

20. /sys/devices/virtual/dmi/id

Vendor: HPE
Product: HPE ProLiant Compute DL325 Gen12
Product Family: ProLiant
Serial: SANJACSCM

21. dmidecode

Additional information from dmidecode 3.6 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

6x Micron MTC40F2046S1RC64BD2 MWFF 64 GB 2 rank 6400, configured at 5200
6x Micron MTC40F2046S1RC64BD2 QSFF 64 GB 2 rank 6400, configured at 5200

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 1.20
BIOS Date: 07/11/2025
BIOS Revision: 1.20
Firmware Revision: 1.16

Compiler Version Notes

C | 502.gcc_r(peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: i386-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
| 557.xz_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C | 502.gcc_r(peak)

(Continued on next page)



SPEC CPU®2017 Integer Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant Compute DL325 Gen12

(3.20 GHz, AMD EPYC 9555P)

SPECrate®2017_int_base = 728

SPECrate®2017_int_peak = 744

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Sep-2025

Hardware Availability: Jul-2025

Software Availability: Oct-2024

Compiler Version Notes (Continued)

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: i386-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```
C      | 500.perlbench_r(base, peak) 502.gcc_r(base) 505.mcf_r(base, peak) 525.x264_r(base, peak)
      | 557.xz_r(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```
C++    | 520.omnetpp_r(base, peak) 523.xalancbmk_r(base, peak) 531.deepsjeng_r(base, peak)
      | 541.leela_r(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

```
Fortran | 548.exchange2_r(base, peak)
```

AMD clang version 17.0.6 (CLANG: AOCC_5.0.0-Build#1316 2024_09_09)

Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64

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Base Portability Flags (Continued)

502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather
-Wl,-mllvm -Wl,-extra-inliner -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lflang
-lamdalloc-ext -ldl

C++ benchmarks:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=advanced -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=2000000
-mllvm -reduce-array-computations=3 -zopt -fno-PIE -no-pie
-fvirtual-function-elimination -fvisibility=hidden
-mllvm -do-block-reorder=advanced -lamdlibm -lflang -lamdalloc-ext
-ldl

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -z muldefs -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto
-fepilog-vectorization-of-inductions -mllvm -optimize-strided-mem-cost
-floop-transform -mllvm -unroll-aggressive -mllvm -unroll-threshold=500
-lamdlibm -lflang -lamdalloc -ldl



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Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Peak Portability Flags

500.perlbench_r: -DSPEC_LINUX_X64 -DSPEC_LP64

502.gcc_r: -D_FILE_OFFSET_BITS=64

505.mcf_r: -DSPEC_LP64

520.omnetpp_r: -DSPEC_LP64

523.xalancbmk_r: -DSPEC_LINUX -DSPEC_LP64

525.x264_r: -DSPEC_LP64

531.deepsjeng_r: -DSPEC_LP64

541.leela_r: -DSPEC_LP64

548.exchange2_r: -DSPEC_LP64

557.xz_r: -DSPEC_LP64

Peak Optimization Flags

C benchmarks:

500.perlbench_r: basepeak = yes

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Peak Optimization Flags (Continued)

502.gcc_r: -m32 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-z muldefs -Ofast -march=znver5 -fveclib=AMDLIBM
-ffast-math -fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -fgnu89-inline
-lamdalloc

505.mcf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-extra-inliner -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

525.x264_r: basepeak = yes

557.xz_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Wl,-mllvm -Wl,-extra-inliner
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lflang -lamdalloc-ext -ldl

C++ benchmarks:

520.omnetpp_r: basepeak = yes

523.xalancbmk_r: basepeak = yes

531.deepsjeng_r: basepeak = yes

541.leela_r: basepeak = yes

Fortran benchmarks:

548.exchange2_r: basepeak = yes



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Peak Other Flags

C benchmarks (except as noted below):

-Wno-unused-command-line-argument

502.gcc_r: -L/usr/lib32 -Wno-unused-command-line-argument

-L/home/work/cpu2017/v119/aocc5/1316/amd_rate_aocc500_znver5_A_lib/lib32

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-Turin-rev1.7.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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