



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

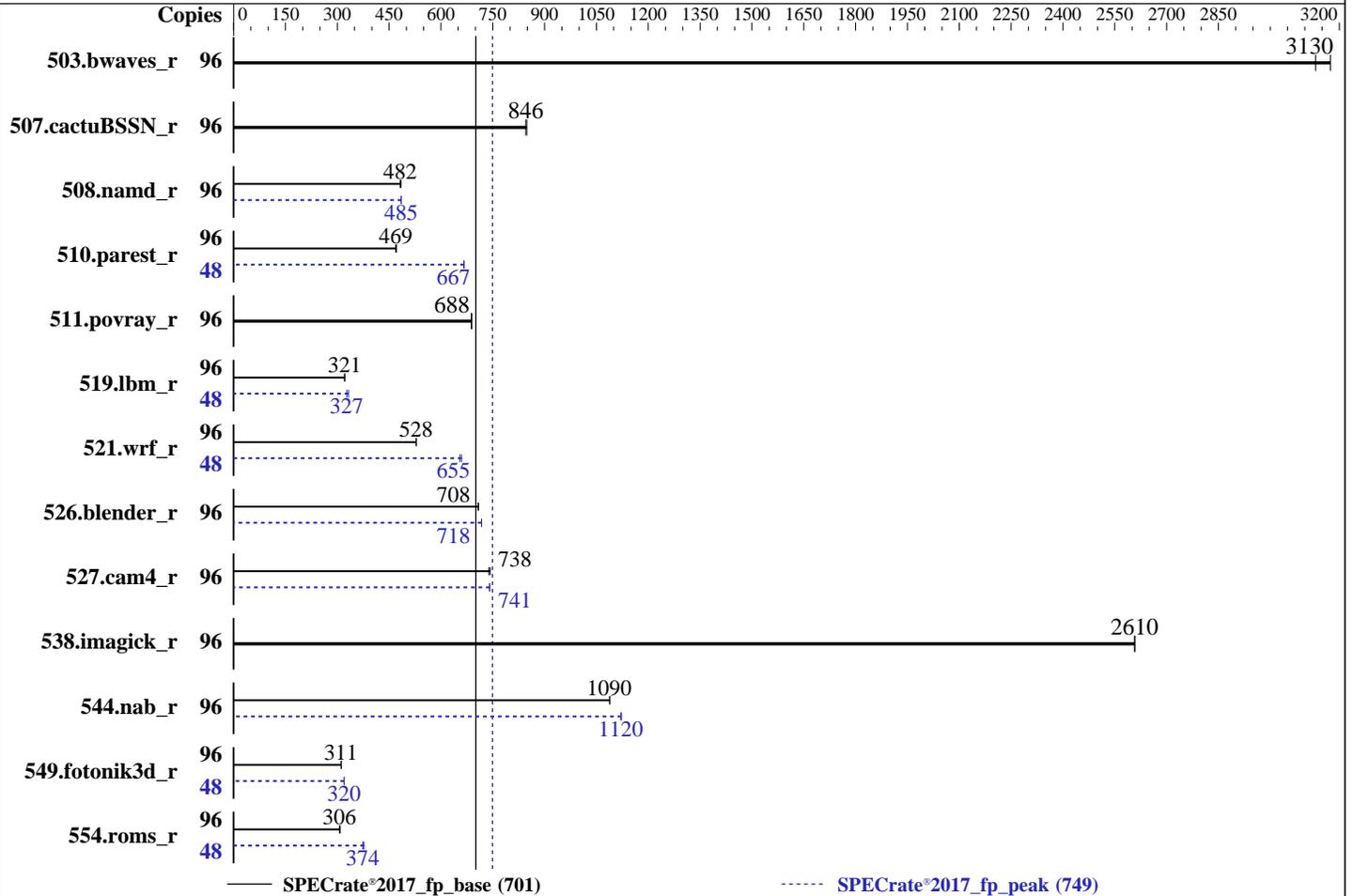
Test Date: Jan-2026

Test Sponsor: Dell Inc.

Hardware Availability: Nov-2025

Tested by: Dell Inc.

Software Availability: Dec-2025



### Hardware

CPU Name: AMD EPYC 9455  
 Max MHz: 4400  
 Nominal: 3150  
 Enabled: 48 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB shared / 6 cores  
 Other: None  
 Memory: 768 GB (12 x 64 GB 2Rx4 PC5-6400B-R, running at 5200)  
 Storage: 70 GB on tmpfs  
 Other: CPU Cooling: Air

### Software

OS: Ubuntu 24.04.3 LTS  
 6.8.0-90-generic  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Version 1.5.3 released Oct-2025  
 File System: tmpfs  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jan-2026  
Hardware Availability: Nov-2025  
Software Availability: Dec-2025

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	96	303	3170	<b>307</b>	<b>3130</b>			96	303	3170	<b>307</b>	<b>3130</b>		
507.cactuBSSN_r	96	<b>144</b>	<b>846</b>	143	849			96	<b>144</b>	<b>846</b>	143	849		
508.namd_r	96	<b>189</b>	<b>482</b>	188	484			96	<b>188</b>	<b>485</b>	188	485		
510.parest_r	96	<b>535</b>	<b>469</b>	533	471			48	<b>188</b>	<b>667</b>	188	667		
511.povray_r	96	325	689	<b>326</b>	<b>688</b>			96	325	689	<b>326</b>	<b>688</b>		
519.lbm_r	96	<b>315</b>	<b>321</b>	315	321			48	<b>155</b>	<b>327</b>	152	332		
521.wrf_r	96	406	529	<b>407</b>	<b>528</b>			48	163	660	<b>164</b>	<b>655</b>		
526.blender_r	96	206	709	<b>207</b>	<b>708</b>			96	204	718	<b>204</b>	<b>718</b>		
527.cam4_r	96	<b>227</b>	<b>738</b>	226	743			96	227	741	<b>227</b>	<b>741</b>		
538.imagick_r	96	91.5	2610	<b>91.6</b>	<b>2610</b>			96	91.5	2610	<b>91.6</b>	<b>2610</b>		
544.nab_r	96	148	1090	<b>149</b>	<b>1090</b>			96	<b>144</b>	<b>1120</b>	144	1120		
549.fotonik3d_r	96	1201	312	<b>1203</b>	<b>311</b>			48	<b>585</b>	<b>320</b>	585	320		
554.roms_r	96	495	308	<b>498</b>	<b>306</b>			48	<b>204</b>	<b>374</b>	202	378		

SPECrate®2017\_fp\_base = **701**

SPECrate®2017\_fp\_peak = **749**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =  
    "/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4/amd_rate_aocc500_znver5_A_lib/lib32:"  
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

Benchmark run from a 70 GB ramdisk created with the cmd: "mount -t tmpfs -o size=70G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS Settings:

Virtualization Technology : Disabled  
NUMA Nodes Per Socket : 4

System Profile : Custom

C-States : Disabled

PCI ASPM L1 Link Power Management : Disabled

Periodic Directory Rinse Tuning : Blended

Determinism Control : Manual

Determinism Slider : Power Determinism

Optimizer Mode : Enabled

Adaptive Allocation : Enabled

Dram Refresh Delay : Performance

DIMM Self Healing -

on Uncorrectable Memory Error : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5\_A1.4/bin/sysinfo

Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

running on SLR6711-R6715 Fri Jan 16 07:55:29 2026

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

## SPECrate®2017\_fp\_base = 701

### PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

## SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jan-2026

**Hardware Availability:** Nov-2025

**Software Availability:** Dec-2025

## Platform Notes (Continued)

- 10. who -r
- 11. Systemd service manager version: systemd 255 (255.4-1ubuntu8.10)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
-----
1. uname -a
Linux SLR6711-R6715 6.8.0-90-generic #91-Ubuntu SMP PREEMPT_DYNAMIC Tue Nov 18 14:14:30 UTC 2025 x86_64
x86_64 x86_64 GNU/Linux
```

```
-----
2. w
07:55:29 up 1 day, 14:31, 2 users, load average: 0.07, 0.03, 0.00
USER      TTY      FROM            LOGIN@   IDLE   JCPU   PCPU   WHAT
root      -        10.17.184.161   07:44   38:31m 0.00s  0.05s  sshd: root@notty
root      tty1    -               Wed17   22.00s  1.27s  0.35s  /bin/bash ./amd_rate_aocc500_znver5_A1.sh
```

```
-----
3. Username
From environment variable $USER: root
```

```
-----
4. ulimit -a
time(seconds)      unlimited
file(blocks)       unlimited
data(kbytes)       unlimited
stack(kbytes)      unlimited
coredump(blocks)   0
memory(kbytes)     unlimited
locked memory(kbytes) 2097152
process            3092894
nofiles            1024
vmemory(kbytes)    unlimited
locks              unlimited
rtprio            0
```

```
-----
5. sysinfo process ancestry
/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_rate.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/dell-run-main.sh rate
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh rate --define DL-VERS=6.4_T17 --output_format
html,pdf,txt
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt fprate
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

## SPECrate®2017\_fp\_base = 701

### PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

## SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jan-2026

**Hardware Availability:** Nov-2025

**Software Availability:** Dec-2025

## Platform Notes (Continued)

```

runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-VERS=6.4_T17 --output_format html,pdf,txt --nopower --runmode rate --tune base:peak --size
test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.fprate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4

```

### 6. /proc/cpuinfo

```

model name      : AMD EPYC 9455 48-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model         : 2
stepping      : 1
microcode     : 0xb00215a
bugs          : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size     : 192 4K pages
cpu cores     : 48
siblings      : 96
1 physical ids (chips)
96 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29,32-37,40-45,48-53,56-61
physical id 0: apicids 0-11,16-27,32-43,48-59,64-75,80-91,96-107,112-123

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:  0-95
Vendor ID:             AuthenticAMD
BIOS Vendor ID:       AMD
Model name:            AMD EPYC 9455 48-Core Processor
BIOS Model name:      AMD EPYC 9455 48-Core Processor           CPU @ 3.1GHz
BIOS CPU family:      107
CPU family:            26
Model:                 2
Thread(s) per core:   2
Core(s) per socket:   48
Socket(s):             1
Stepping:              1
Frequency boost:      enabled
CPU(s) scaling MHz:   56%
CPU max MHz:           3150.0000
CPU min MHz:           1500.0000
BogoMIPS:              6291.58
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtsmp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
extd_apicid aperfmperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx fl6c rdrand lahf_lm
cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

## SPECrate®2017\_fp\_base = 701

### PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

## SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Jan-2026  
**Hardware Availability:** Nov-2025  
**Software Availability:** Dec-2025

### Platform Notes (Continued)

ibrs ibpb stibp ibrs\_enhanced vmcall fsgsbase tsc\_adjust bmi1 avx2 smep bmi2 invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local user\_shstk avx\_vnni avx512\_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd\_ppin cppc amd\_ibpb\_ret arat npt lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold avic v\_vmsave\_vmload vgif x2avic v\_spec\_ctrl vnmi avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid bus\_lock\_detect movdiri movdir64b overflow\_recov succor smca avx512\_vp2intersect flush\_lld debug\_swap

L1d cache: 2.3 MiB (48 instances)  
L1i cache: 1.5 MiB (48 instances)  
L2 cache: 48 MiB (48 instances)  
L3 cache: 256 MiB (8 instances)  
NUMA node(s): 4  
NUMA node0 CPU(s): 0-11,48-59  
NUMA node1 CPU(s): 12-23,60-71  
NUMA node2 CPU(s): 24-35,72-83  
NUMA node3 CPU(s): 36-47,84-95  
Vulnerability Gather data sampling: Not affected  
Vulnerability Itlb multihit: Not affected  
Vulnerability L1tf: Not affected  
Vulnerability Mds: Not affected  
Vulnerability Meltdown: Not affected  
Vulnerability Mmio stale data: Not affected  
Vulnerability Reg file data sampling: Not affected  
Vulnerability Retbleed: Not affected  
Vulnerability Spec rstack overflow: Not affected  
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl  
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected  
Vulnerability Srbds: Not affected  
Vulnerability Tsx async abort: Not affected  
Vulnerability Vmscape: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	2.3M	12	Data	1	64	1	64
L1i	32K	1.5M	8	Instruction	1	64	1	64
L2	1M	48M	16	Unified	2	1024	1	64
L3	32M	256M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)  
node 0 cpus: 0-11,48-59  
node 0 size: 192817 MB  
node 0 free: 192162 MB  
node 1 cpus: 12-23,60-71  
node 1 size: 193525 MB  
node 1 free: 188959 MB  
node 2 cpus: 24-35,72-83  
node 2 size: 193482 MB  
node 2 free: 192928 MB  
node 3 cpus: 36-47,84-95  
node 3 size: 193476 MB

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Jan-2026  
**Hardware Availability:** Nov-2025  
**Software Availability:** Dec-2025

### Platform Notes (Continued)

```
node 3 free: 192947 MB
node distances:
node  0  1  2  3
  0:  10  12  12  12
  1:  12  10  12  12
  2:  12  12  10  12
  3:  12  12  12  10
```

```
-----
9. /proc/meminfo
MemTotal:      791860676 kB
```

```
-----
10. who -r
run-level 5 Jan 14 17:23
```

```
-----
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.10)
Default Target Status
graphical          running
```

```
-----
12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled ModemManager apparmor appport blk-availability cloud-config cloud-final cloud-init cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb ssh sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
enabled-runtime netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled console-getty debug-shell iscsid nftables rsync serial-getty@ systemd-boot-check-no-failures systemd-confext systemd-network-generator systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext systemd-time-wait-sync upower
indirect systemd-sysupdate systemd-sysupdate-reboot uidd
masked cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-6.8.0-90-generic
root=UUID=ed5afdl0-1d78-4ebf-ae8d-321c92452c79
ro
```

```
-----
14. cpupower frequency-info
analyzing CPU 54:
current policy: frequency should be within 1.50 GHz and 3.15 GHz.
The governor "schedutil" may decide which speed to use
within this range.

boost state support:
Supported: yes
Active: yes
Boost States: 0
Total States: 3
Pstate-P0: 3150MHz
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

## Dell Inc.

## SPECrate®2017\_fp\_base = 701

### PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

## SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573  
**Test Sponsor:** Dell Inc.  
**Tested by:** Dell Inc.

**Test Date:** Jan-2026  
**Hardware Availability:** Nov-2025  
**Software Availability:** Dec-2025

### Platform Notes (Continued)

```

15. sysctl
    kernel.numa_balancing      1
    kernel.randomize_va_space  0
    vm.compaction_proactiveness 20
    vm.dirty_background_bytes  0
    vm.dirty_background_ratio  10
    vm.dirty_bytes             0
    vm.dirty_expire_centisecs  3000
    vm.dirty_ratio             8
    vm.dirty_writeback_centisecs 500
    vm.dirtytime_expire_seconds 43200
    vm.extfrag_threshold       500
    vm.min_unmapped_ratio      1
    vm.nr_hugepages            0
    vm.nr_hugepages_mempolicy  0
    vm.nr_overcommit_hugepages 0
    vm.swappiness              1
    vm.watermark_boost_factor  15000
    vm.watermark_scale_factor   10
    vm.zone_reclaim_mode       1

-----
16. /sys/kernel/mm/transparent_hugepage
    defrag      [always] defer defer+madvise madvise never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared        256
    max_ptes_swap          64
    pages_to_scan          4096
    scan_sleep_millisecs  10000

-----
18. OS release
    From /etc/*-release /etc/*-version
    os-release Ubuntu 24.04.3 LTS

-----
19. Disk information
    SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1.4
    Filesystem      Type  Size  Used Avail Use% Mounted on
    tmpfs           tmpfs 70G  3.3G  67G   5% /mnt/ramdisk

-----
20. /sys/devices/virtual/dmi/id
    Vendor:      Dell Inc.
    Product:     PowerEdge R6715
    Product Family: PowerEdge
    Serial:      SLR6711

-----
21. dmidecode
    Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
    The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

12x 802C0000802C MTC40F2046S1RC64BD2 64 GB 2 rank 6400, configured at 5200

### 22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.5.3  
BIOS Date: 10/29/2025  
BIOS Revision: 1.5

## Compiler Version Notes

C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

C++, C, Fortran | 507.cactuBSSN\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Compiler Version Notes (Continued)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

-----  
Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

-----  
Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Base Portability Flags

```

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

C++ benchmarks:

```

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

Fortran benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-lflang -ldl

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlIBM -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamdlIBM -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamdlIBM -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Base Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Peak Optimization Flags

C benchmarks:

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick\_r: basepeak = yes

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Peak Optimization Flags (Continued)

549.fotonik3d\_r (continued):

```
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -fvector-transform  
-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang
```

```
554.roms_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm  
-lamdalloc -ldl -lflang
```

Benchmarks using both Fortran and C:

```
521.wrf_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-fremap-arrays -fstrip-mining  
-mllvm -inline-threshold=1000  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

```
527.cam4_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang
```

Benchmarks using both C and C++:

```
511.povray_r: basepeak = yes
```

```
526.blender_r: -m64 -std=c++14  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2026

Hardware Availability: Nov-2025

Software Availability: Dec-2025

## Peak Optimization Flags (Continued)

526.blender\_r (continued):

```
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt
-mllvm -unroll-threshold=100 -lamdlibm -lamdalloc -ldl
```

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2026 Standard Performance Evaluation Corporation

Dell Inc.

SPECrate®2017\_fp\_base = 701

PowerEdge R6715 (AMD EPYC 9455 48-Core Processor)

SPECrate®2017\_fp\_peak = 749

**CPU2017 License:** 6573

**Test Sponsor:** Dell Inc.

**Tested by:** Dell Inc.

**Test Date:** Jan-2026

**Hardware Availability:** Nov-2025

**Software Availability:** Dec-2025

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2026-01-16 02:55:28-0500.

Report generated on 2026-02-25 10:35:16 by CPU2017 PDF formatter v6716.

Originally published on 2026-02-25.