SPEChpc™ 2021 Small Result

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xFusion

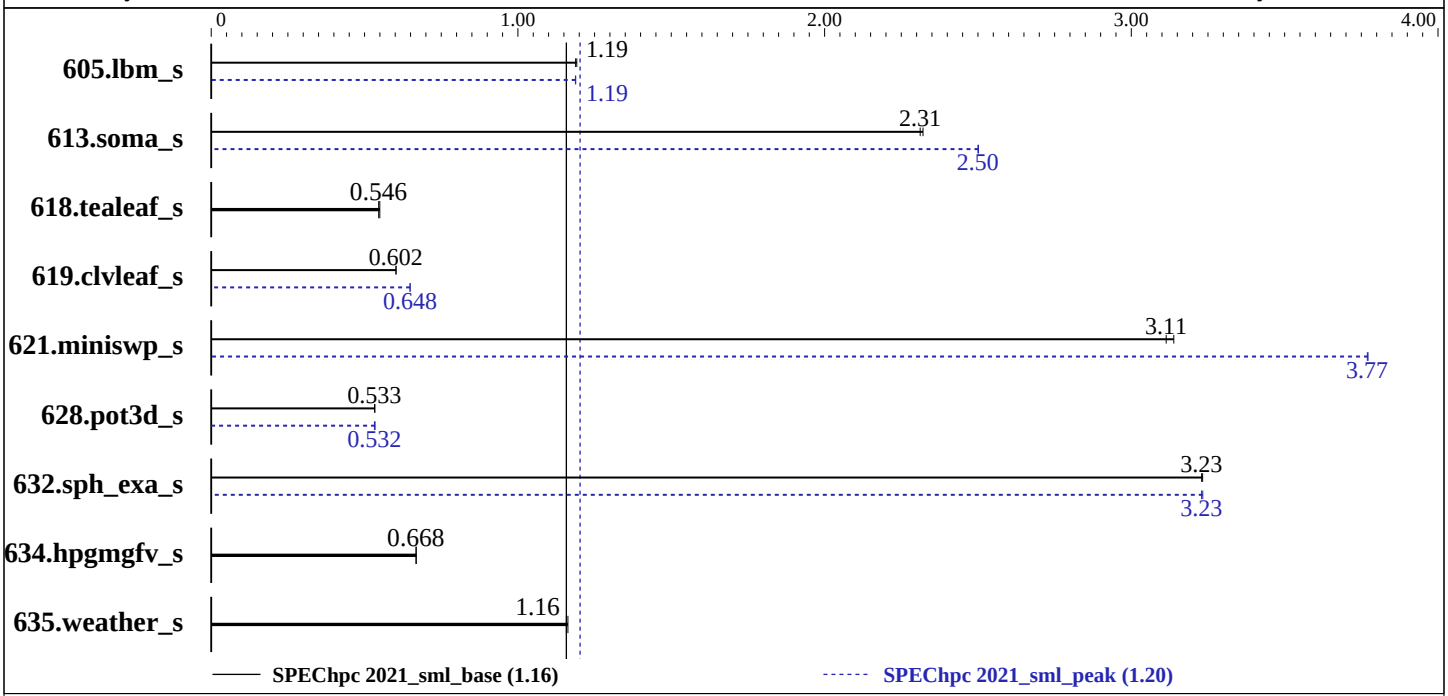
FusionServer 2288H V6 (Intel Xeon Platinum 8380)

SPEChpc 2021_sml_base = 1.16

SPEChpc 2021_sml_peak = 1.20

hpc2021 License: 6488
Test Sponsor: xFusion
Tested by: xFusion

Test Date: Jul-2022
Hardware Availability: Apr-2021
Software Availability: Oct-2021



Results Table

Benchmark	Base									Peak								
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
605.lbm_s	OMP	32	10	1300	1.19	**1305**	**1.19**			OMP	16	20	**1305**	**1.19**	1304	1.19		
613.soma_s	OMP	32	10	**692**	**2.31**	689	2.32			OMP	4	80	**640**	**2.50**	640	2.50		
618.tealeaf_s	OMP	32	10	**3754**	**0.546**	3729	0.550			OMP	32	10	**3754**	**0.546**	3729	0.550		
619.clvleaf_s	OMP	32	10	2738	0.603	**2739**	**0.602**			OMP	16	20	2544	0.649	**2545**	**0.648**		
621.miniswp_s	OMP	32	10	350	3.14	**353**	**3.11**			OMP	4	80	**292**	**3.77**	292	3.77		
628.pot3d_s	OMP	32	10	3139	0.534	**3144**	**0.533**			OMP	32	10	**3147**	**0.532**	3135	0.534		
632.sph_exa_s	OMP	32	10	**712**	**3.23**	712	3.23			OMP	32	10	**712**	**3.23**	711	3.23		
634.hpgmgfv_s	OMP	32	10	1458	0.669	**1460**	**0.668**			OMP	32	10	1458	0.669	**1460**	**0.668**		
635.weather_s	OMP	32	10	**2245**	**1.16**	2236	1.16			OMP	32	10	**2245**	**1.16**	2236	1.16		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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Hardware Summary

Type of System:	Homogeneous Cluster
Compute Node:	Fusionserver 2288H V6
Interconnect:	Mellanox HDR
Compute Nodes Used:	2
Total Chips:	4
Total Cores:	160
Total Threads:	320
Total Memory:	512 GB
Max. Peak Threads:	80

Software Summary

Compiler:	Intel oneAPI Compiler 2021.4.0
MPI Library:	Intel MPI Library for Linux* OS, Version 2021.4.0 Build 20210831
Other MPI Info:	--
Other Software:	--
Base Parallel Model:	OMP
Base Ranks Run:	32
Base Threads Run:	10
Peak Parallel Models:	OMP
Minimum Peak Ranks:	4
Maximum Peak Ranks:	32
Max. Peak Threads:	80
Min. Peak Threads:	10

Node Description: Fusionserver 2288H V6

Hardware

Number of nodes:	2
Uses of the node:	Compute
Vendor:	xFusion
Model:	FusionServer 2288H V6
CPU Name:	Intel Xeon Platinum 8380
CPU(s) orderable:	1, 2 chips
Chips enabled:	2
Cores enabled:	80
Cores per chip:	40
Threads per core:	2
CPU Characteristics:	Turbo Boost Technology up to 3.4 GHz
CPU MHz:	2300
Primary Cache:	32 KB I + 48 KB D on chip per core
Secondary Cache:	1.25 MB I+D on chip per core
L3 Cache:	60 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx8 PC4-3200R)
Disk Subsystem:	2 x 480 GB SATA 2.5" SSD (RAID 1)
Other Hardware:	None
Accel Count:	None
Accel Model:	--
Accel Vendor:	None
Accel Type:	None
Accel Connection:	None
Accel ECC enabled:	None
Accel Description:	None
Adapter:	MCX653105A-EFAT
Number of Adapters:	1
Slot Type:	PCI-Express 4.0 x16
Data Rate:	100 Gb/s
Ports Used:	1

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Software

Accelerator Driver:	--
Adapter:	MCX653105A-EFAT
Adapter Driver:	5.4-3.1.0
Adapter Firmware:	20.32.1010
Operating System:	CentOS Linux release 8.2.2004 4.18.0-193.el8.x86_644
Local File System:	xfs
Shared File System:	NFS
System State:	Multi-user, run level 3
Other Software:	None

Node Description: Fusionserver 2288H V6

Hardware (Continued)

Interconnect Type: Mellanox HDR

Interconnect Description: Mellanox HDR

Hardware

Vendor:	Mellanox
Model:	Mellanox HDR
Switch Model:	Mellanox MQM8790-HS2F InfiniBand Switch
Number of Switches:	1
Number of Ports:	40
Data Rate:	200 Gbit/s
Firmware:	--
Topology:	Mesh
Primary Use:	MPI Traffic

Software

: --

Submit Notes

```
The config file option 'submit' was used.
export LD_PRELOAD="/usr/lib64/libhugetlbfs.so $LD_PRELOAD"
export OMP_PROC_BIND=true
mpiexec.hydra -bootstrap ssh -hostfile $[top]/2node --bind-to core -np $ranks -ppn $ppn -genv OMP_NUM_THREADS=$threads $command
```

Compiler Version Notes

```
==============================================================================
 CC  605.lbm_s(base, peak) 613.soma_s(base, peak) 618.tealeaf_s(base, peak)
     621.miniswp_s(base, peak) 634.hpgmgfv_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2021.4.0 (2021.4.0.20210924)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/opt/compiler/oneapi/2021.4.0/compiler/2021.4.0/linux/bin
------------------------------------------------------------------------------

==============================================================================
 CXXC 632.sph_exa_s(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2021.4.0 (2021.4.0.20210924)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /home/opt/compiler/oneapi/2021.4.0/compiler/2021.4.0/linux/bin
```

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Compiler Version Notes (Continued)

```
------------------------------------------------------------------------------

==============================================================================
 FC  619.clvleaf_s(base, peak) 628.pot3d_s(base, peak) 635.weather_s(base,
      peak)
------------------------------------------------------------------------------
ifx (IFORT) 2021.4.0 Beta 20210924
Copyright (C) 1985-2021 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`mpiicc -cc=icx -lstdc++`(*)

C++ benchmarks:
`mpiicpc -cxx=icx -lstdc++`(*)

Fortran benchmarks:
`mpiifort -fc=ifx -lstdc++`(*)

(*) Indicates a compiler flag that was found in a non-compiler variable.

Base Portability Flags

613.soma_s: `-DSPEC_NO_VAR_ARRAY_REDUCE`

Base Optimization Flags

C benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp -ansi-alias`

C++ benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp -ansi-alias`

Fortran benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp -nostandard-realloc-lhs -align array64byte`

Peak Compiler Invocation

C benchmarks:
`mpiicc -cc=icx -lstdc++(*)`

C++ benchmarks:
`mpiicpc -cxx=icx -lstdc++(*)`

Fortran benchmarks:
`mpiifort -fc=ifx -lstdc++(*)`

(*) Indicates a compiler flag that was found in a non-compiler variable.

Peak Portability Flags

613.soma_s: `-DSPEC_NO_VAR_ARRAY_REDUCE`

Peak Optimization Flags

C benchmarks:

605.lbm_s: `-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp -ansi-alias`

613.soma_s: `Same as 605.lbm_s`

618.tealeaf_s: `basepeak = yes`

621.miniswp_s: `Same as 605.lbm_s`

634.hpgmgfv_s: `basepeak = yes`

C++ benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -ffast-math -fiopenmp -ansi-alias`

Fortran benchmarks:

619.clvleaf_s: `-Ofast -ipo -xCORE-AVX512 -mllvm -hir-nontemporal-cacheline-count=0 -fiopenmp -nostandard-realloc-lhs -align array64byte`

628.pot3d_s: `-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -fiopenmp -nostandard-realloc-lhs -align array64byte`

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Peak Optimization Flags (Continued)

635.weather_s: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/hpc2021/flags/Intel-oneAPI-icx2021-official-linux64.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/hpc2021/flags/Intel-oneAPI-icx2021-official-linux64.xml`

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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