SPEChpc™ 2021 Tiny Result

Copyright 2021-2024 Standard Performance Evaluation Corporation

Intel

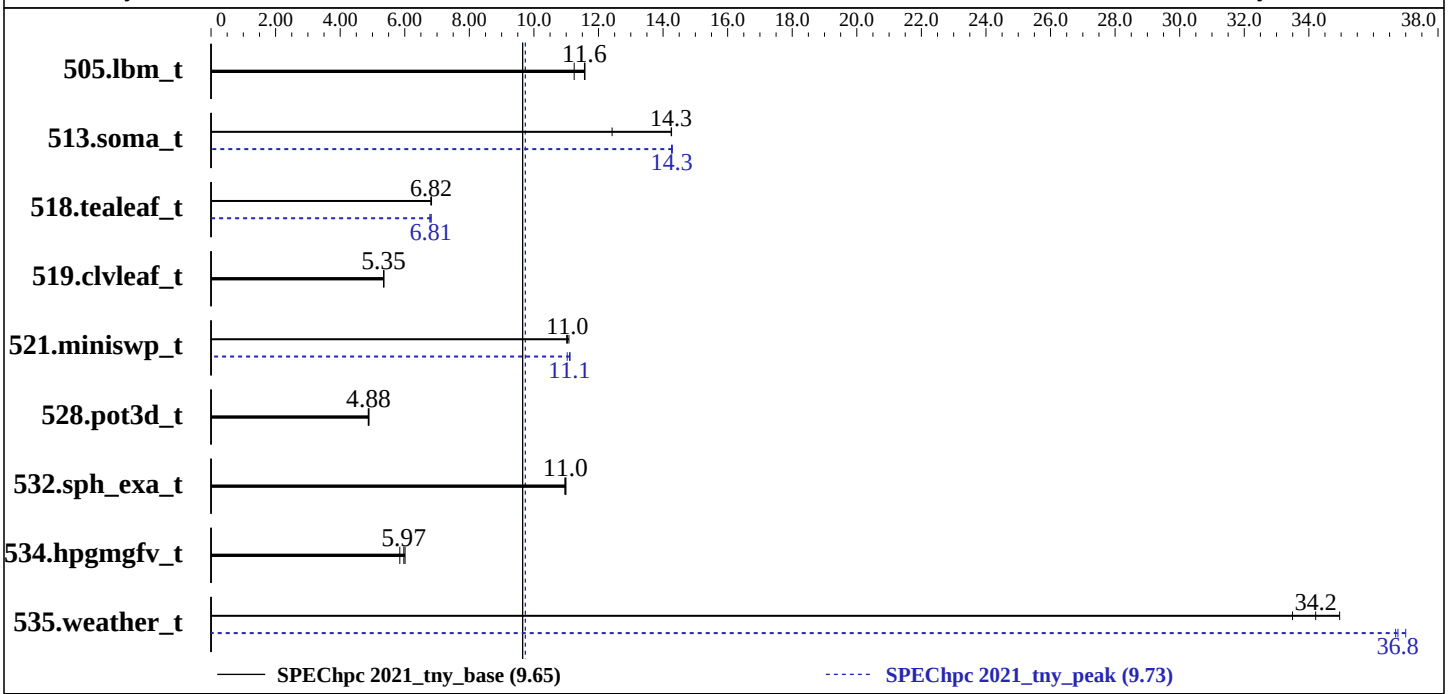
Endeavour: Intel Server D50DNP1SBB (Intel Xeon Platinum 8592+)

SPEChpc 2021_tny_base = 9.65

SPEChpc 2021_tny_peak = 9.73

hpc2021 License: 13
Test Sponsor: Intel
Tested by: Intel

Test Date: Apr-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024



Results Table

Benchmark	Base									Peak								
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	OMP	4	32	**195**	**11.6**	200	11.2	194	11.6	OMP	4	32	**195**	**11.6**	200	11.2	194	11.6
513.soma_t	OMP	4	32	259	14.3	298	12.4	**260**	**14.3**	OMP	4	32	259	14.3	**259**	**14.3**	259	14.3
518.tealeaf_t	OMP	4	32	**242**	**6.82**	242	6.83	242	6.81	OMP	4	32	243	6.78	**242**	**6.81**	242	6.82
519.clvleaf_t	OMP	4	32	**308**	**5.35**	309	5.35	308	5.35	OMP	4	32	**308**	**5.35**	309	5.35	308	5.35
521.miniswp_t	OMP	4	32	144	11.1	**145**	**11.0**	145	11.0	OMP	4	32	145	11.0	144	11.1	**144**	**11.1**
528.pot3d_t	OMP	4	32	**436**	**4.88**	434	4.89	436	4.87	OMP	4	32	**436**	**4.88**	434	4.89	436	4.87
532.sph_exa_t	OMP	4	32	178	11.0	177	11.0	**178**	**11.0**	OMP	4	32	178	11.0	177	11.0	**178**	**11.0**
534.hpgmgfv_t	OMP	4	32	201	5.85	**197**	**5.97**	196	6.01	OMP	4	32	201	5.85	**197**	**5.97**	196	6.01
535.weather_t	OMP	4	32	92.3	35.0	**94.3**	**34.2**	96.3	33.5	OMP	64	2	87.9	36.7	87.2	37.0	**87.7**	**36.8**
SPEChpc 2021_tny_base =					**9.65**													
SPEChpc 2021_tny_peak =					**9.73**													

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

SPEChpc™ 2021 Tiny Result

Copyright 2021-2024 Standard Performance Evaluation Corporation

Intel

Endeavour: Intel Server D50DNP1SBB (Intel Xeon Platinum 8592+)

SPEChpc 2021_tny_base = 9.65

SPEChpc 2021_tny_peak = 9.73

hpc2021 License: 13
Test Sponsor: Intel
Tested by: Intel

Test Date: Apr-2024
Hardware Availability: Dec-2023
Software Availability: Mar-2024

Hardware Summary

Type of System:	Homogenous Cluster
Compute Node:	Intel Server D50DNP1SBB (Xeon 8592+)
Interconnect:	Mellanox HDR
Compute Nodes Used:	1
Total Chips:	2
Total Cores:	128
Total Threads:	256
Total Memory:	512 GB
Max. Peak Threads:	32

Software Summary

Compiler:	Intel oneAPI Compiler 2024.1.0
MPI Library:	Intel MPI Library 2021.12 for Linux OS
Other MPI Info:	None
Other Software:	None
Base Parallel Model:	OMP
Base Ranks Run:	4
Base Threads Run:	32
Peak Parallel Models:	OMP
Minimum Peak Ranks:	4
Maximum Peak Ranks:	64
Max. Peak Threads:	32
Min. Peak Threads:	2

Node Description: Intel Server D50DNP1SBB (Xeon 8592+)

Hardware

Number of nodes:	1
Uses of the node:	Compute
Vendor:	Intel
Model:	Intel Server D50DNP1SBB (Xeon 8592+)
CPU Name:	Intel Xeon Platinum 8592+
CPU(s) orderable:	1, 2 chips
Chips enabled:	2
Cores enabled:	128
Cores per chip:	64
Threads per core:	2
CPU Characteristics:	Turbo Boost Technology up to 3.9 GHz
CPU MHz:	1900
Primary Cache:	32 KB I + 48 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	320 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (16 x 32 GB 2Rx8 PC5-5600B-R)
Disk Subsystem:	1 x 1 TB NVMe U.2 2.5" SSD
Other Hardware:	None
Accel Count:	None
Accel Model:	None
Accel Vendor:	None
Accel Type:	None
Accel Connection:	None
Accel ECC enabled:	None
Accel Description:	None
Adapter:	Mellanox ConnectX-6 HDR
Number of Adapters:	1
Slot Type:	PCI-Express 4.0 x16
Data Rate:	200Gbit/s
Ports Used:	1
Interconnect Type:	Mellanox HDR

Software

Accelerator Driver:	None
Adapter:	Mellanox ConnectX-6 HDR
Adapter Driver:	23.04-0.5.3
Adapter Firmware:	20.37.1014
Operating System:	Rocky Linux 8.8 (Green Obsidian) 4.18.0-477.15.1.el8_8.x86_64
Local File System:	xfs
Shared File System:	PANASAS FS
System State:	Run level 5
Other Software:	None

Interconnect Description: Mellanox HDR

Hardware

Vendor:	Mellanox
Model:	Mellanox HDR
Switch Model:	Mellanox MQM8790-HS2F Quantum HDR InfiniBand Switch
Number of Switches:	18
Number of Ports:	40
Data Rate:	200 Gbit/s
Firmware:	20.36.1010
Topology:	Fat-tree
Primary Use:	MPI Traffic

Software

: --

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
The PANASAS filesystem as described on this result page was formerly
generally available.  At the time of this publication, it may
not be shipping, and/or may not be supported, and/or may fail
to meet other tests of General Availability described in the
SPEC HPG Policy document, http://www.spec.org/hpg/policy.html
```

Compiler Version Notes

```
==============================================================================
 CXXC 532.sph_exa_t(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2024.1.0 (2024.1.0.20240308)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler
Configuration file:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler/../icpx.cfg
------------------------------------------------------------------------------

==============================================================================
 CC  505.lbm_t(base, peak) 513.soma_t(base, peak) 518.tealeaf_t(base, peak)
      521.miniswp_t(base, peak) 534.hpgmgfv_t(base, peak)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2024.1.0 (2024.1.0.20240308)
Target: x86_64-unknown-linux-gnu
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Thread model: posix
InstalledDir:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler
Configuration file:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler/../icx.cfg
------------------------------------------------------------------------------

==============================================================================
 FC  519.clvleaf_t(base, peak) 528.pot3d_t(base, peak) 535.weather_t(base,
     peak)
------------------------------------------------------------------------------
ifx (IFX) 2024.1.0 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`mpiicc -cc=icx`

C++ benchmarks:
`mpiicpc -cxx=icpx`

Fortran benchmarks:
`mpiifort -fc=ifx`

Base Portability Flags

505.lbm_t: `-lstdc++ -std=c++14`
513.soma_t: `-lstdc++ -std=c++14`
518.tealeaf_t: `-lstdc++ -std=c++14`
521.miniswp_t: `-lstdc++ -std=c++14`
534.hpgmgfv_t: `-lstdc++ -std=c++14`

Base Optimization Flags

C benchmarks:
`-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512 -qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto -funroll-loops`

(Continued on next page)

Base Optimization Flags (Continued)

C++ benchmarks:
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops

Fortran benchmarks:
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops -nostandard-realloc-lhs -align array64byte

Base Other Flags

C benchmarks:
-Wno-incompatible-function-pointer-types

Peak Compiler Invocation

C benchmarks:
mpiicc -cc=icx

C++ benchmarks:
mpiicpc -cxx=icpx

Fortran benchmarks:
mpiifort -fc=ifx

Peak Portability Flags

505.lbm_t: -lstdc++ -std=c++14
513.soma_t: -lstdc++ -std=c++14
518.tealeaf_t: -lstdc++ -std=c++14
521.miniswp_t: -lstdc++ -std=c++14
534.hpgmgfv_t: -lstdc++ -std=c++14

Peak Optimization Flags

C benchmarks:

505.lbm_t: `basepeak = yes`

513.soma_t: `-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512 -qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto -funroll-loops`

518.tealeaf_t: `Same as 513.soma_t`

521.miniswp_t: `Same as 513.soma_t`

534.hpgmgfv_t: `basepeak = yes`

C++ benchmarks:

532.sph_exa_t: `basepeak = yes`

Fortran benchmarks:

519.clvleaf_t: `basepeak = yes`

528.pot3d_t: `basepeak = yes`

535.weather_t: `-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512 -qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto -funroll-loops -nostandard-realloc-lhs -align array64byte`

Peak Other Flags

C benchmarks:
`-Wno-incompatible-function-pointer-types`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.xml`

SPEChpc is a trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEChpc2021 v1.1.8 on 2024-04-17 12:37:44-0400.
Report generated on 2024-12-31 10:29:44 by hpc2021 PS formatter v1.0.3.
Originally published on 2024-12-25.