SPEC ACCEL™ OMP Result

Copyright 2015-2019 Standard Performance Evaluation Corporation

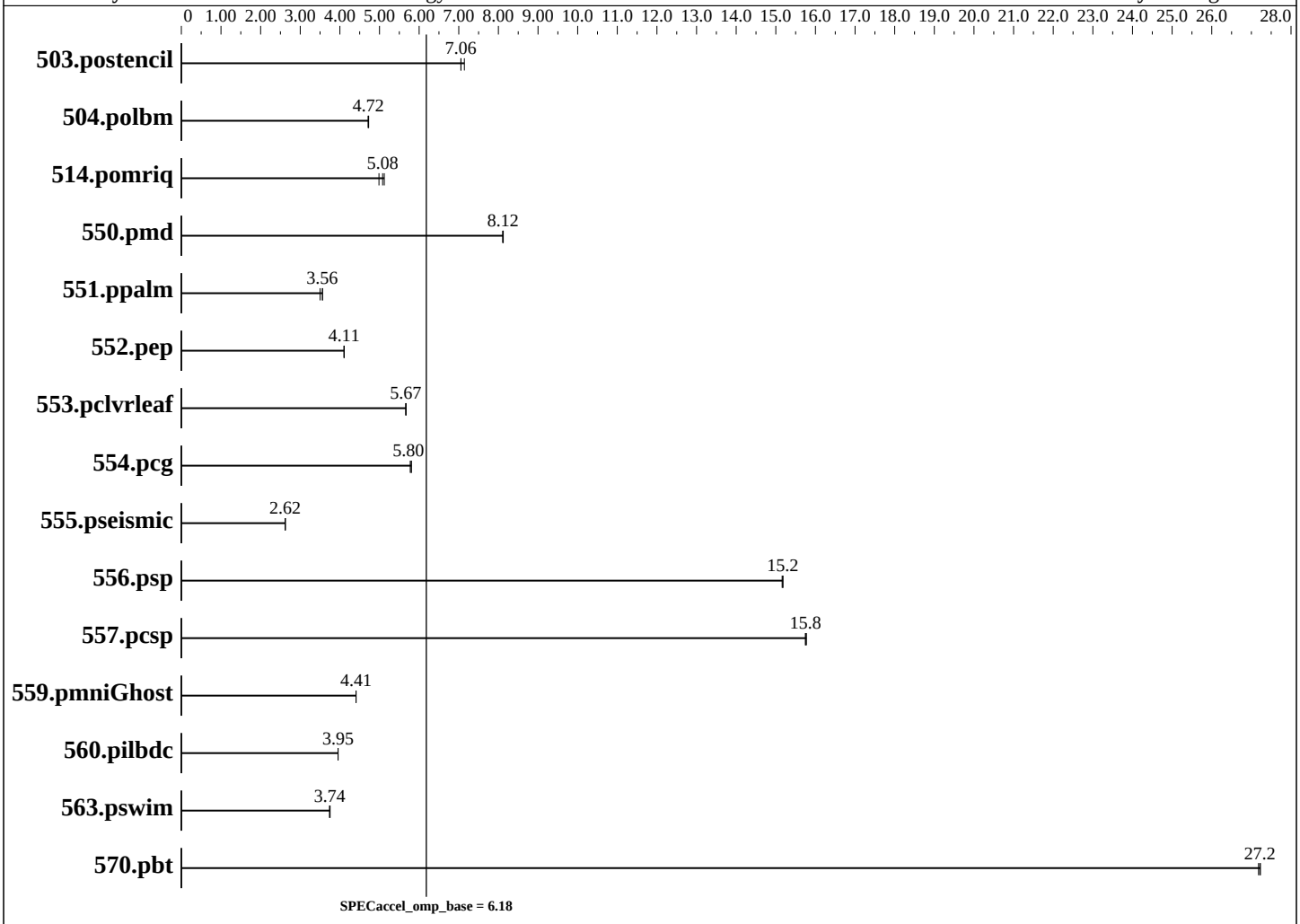
Lenovo Global Technology
EPYC 7742 CPU
ThinkSystem SR655

SPECaccel_omp_peak = Not Run

SPECaccel_omp_base = 6.18

ACCEL license: 16
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Aug-2019
Hardware Availability: Aug-2019
Software Availability: Aug-2019



Hardware

CPU Name:	AMD EPYC 7742 CPU
CPU Characteristics:	Turbo up to 3.4 GHz
CPU MHz:	2250
CPU MHz Maximum:	3400
FPU:	Integrated
CPU(s) enabled:	64 cores, 1 chip, 64 cores/chip, 2 threads/core
CPU(s) orderable:	1 Chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	256 MB I+D on chip per chip 16 MB shared / 4 cores

Continued on next page

Accelerator

Accel Model Name:	EPYC 7742 CPU
Accel Vendor:	AMD
Accel Name:	EPYC 7742 CPU
Type of Accel:	CPU
Accel Connection:	Not applicable
Does Accel Use ECC:	yes
Accel Description:	1 x AMD EPYC 7742 CPU
Accel Driver:	Not applicable

SPEC ACCEL OMP Result

Copyright 2015-2019 Standard Performance Evaluation Corporation

Lenovo Global Technology
EPYC 7742 CPU
ThinkSystem SR655

SPECaccel_omp_peak = Not Run

SPECaccel_omp_base = 6.18

ACCEL license: 16
Test sponsor: Lenovo Global Technology
Tested by: Lenovo Global Technology

Test date: Aug-2019
Hardware Availability: Aug-2019
Software Availability: Aug-2019

Hardware (Continued)

Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)
Disk Subsystem: 1 x 480 GB SATA 2.5" SSD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 15 SP1, kernel 4.12.14-195-default
Compiler: Intel C/C++/Fortran 19.0 Update 4 for Linux Version 19.0.4 Build 20190416
File System: xfs
System State: Run-level 3
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.postencil	**15.4**	**7.06**	15.3	7.14	15.5	7.05						
504.polbm	**25.8**	**4.72**	25.8	4.73	25.9	4.71						
514.pomriq	121	5.12	125	4.99	**122**	**5.08**						
550.pmd	29.7	8.11	29.7	8.12	**29.7**	**8.12**						
551.ppalm	**153**	**3.56**	153	3.56	155	3.50						
552.pep	56.3	4.10	**56.2**	**4.11**	56.2	4.11						
553.pclvrleaf	202	5.67	202	5.66	**202**	**5.67**						
554.pcg	57.4	5.80	**57.4**	**5.80**	57.7	5.78						
555.pseismic	108	2.62	**108**	**2.62**	108	2.62						
556.psp	54.0	15.2	53.9	15.2	**53.9**	**15.2**						
557.pcsp	54.5	15.8	**54.5**	**15.8**	54.6	15.7						
559.pmniGhost	**90.1**	**4.41**	90.1	4.41	90.1	4.41						
560.pilbdc	165	3.96	165	3.95	**165**	**3.95**						
563.pswim	**42.5**	**3.74**	42.4	3.75	42.5	3.74						
570.pbt	28.7	27.2	28.6	27.2	**28.7**	**27.2**						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Platform Notes

```
Sysinfo program /home/ACCEL1.2/Docs/sysinfo
$Rev: 6965 $ $Date:: 2015-04-21 #$ c05a7f14b1b1765e3fe1df68447e8a35
running on linux-x8nq Fri Aug  9 02:38:32 2019

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/accel/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : AMD EPYC 7742 64-Core Processor
      1 "physical id"s (chips)
      128 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 64
      siblings  : 128
      physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21
      22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46
      47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63
   cache size : 512 KB

From /proc/meminfo
   MemTotal:       263945344 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="SLES"
      VERSION="15-SP1"
      VERSION_ID="15.1"
      PRETTY_NAME="SUSE Linux Enterprise Server 15 SP1"
      ID="sles"
      ID_LIKE="suse"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:15:sp1"

uname -a:
   Linux linux-x8nq 4.12.14-195-default #1 SMP Tue May 7 10:55:11 UTC 2019
   (8fba516) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 9 00:15

SPEC is set to: /home/ACCEL1.2
   Filesystem     Type   Size  Used Avail Use% Mounted on
   /dev/sda4      btrfs  444G   75G  369G  17% /home
Additional information from dmidecode:
```

Continued on next page

Platform Notes (Continued)

```
      Warning: Use caution when you interpret this section. The 'dmidecode' program
      reads system data which is "intended to allow hardware to be accurately
      determined", but the intent may not be met, as there are frequent changes to
      hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS Lenovo CFE103A 07/04/2019
    Memory:
     8x Samsung M393A4K40DB2-CWE 32 kB 2 rank 3200 MT/s
     8x Unknown Unknown

  (End of data from sysinfo program)
```

General Notes

```
Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication,
that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
```

Base Compiler Invocation

C benchmarks:
icc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

503.postencil: -DSPEC_USE_INNER_SIMD
504.polbm: -DSPEC_USE_INNER_SIMD
514.pomriq: -DSPEC_USE_INNER_SIMD
550.pmd: -DSPEC_USE_INNER_SIMD -80
551.ppalm: -DSPEC_USE_INNER_SIMD
552.pep: -DSPEC_USE_INNER_SIMD
553.pclvrleaf: -DSPEC_USE_INNER_SIMD
554.pcg: -DSPEC_USE_INNER_SIMD
555.pseismic: -DSPEC_USE_INNER_SIMD

Continued on next page

Base Portability Flags (Continued)

556.psp: -DSPEC_USE_INNER_SIMD
557.pcsp: -DSPEC_USE_INNER_SIMD
559.pmniGhost: -DSPEC_USE_INNER_SIMD -nofor-main
560.pilbdc: -DSPEC_USE_INNER_SIMD
563.pswim: -DSPEC_USE_INNER_SIMD
570.pbt: -DSPEC_USE_INNER_SIMD

Base Optimization Flags

C benchmarks:
-O3 -march=core-avx2 -qopenmp -qopenmp-offload=host -no-prec-div
-no-prec-sqrt -ansi-alias -ipo -fp-model fast=2

Fortran benchmarks:
-O3 -march=core-avx2 -qopenmp -qopenmp-offload=host -no-prec-div
-no-prec-sqrt -ansi-alias -ipo -fp-model fast=2

Benchmarks using both Fortran and C:
-O3 -march=core-avx2 -qopenmp -qopenmp-offload=host -no-prec-div
-no-prec-sqrt -ansi-alias -ipo -fp-model fast=2

The flags file that was used to format this result can be browsed at
https://www.spec.org/accel/flags/Intel-icc17.0-linux64.20190828.html

You can also download the XML flags source by saving the following link:
https://www.spec.org/accel/flags/Intel-icc17.0-linux64.20190828.xml

SPEC ACCEL is a trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC ACCEL v1.2.
Report generated on Wed Aug 28 18:18:01 2019 by SPEC ACCEL PS/PDF formatter v1290.
Originally published on 28 August 2019.