SPEChpc™ 2021 Tiny Result

Supermicro

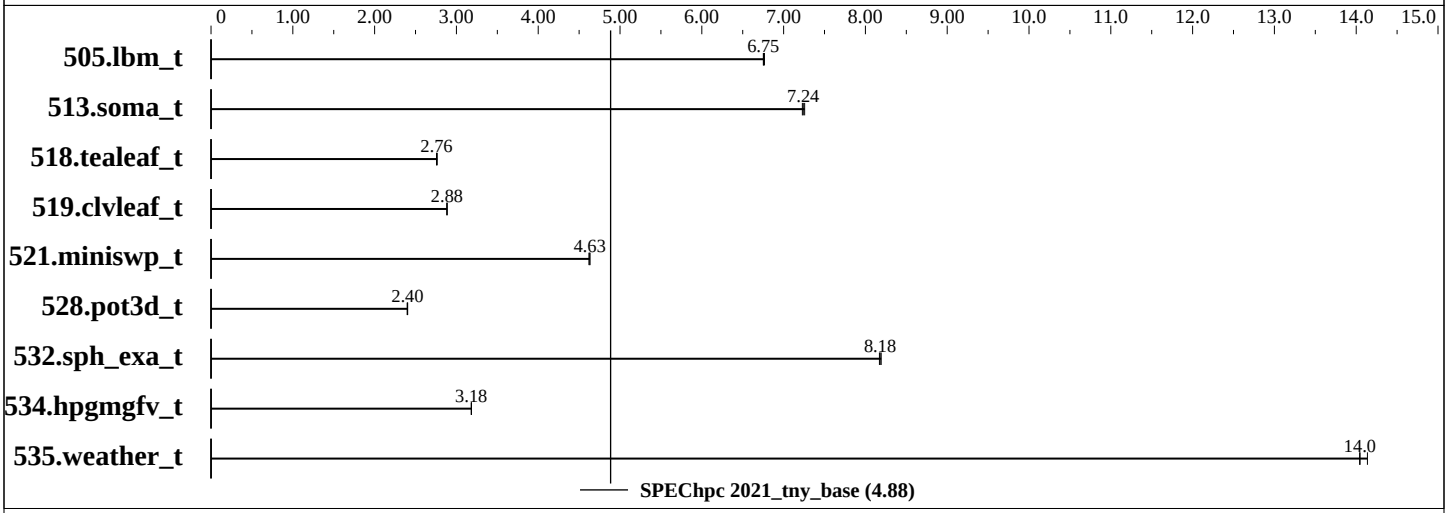
UP SuperServer SYS-521C-NR (Intel Xeon Platinum 8592+)

SPEChpc 2021_tny_base = 4.88

SPEChpc 2021_tny_peak = Not Run

hpc2021 License: 6569
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2023
Hardware Availability: Dec-2023
Software Availability: Oct-2023



Results Table

Benchmark	Base									Peak								
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	MPI	128	1	333	6.77	**333**	**6.75**	333	6.75									
513.soma_t	MPI	128	1	**511**	**7.24**	512	7.23	510	7.26									
518.tealeaf_t	MPI	128	1	**598**	**2.76**	597	2.76	598	2.76									
519.clvleaf_t	MPI	128	1	572	2.88	**572**	**2.88**	572	2.89									
521.miniswp_t	MPI	128	1	**346**	**4.63**	345	4.63	346	4.62									
528.pot3d_t	MPI	128	1	886	2.40	**884**	**2.40**	884	2.40									
532.sph_exa_t	MPI	128	1	239	8.17	**238**	**8.18**	238	8.19									
534.hpgmgfv_t	MPI	128	1	369	3.18	**369**	**3.18**	369	3.18									
535.weather_t	MPI	128	1	228	14.1	**230**	**14.0**	230	14.0									

SPEChpc 2021_tny_base = 4.88

SPEChpc 2021_tny_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Supermicro

UP SuperServer SYS-521C-NR (Intel Xeon Platinum 8592+)

SPEChpc 2021_tny_base = 4.88

SPEChpc 2021_tny_peak = Not Run

hpc2021 License: 6569
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Nov-2023
Hardware Availability: Dec-2023
Software Availability: Oct-2023

Hardware Summary

Type of System:	Homogenous
Compute Node:	UP SuperServer SYS-521C-NR
Interconnect:	None
Compute Nodes Used:	1
Total Chips:	1
Total Cores:	64
Total Threads:	128
Total Memory:	512 GB
Max. Peak Threads:	--

Software Summary

Compiler:	C/C++/Fortran: Version 2024.0.0 of Intel oneAPI Compiler
MPI Library:	Intel MPI Library for Linux* OS, Version 2021.11 Build 20231005
Other MPI Info:	None
Other Software:	None
Base Parallel Model:	MPI
Base Ranks Run:	128
Base Threads Run:	1
Peak Parallel Models:	Not Run
Minimum Peak Ranks:	--
Maximum Peak Ranks:	--
Max. Peak Threads:	--
Min. Peak Threads:	--

Node Description: UP SuperServer SYS-521C-NR

Hardware

Number of nodes:	1
Uses of the node:	compute
Vendor:	Supermicro
Model:	UP SuperServer SYS-521C-NR
CPU Name:	Intel Xeon Platinum 8592+
CPU(s) orderable:	1 chip
Chips enabled:	1
Cores enabled:	64
Cores per chip:	64
Threads per core:	2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.9 GHz
CPU MHz:	1900
Primary Cache:	32 KB I + 48 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	320 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (8 x 64 GB 2Rx4 PC5-5600B-R)
Disk Subsystem:	1 x 240 GB SATA 2.5" SSD
Other Hardware:	None
Accel Count:	0
Accel Model:	None
Accel Vendor:	None
Accel Type:	None
Accel Connection:	None
Accel ECC enabled:	None
Accel Description:	None
Adapter:	None
Number of Adapters:	0
Slot Type:	None
Data Rate:	None

(Continued on next page)

Software

Accelerator Driver:	--
Adapter:	None
Adapter Driver:	None
Adapter Firmware:	None
Operating System:	SUSE Linux Enterprise Server 15 SP5 Kernel 5.14.21-150500.53-default
Local File System:	xfs
Shared File System:	None
System State:	Multi-user, run level 3
Other Software:	None

Node Description: UP SuperServer SYS-521C-NR

Hardware (Continued)

Ports Used: 0
Interconnect Type: None

Interconnect Description: None

Hardware

Vendor: None
Model: None
Switch Model: None
Number of Switches: 0
Number of Ports: 0
Data Rate: None
Firmware: None
Topology: None
Primary Use: None

Software

: --

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
MPI startup command:
  mpirun command (mpiexec.hydra) was used to start MPI jobs.
```

Compiler Version Notes

```
==============================================================================
 CXXC 532.sph_exa_t(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2024.0.0 (2024.0.0.20231017)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/intel/oneapi/compiler/2024.0/bin/compiler
Configuration file: /opt/intel/oneapi/compiler/2024.0/bin/compiler/../icpx.cfg
------------------------------------------------------------------------------

==============================================================================
 CC  505.lbm_t(base) 513.soma_t(base) 518.tealeaf_t(base) 521.miniswp_t(base)
     534.hpgmgfv_t(base)
------------------------------------------------------------------------------
Intel(R) oneAPI DPC++/C++ Compiler 2024.0.0 (2024.0.0.20231017)
```

(Continued on next page)

Compiler Version Notes (Continued)

```
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/intel/oneapi/compiler/2024.0/bin/compiler
Configuration file: /opt/intel/oneapi/compiler/2024.0/bin/compiler/../icx.cfg
------------------------------------------------------------------------------

==============================================================================
 FC  519.clvleaf_t(base) 535.weather_t(base)
------------------------------------------------------------------------------
ifx (IFX) 2024.0.0 20231017
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------

==============================================================================
 FC  528.pot3d_t(base)
------------------------------------------------------------------------------
ifx: command line warning #10157: ignoring option '-W'; argument is of wrong
  type
ifx (IFX) 2024.0.0 20231017
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`mpiicc -cc=icx`

C++ benchmarks:
`mpiicpc -cxx=icpx`

Fortran benchmarks:
`mpiifort -fc=ifx`

Base Portability Flags

505.lbm_t: `-lstdc++`
513.soma_t: `-lstdc++ -DSPEC_NO_VAR_ARRAY_REDUCE`
518.tealeaf_t: `-lstdc++`
519.clvleaf_t: `-lstdc++`
521.miniswp_t: `-lstdc++`
528.pot3d_t: `-lstdc++`
532.sph_exa_t: `-lstdc++`
534.hpgmgfv_t: `-lstdc++`

(Continued on next page)

Base Portability Flags (Continued)

535.weather_t: `-lstdc++`

Base Optimization Flags

C benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -ansi-alias`

C++ benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512 -ansi-alias`

Fortran benchmarks:
`-Ofast -ipo -xCORE-AVX512 -mprefer-vector-width=512`
`-nostandard-realloc-lhs -align array64byte`

Base Other Flags

Fortran benchmarks:

528.pot3d_t: `-Wno-incompatible-function-pointer-types`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-06-05.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2023-06-05.xml`

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEChpc2021 v1.1.8 on 2023-11-30 06:39:49-0500.
Report generated on 2024-01-03 17:49:33 by hpc2021 PDF formatter v1.0.3.
Originally published on 2024-01-03.