SPEC® OMPG2012 Result

Cray

(Test Sponsor: Indiana University)

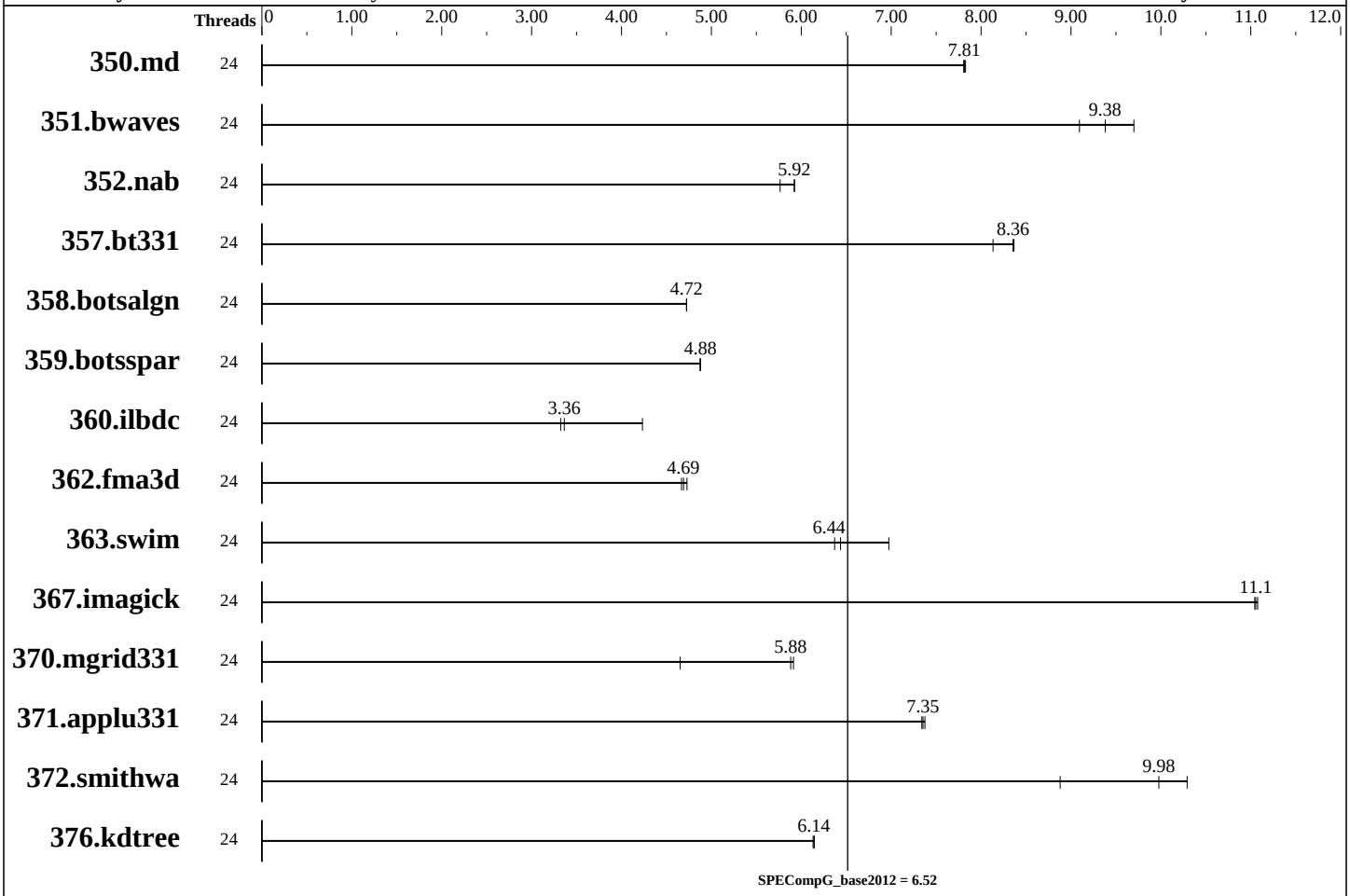
Cray XC30 (Intel Xeon E5-2697 v2)

SPECompG_peak2012 = Not Run

SPECompG_base2012 = 6.52

OMP2012 license:3440A
Test sponsor: Indiana University
Tested by: Indiana University

Test date: Jun-2017
Hardware Availability: Apr-2013
Software Availability: Mar-2017



Hardware

CPU Name:	Intel Xeon E5-2697 v2
CPU Characteristics:	Intel Turbo Boost Technology off, Hyper-Threading on
CPU MHz:	2700
CPU MHz Maximum:	2700
FPU:	Integrated
CPU(s) enabled:	24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	30 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (8 x 8 GB 2Rx4 PC3-14900R-13, ECC)
Disk Subsystem:	None
Other Hardware:	None
Base Threads Run:	24

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP3 (x86_64), Cray Linux Environment 5.2 3.0.101-0.46.1_1.0502.8871-cray_ari_c
Compiler:	C/C++/Fortran: Version 17.0.2.174 of Intel Parallel Studio XE for Linux Build 20170213
Auto Parallel:	No
File System:	Lustre 2.5 (DDN SFA12K) over QDR InfiniBand
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
350.md	24	593	7.81	**592**	**7.81**	592	7.83							
351.bwaves	24	498	9.09	**483**	**9.38**	467	9.70							
352.nab	24	657	5.93	**657**	**5.92**	675	5.76							
357.bt331	24	583	8.13	**567**	**8.36**	567	8.37							
358.botsalgn	24	**921**	**4.72**	921	4.72	921	4.72							
359.botsspar	24	**1076**	**4.88**	1076	4.88	1077	4.88							
360.ilbdc	24	1071	3.32	841	4.23	**1058**	**3.36**							
362.fma3d	24	804	4.73	**810**	**4.69**	814	4.67							
363.swim	24	**704**	**6.44**	711	6.37	650	6.97							
367.imagick	24	635	11.1	**636**	**11.1**	637	11.0							
370.mgrid331	24	**751**	**5.88**	950	4.65	747	5.91							
371.applu331	24	**824**	**7.35**	826	7.34	822	7.38							
372.smithwa	24	604	8.88	521	10.3	**537**	**9.98**							
376.kdtree	24	732	6.15	734	6.13	**733**	**6.14**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Sysinfo program
/N/dc2/projects/hpc/lijunj/spec/omp2012-1.1-run/bigred2plus/Docs/sysinfo
Revision 563 of 2016-06-10 (097295389cf6073d8c3b03fa376740a5)
running on nid00536 Sat Jun 17 22:43:02 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/omp2012/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2697 v2 @ 2.70GHz
      2 "physical id"s (chips)
      48 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 12
      siblings  : 24
      physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 13
      physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 13
   cache size : 30720 KB
```

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Platform Notes (Continued)

```
From /proc/meminfo
   MemTotal:       66072376 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux nid00536 3.0.101-0.46.1_1.0502.8871-cray_ari_c #1 SMP Sat Oct 22
   15:27:12 UTC 2016 x86_64 x86_64 x86_64 GNU/Linux


SPEC is set to: /N/dc2/projects/hpc/lijunj/spec/omp2012-1.1-run/bigred2plus
   Filesystem            Type    Size  Used Avail Use% Mounted on
   10.10.0.171@o2ib:/dc2 lustre  5.3P  5.0P  197T  97% /N/dc2

Cannot run dmidecode; consider saying 'chmod +s /usr/sbin/dmidecode'

(End of data from sysinfo program)
```

General Notes

```
Environment Variables:
    KMP_STACKSIZE=1G
    ulimit -s unlimited
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Base Portability Flags

350.md: `-free`
357.bt331: `-mcmodel=medium`
363.swim: `-mcmodel=medium`
367.imagick: `-std=c99`

Base Optimization Flags

C benchmarks:
`-ansi-alias -qopenmp -ipo -O3 -no-prec-div -no-prec-sqrt -fp-model fast=2 -xHost`

C++ benchmarks:
`-ansi-alias -qopenmp -ipo -O3 -no-prec-div -no-prec-sqrt -fp-model fast=2 -xHost`

Fortran benchmarks:
`-qopenmp -ipo -O3 -no-prec-div -no-prec-sqrt -fp-model fast=2 -xHost`

The flags file that was used to format this result can be browsed at
http://www.spec.org/omp2012/flags/Intel-ic17-linux64.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/omp2012/flags/Intel-ic17-linux64.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC OMP2012 v1.1.
Report generated on Wed Aug 16 15:43:34 2017 by SPEC OMP2012 PS/PDF formatter v541.
Originally published on 16 August 2017.